

Factory Considerations for High Volume Manufacturing using 300 mm wafers

Paolo Gargini

Dev Pillai

Technology and Manufacturing Group

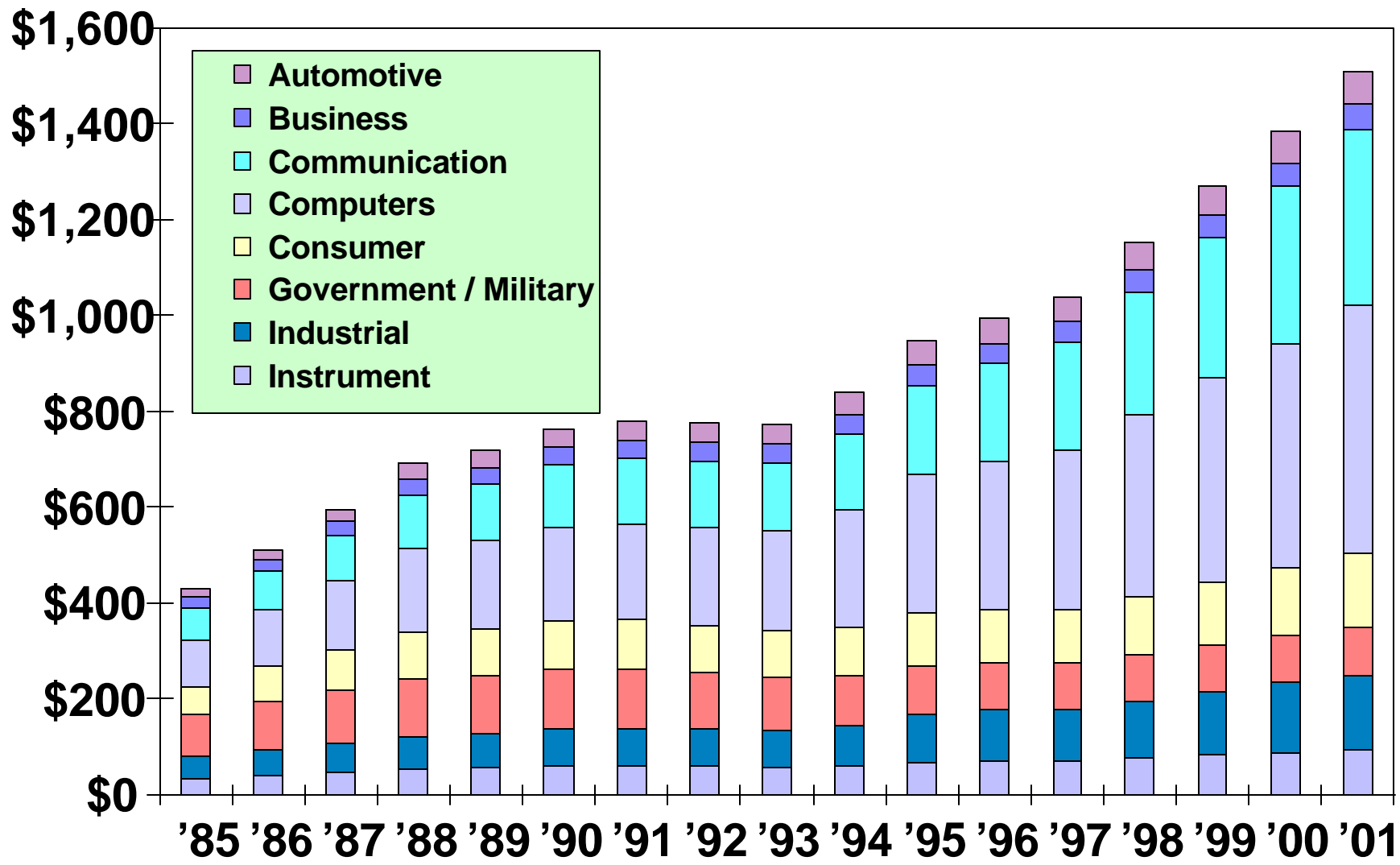
Intel Corporation

Agenda

- Industry outlook
- 200mm wafer experience
- 300mm wafer conversion
- Equipment cost/productivity goals
- Material handling standardization
- Inter-operability
- Bay layout options
- Factory layout options
- Global cooperation
- Conclusions

Global Electronics Sales

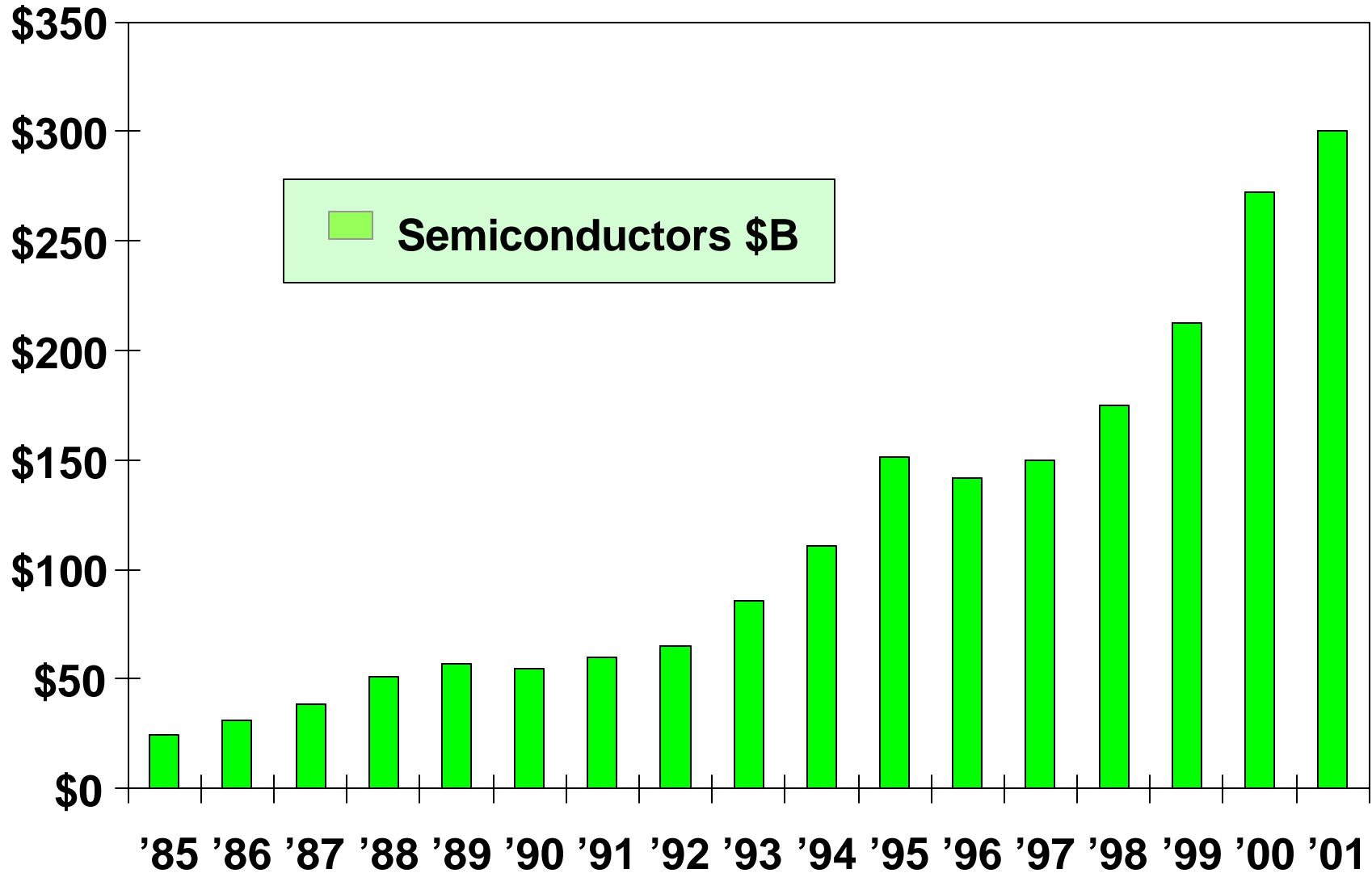
\$B



Source: Electronic Outlook Corp. Q3'97

World Semiconductor Sales

\$B

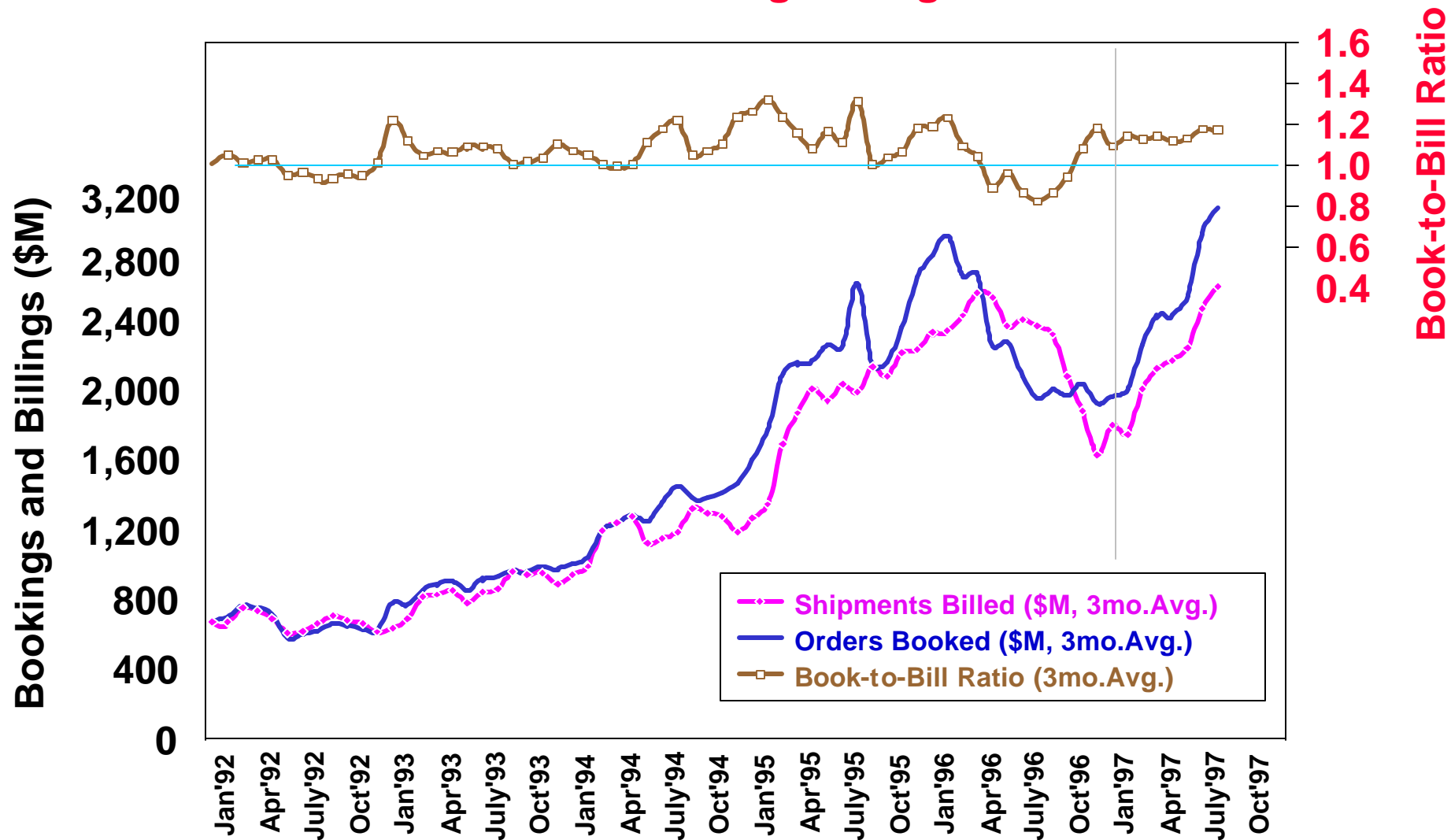


Source: Dataquest 9'97

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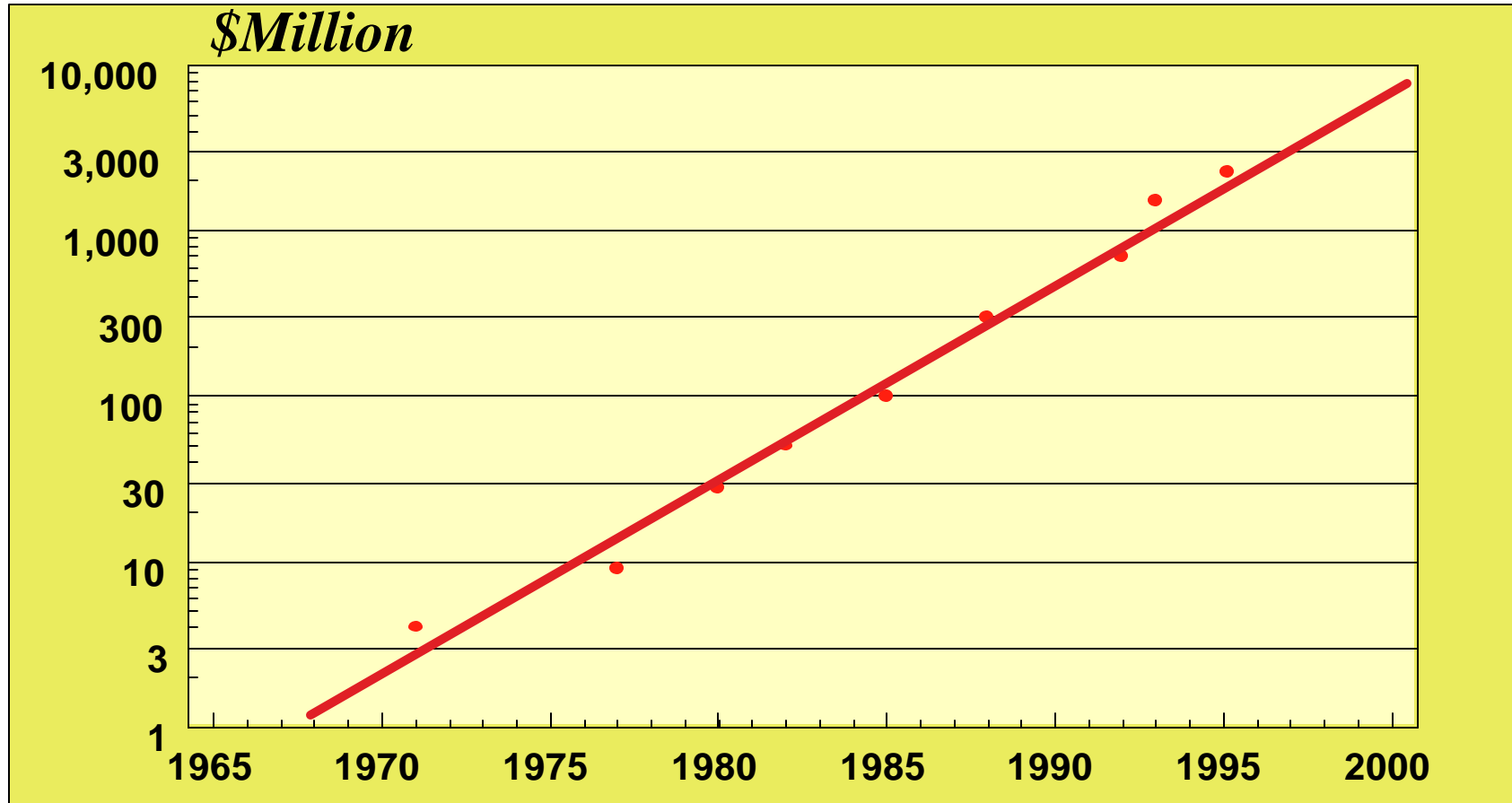
Semi Equipment Sales

3-month rolling average



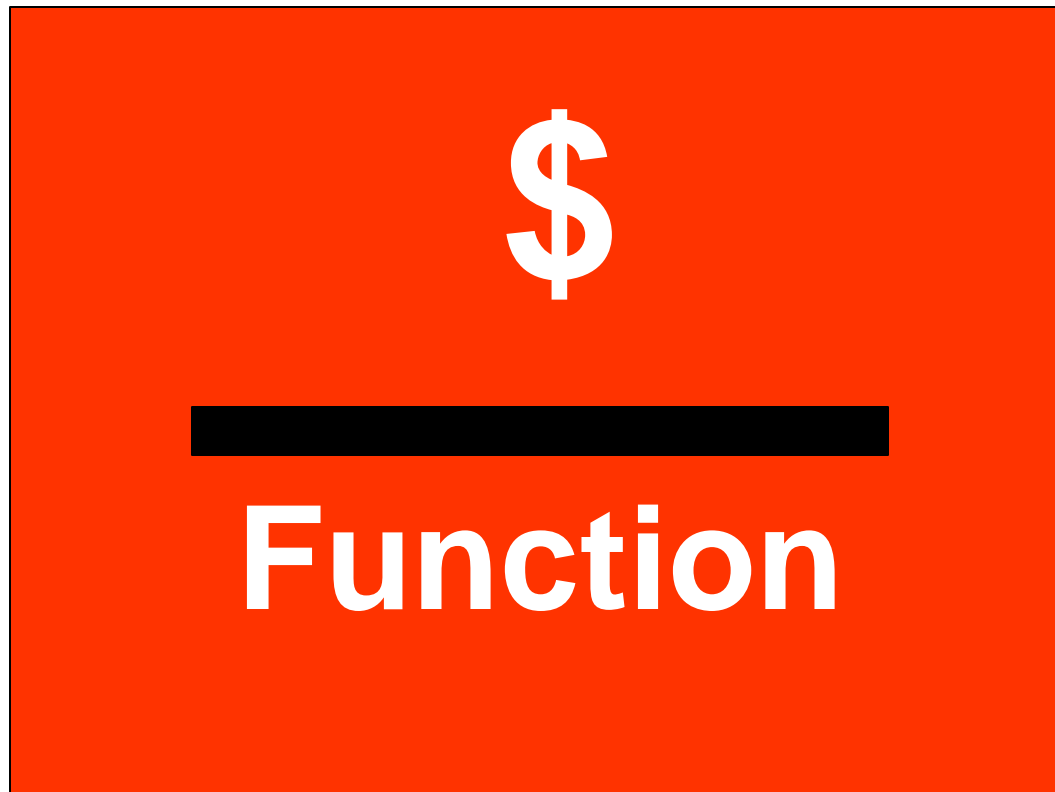
Source: Semiconductor Equipment and Materials International (SEMI) 10'97

Escalating Cost of Semiconductor Wafer Fabrication Plants

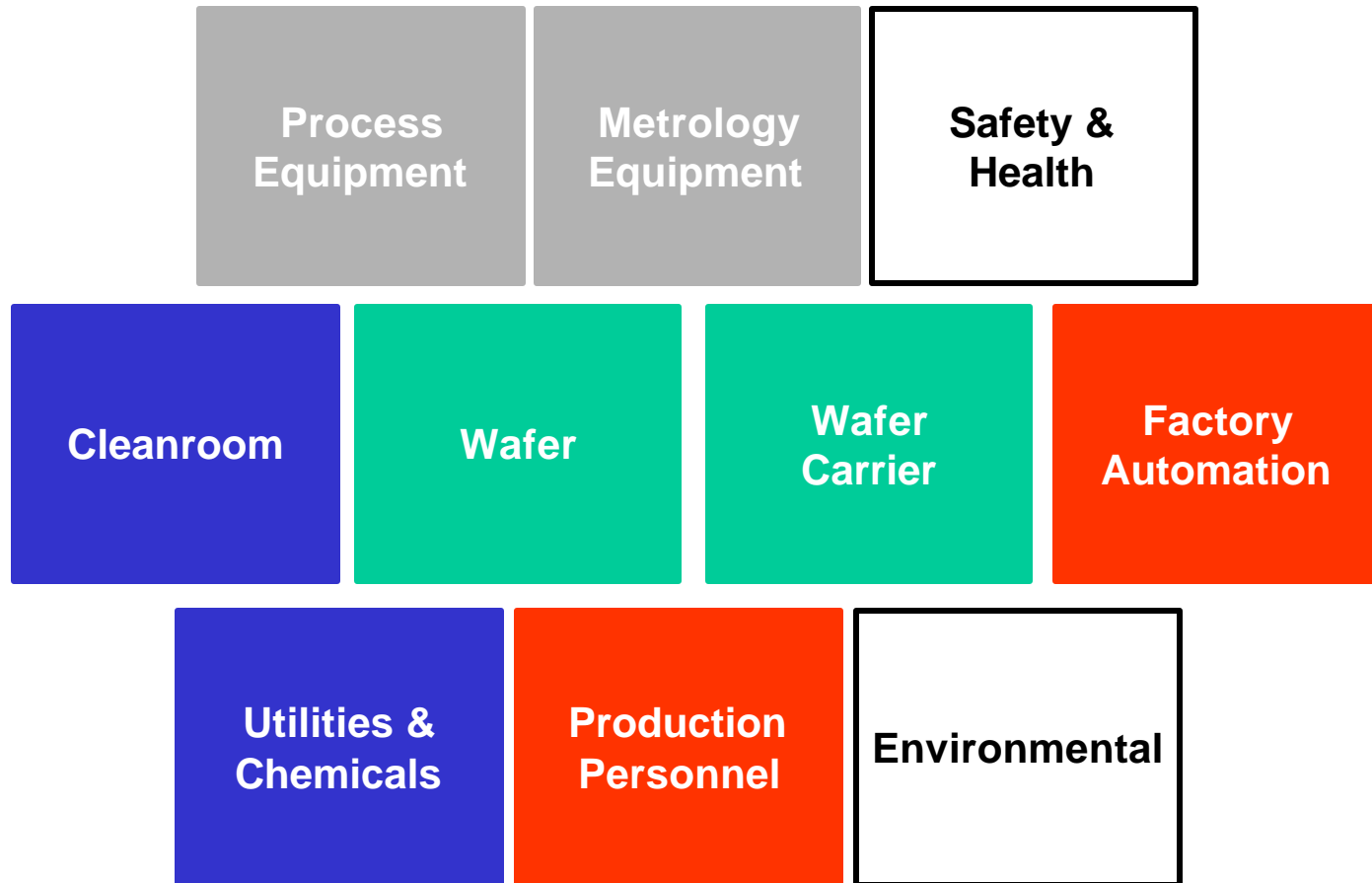


Source: VLSI Research, Inc.

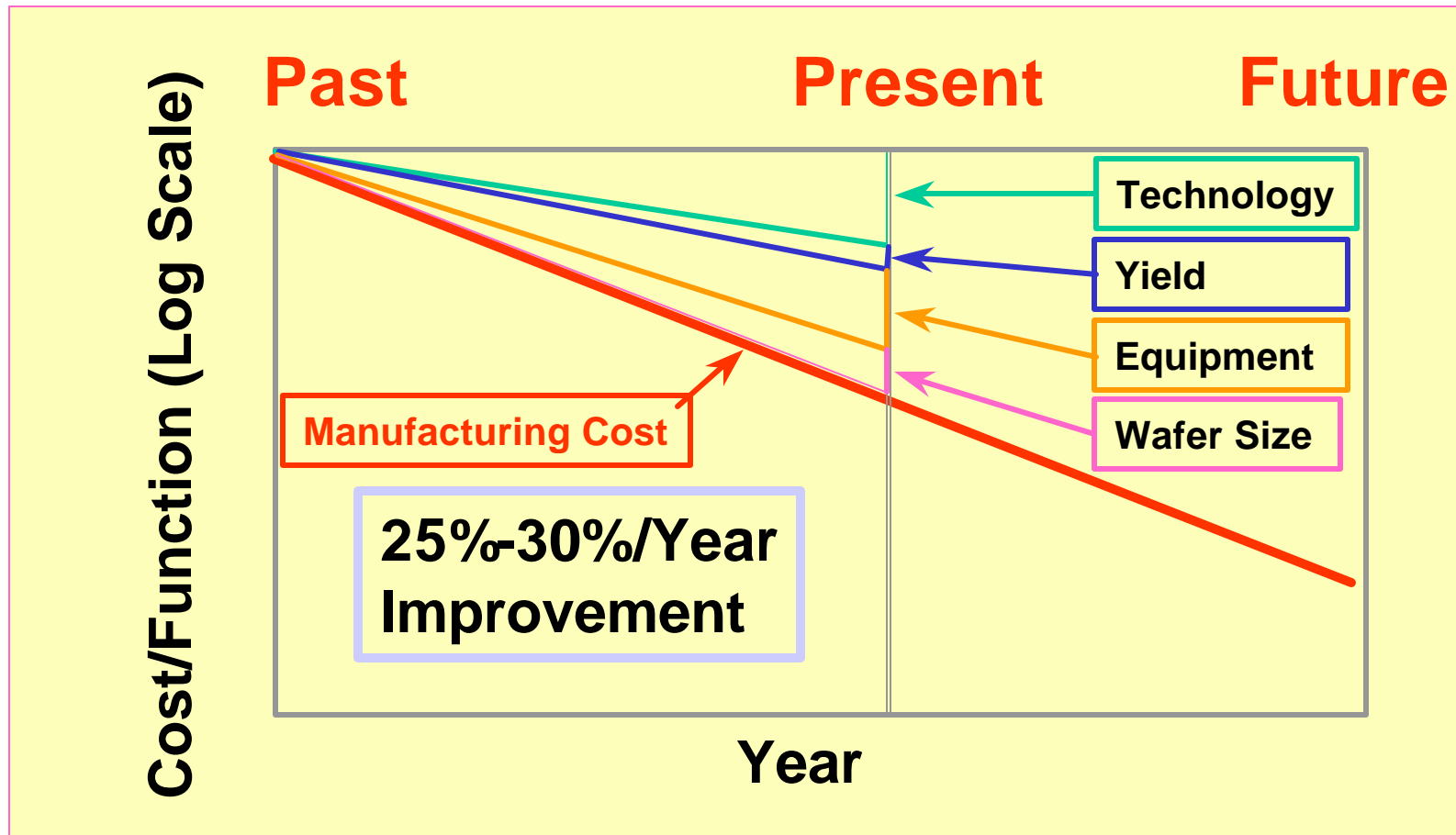
The Key Indicator: Cost/Function



Cost reduction needs to come from all these components while preserving Safety/Health and the Environment



The Cost Reduction Engine



Looking Back at 200mm wafer conversion

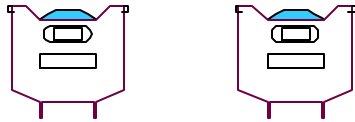
- In the 200mm conversion the semiconductor industry **missed** the opportunity to take advantage of the standardization process
- In the 200mm process and metrology equipment, although the **E15** load port standard **existed**, there was **no** drive by the IC manufacturers to request tool makers to adhere to this standard.

Consequently,

- ☞ Tool makers did not adhere to load port heights nor to cassette orientations
- ☞ Loading and Unloading of tools either by manual systems or automated systems were complicated and costly

There is no Standardization for 200mm Equipment Load port orientation

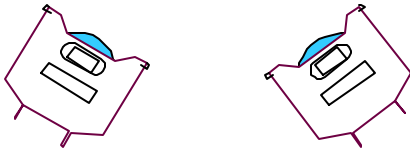
Tool plan view



Tool plan view

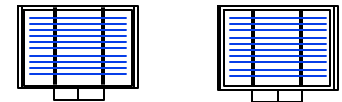


Tool plan view



Each tool is different!

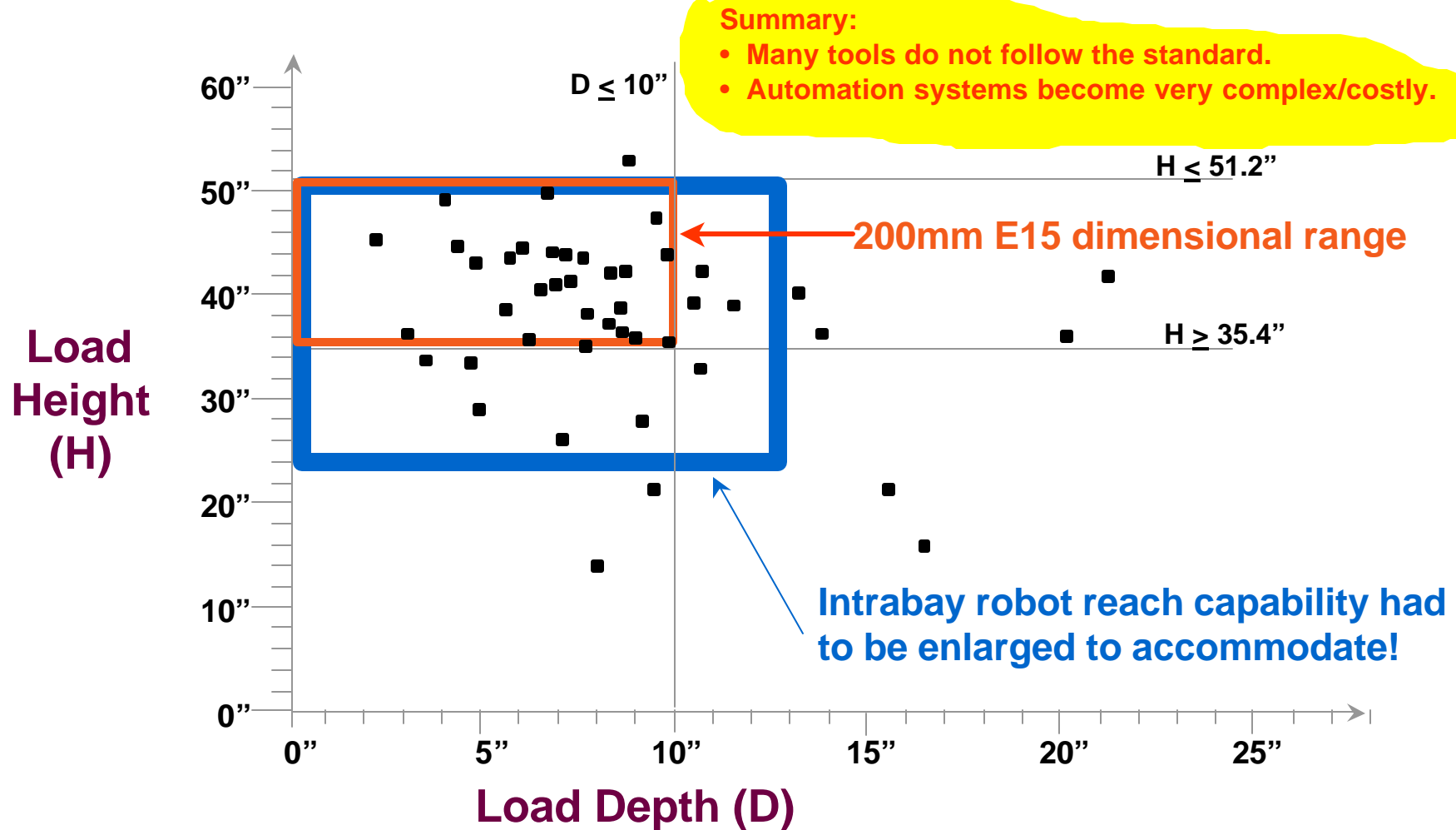
Tool plan view



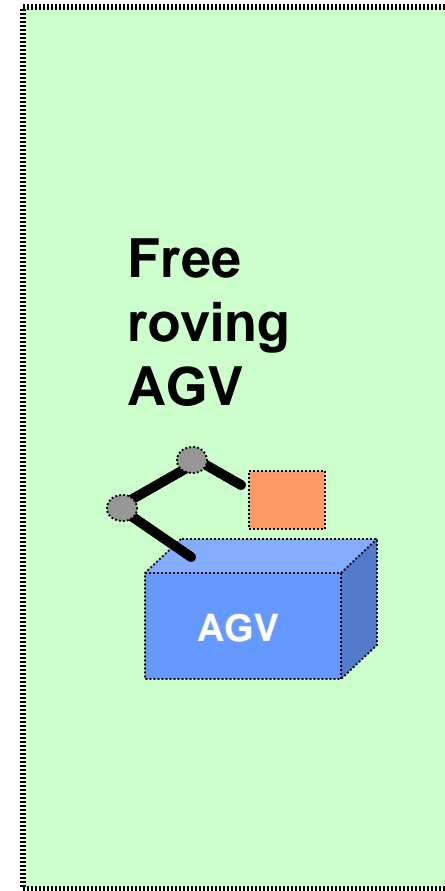
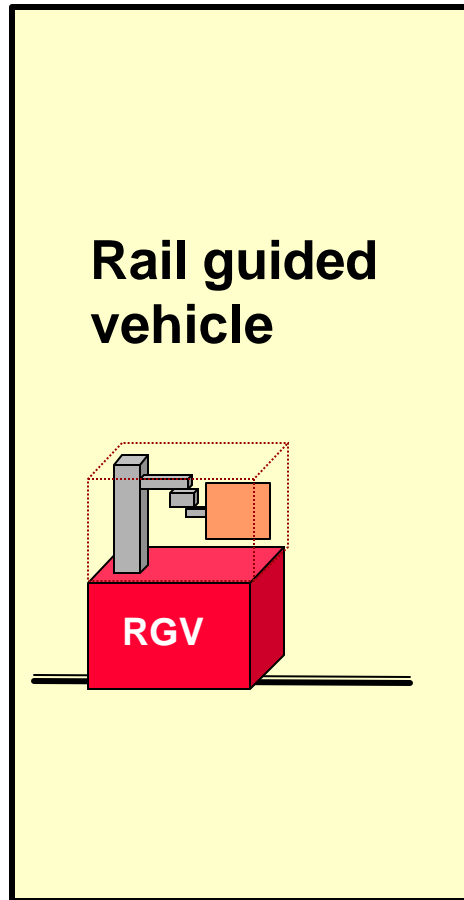
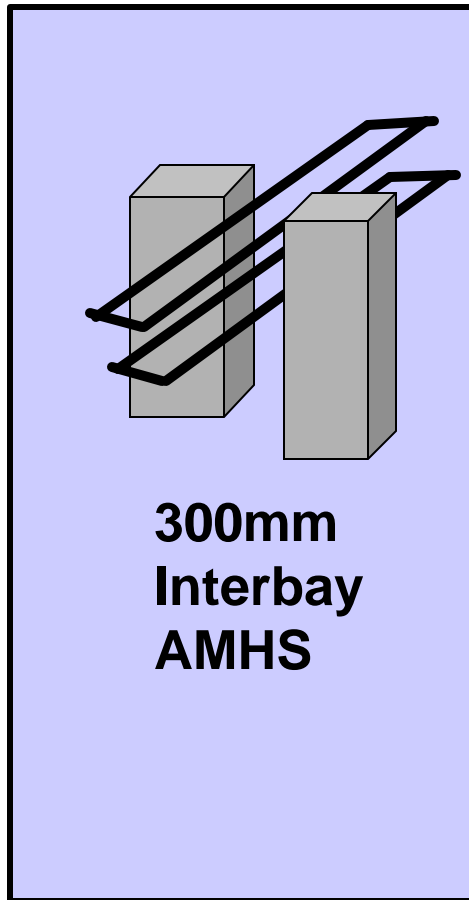
Tool plan view



200mm E15 load port Evaluation: Adherence to H and D dimensions



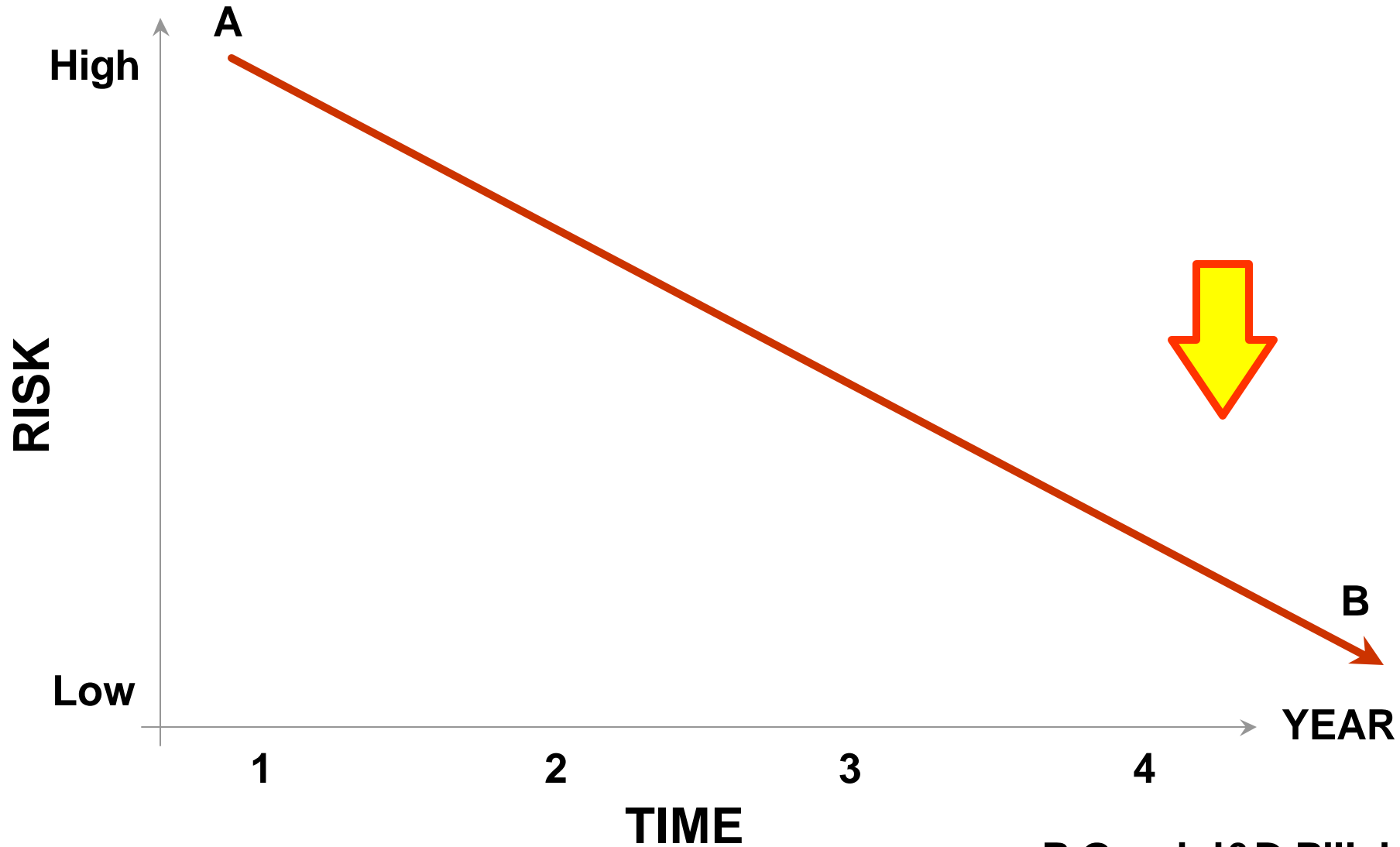
200mm AMHS Building Blocks



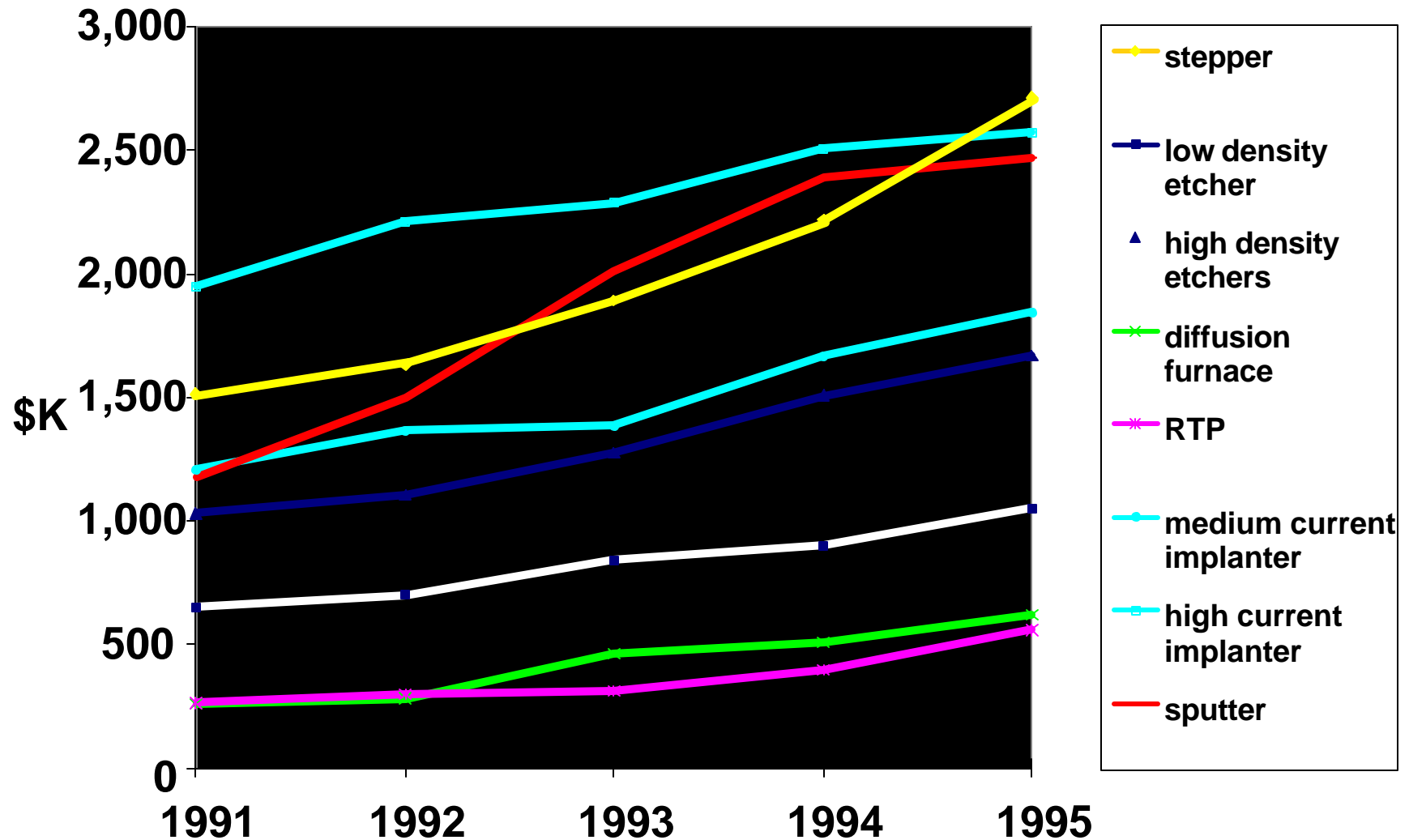
Consequences of lack of standardization

- Each semiconductor company approached automation in a different way
 - Extensive load port customization
 - Unique material transport solutions
- Multiple equipment solutions pursued
 - Increased equipment development time
 - Increased equipment cost

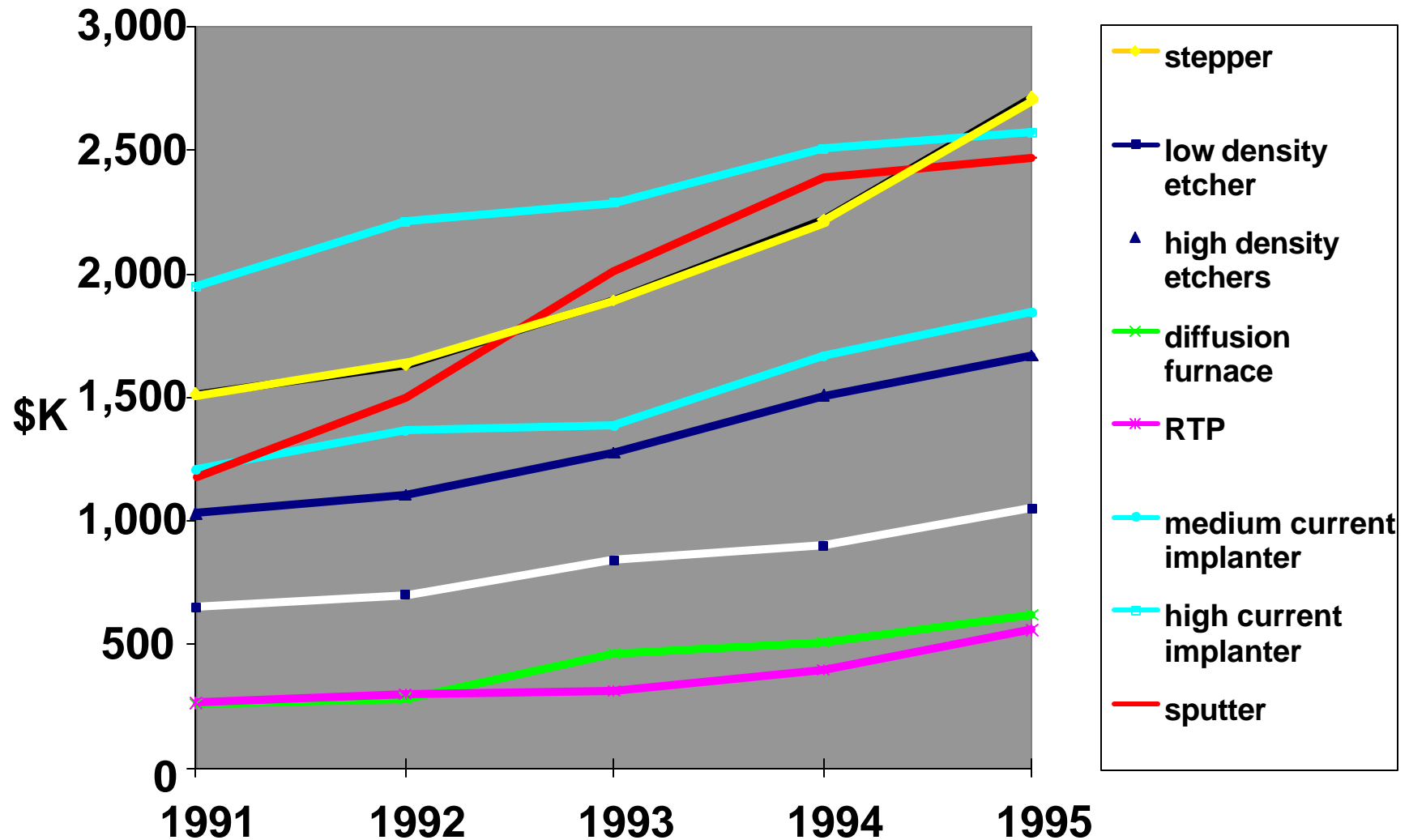
200mm Standardization Process



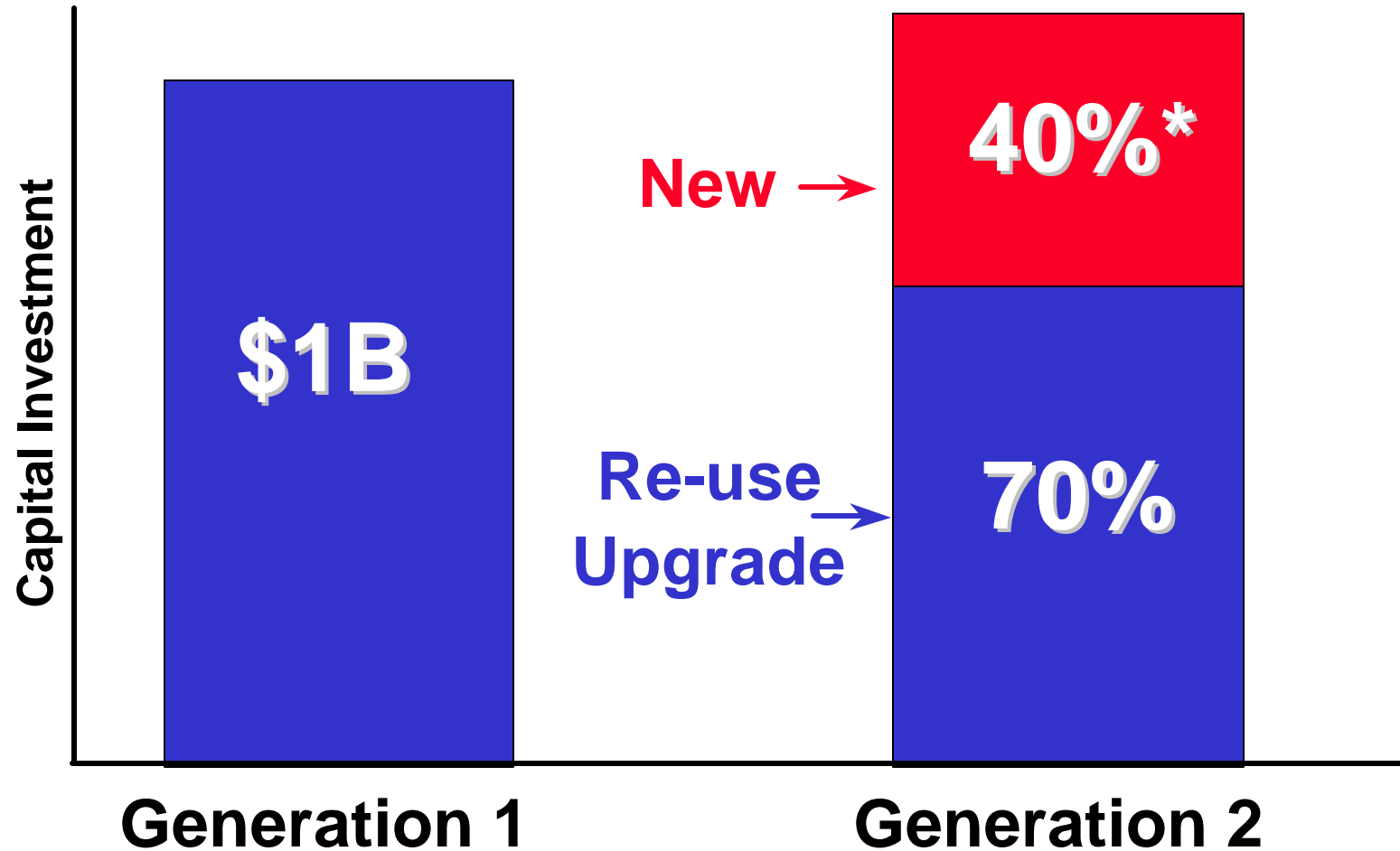
Equipment ASP Trends



Equipment ASP Trends



Capital Equipment Investment for New vs. Re-use of Installed-base



*10% higher complexity

The 300mm wafer transition

Wafer Size History

0.5"



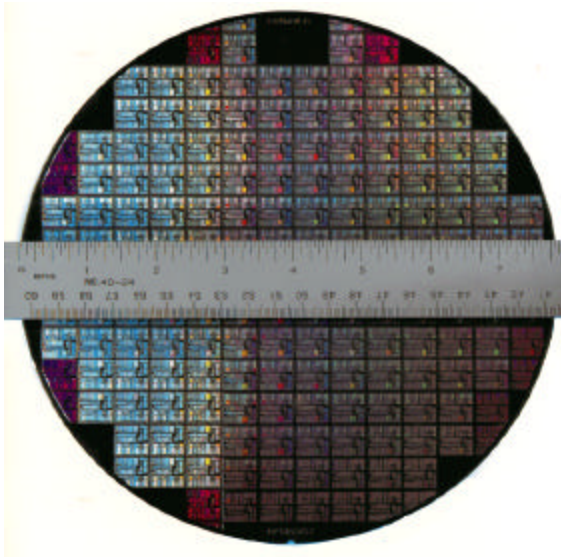
1957

2"



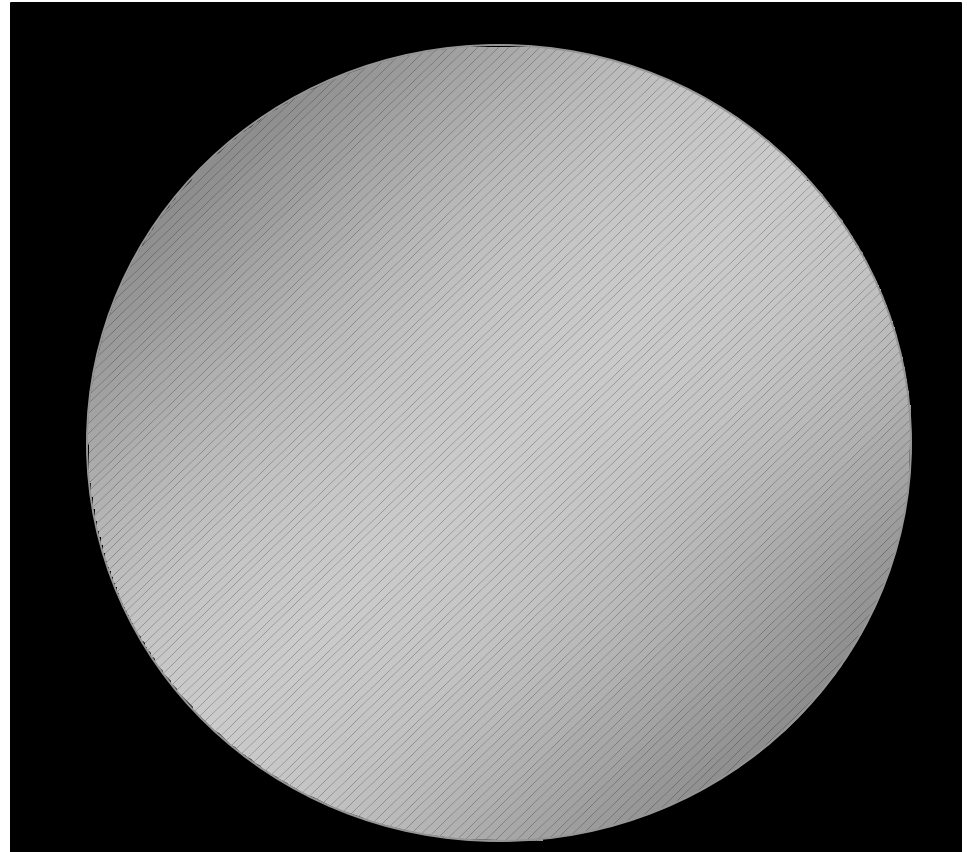
1971

8"



Today

12"

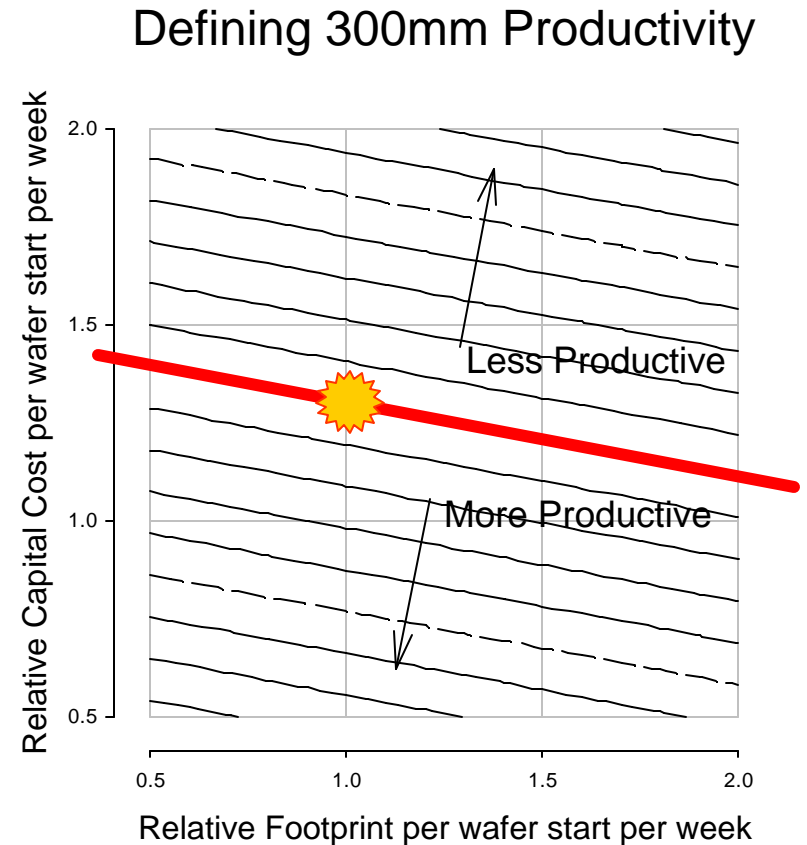


Tomorrow

P.Gargini&D.Pillai

300mm Must Be More Productive than 200mm

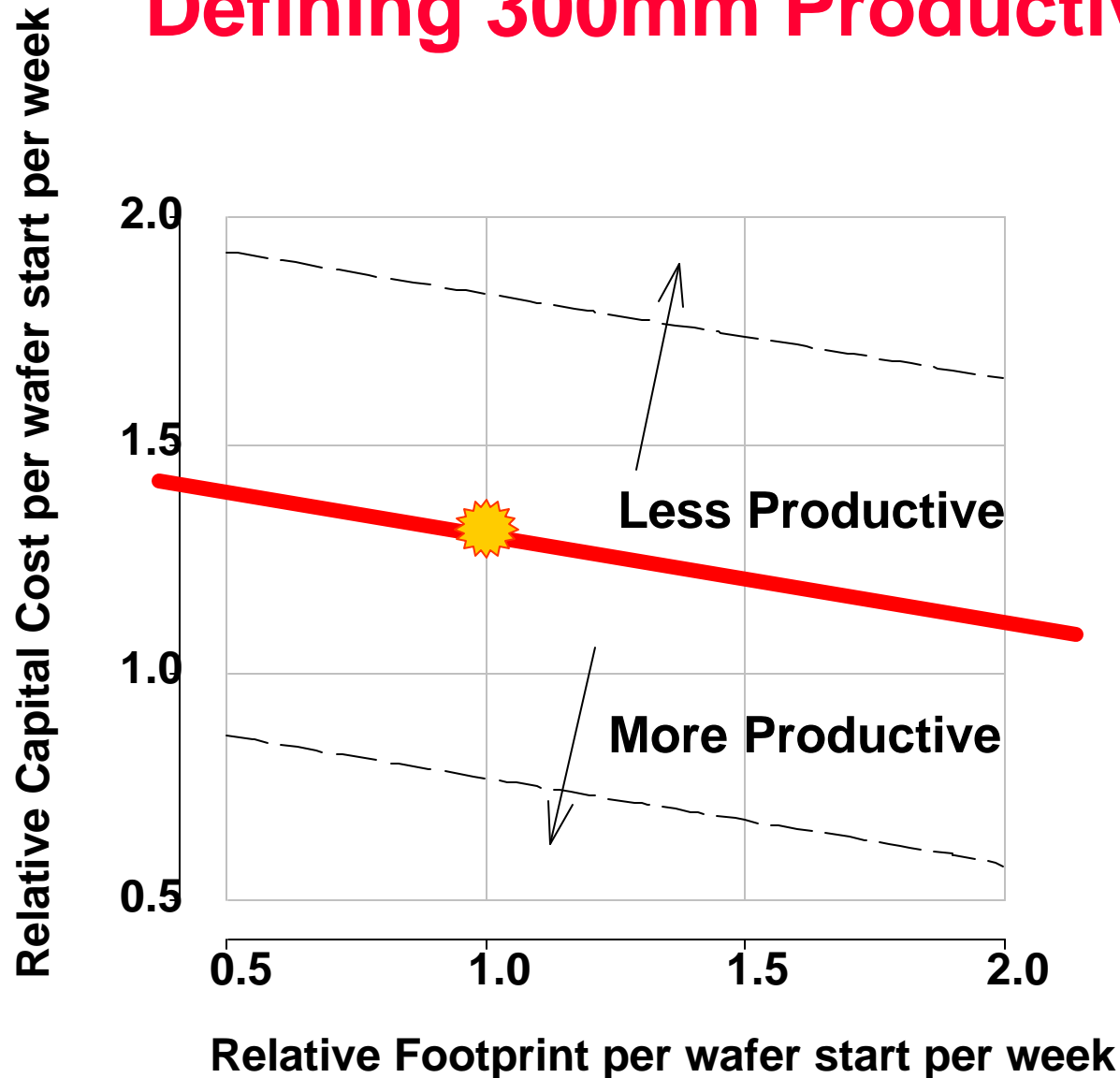
- Productivity can be measured by cost/die, people per wafer, factory size/wafer, ...
- A productivity analysis comparing 300mm to 200mm shows that the two parameters which most influence productivity are:
 - ☞ Relative Tool Cost per wspw
 - ☞ Relative Tool Size per wspw
- For every tool, these two parameters define a point on the graph.
- To meet the productivity targets, each such point must lie below the red line.
- This analysis drives 300mm planning in general, and Equipment Performance Requirements in particular.



D.Seligson

P.Gargini&D.Pillai

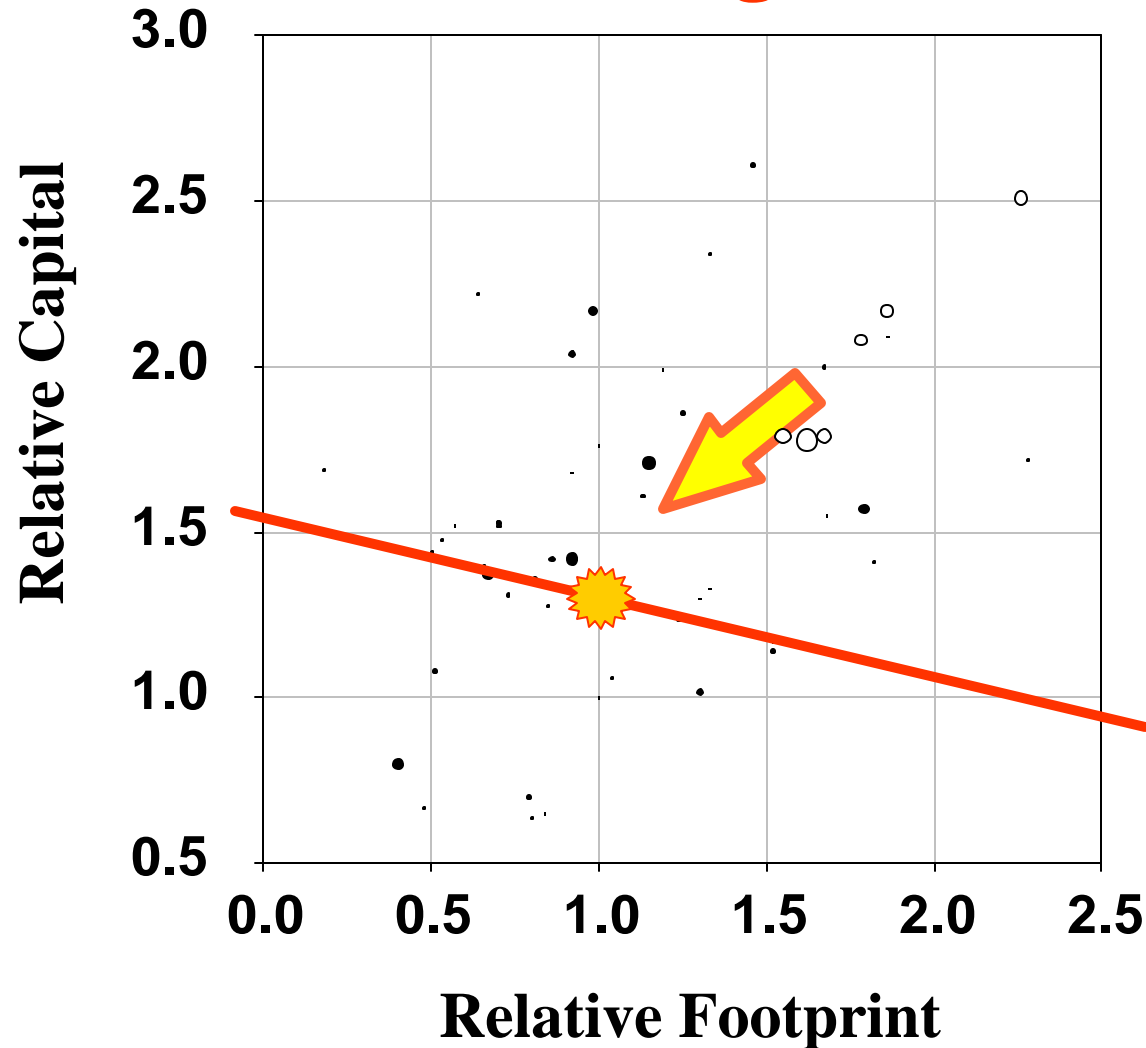
Defining 300mm Productivity



D.Seligson

P.Gargini&D.Pillai

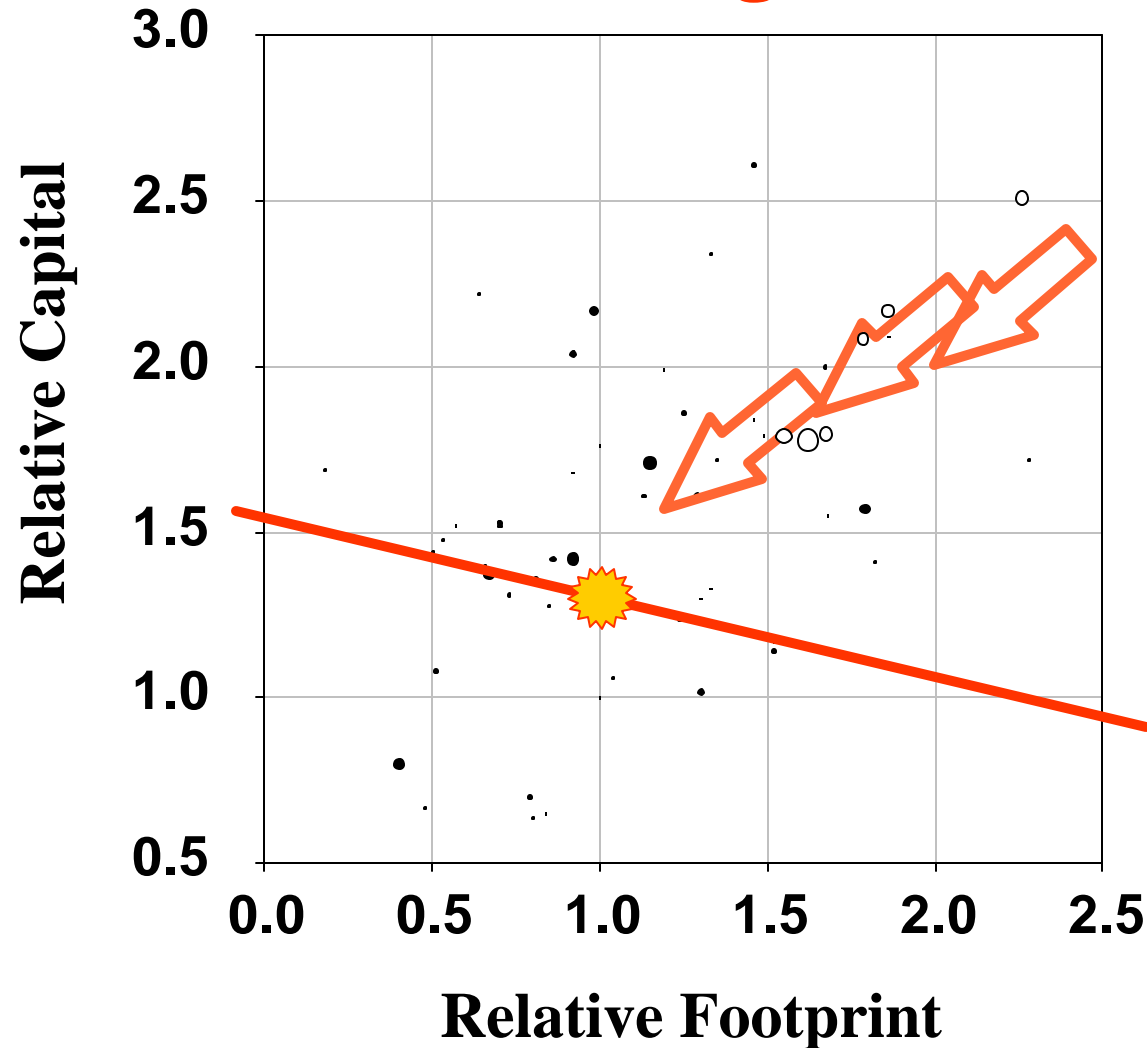
Early Productivity Scaling Factors for Tools in a 180 nm Logic Process Flow



D.Seligson

P.Gargini&D.Pillai

Early Productivity Scaling Factors for Tools in a 180 nm Logic Process Flow

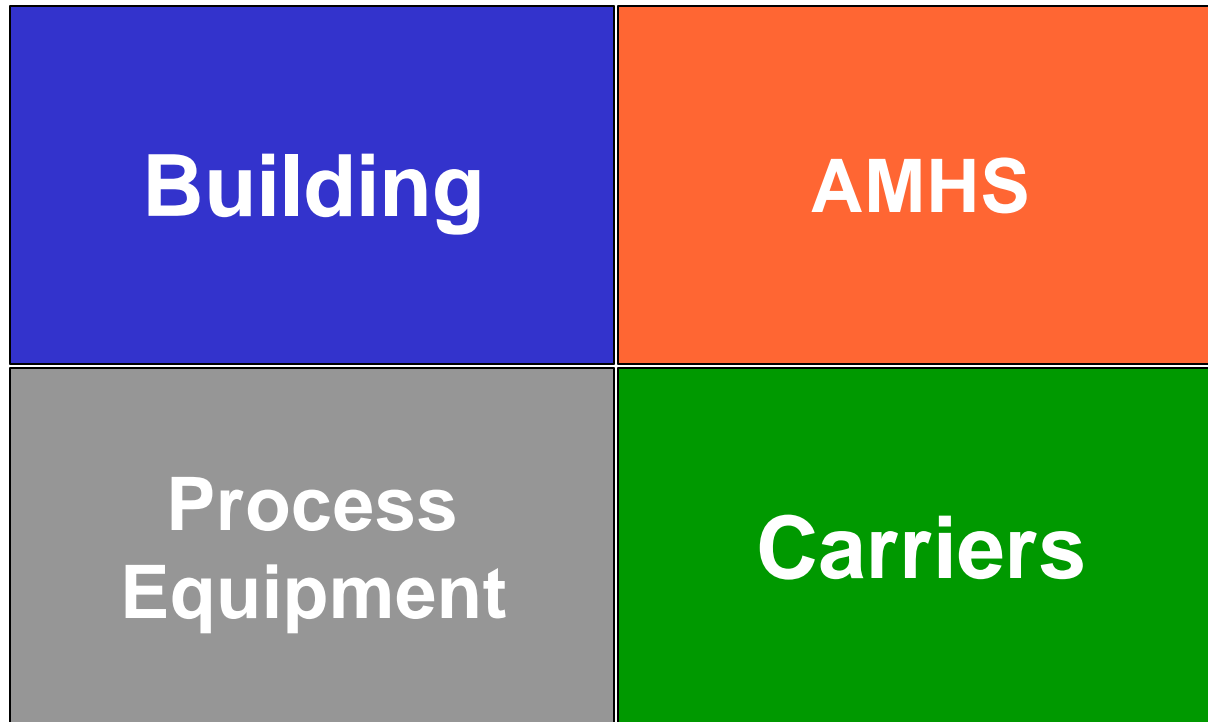


D.Seligson

P.Gargini&D.Pillai

Conversion from 200mm to 300mm

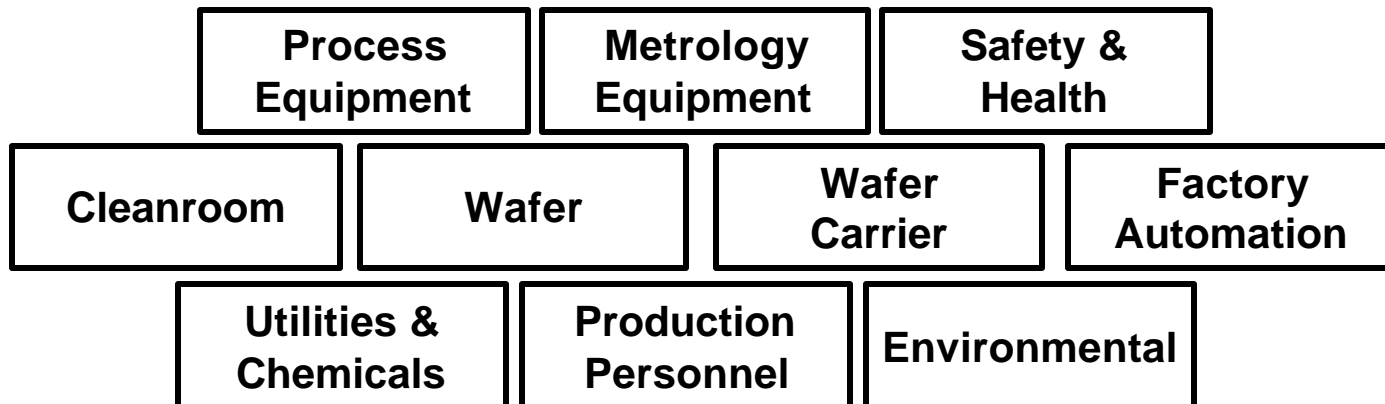
Synchronization= A renewed management challenge



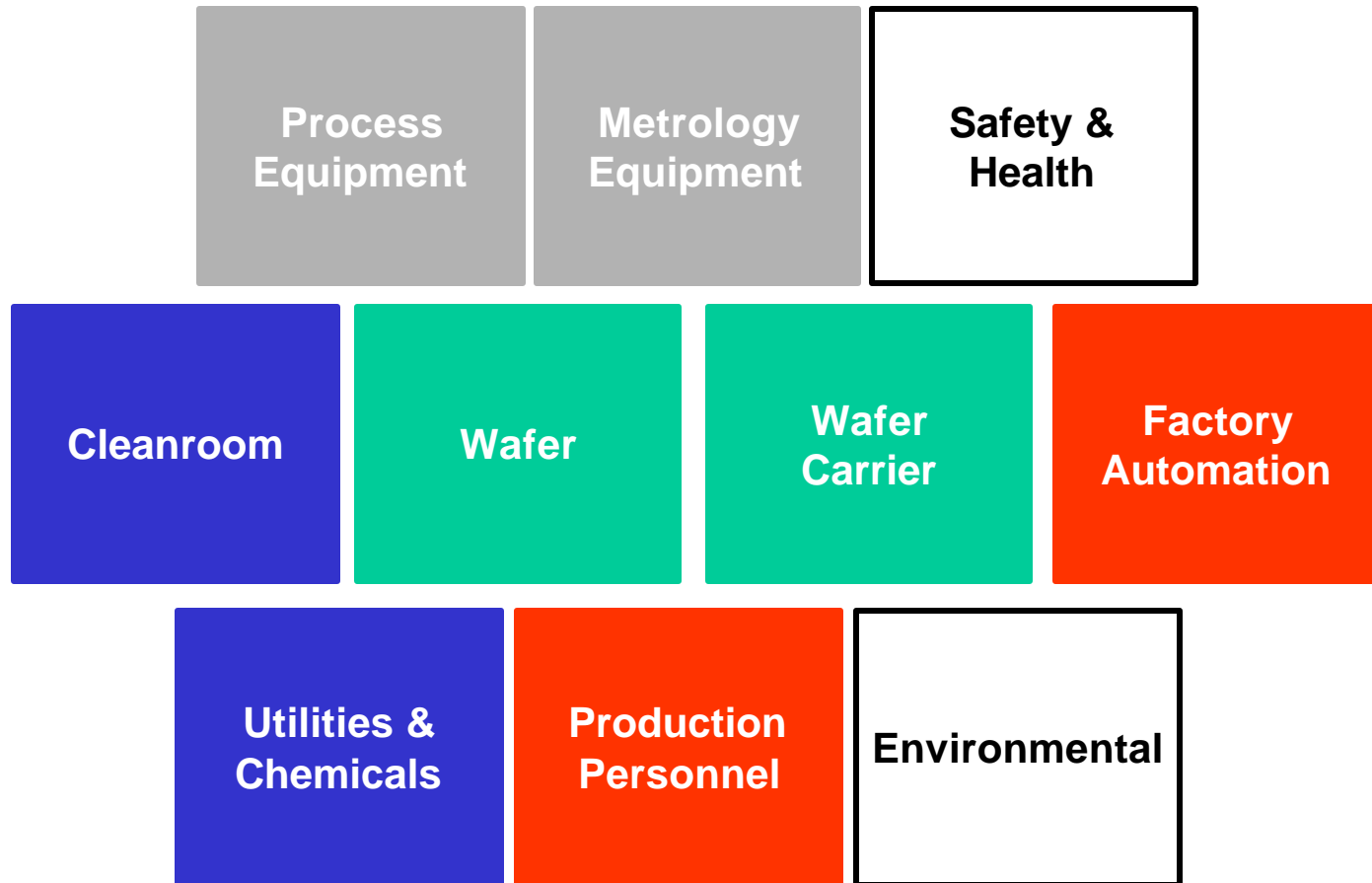
Why standards are important

- Standards provide a mechanism for integrating the different elements of a factory cost-effectively:
 - ☞ Faster, better and cheaper
- Standardization provides opportunities for all:
 - ☞ If there is a standard, and you have a product that meets that standard, then you have an opportunity to compete in that business
- There are significant opportunities for 300 mm:
 - ☞ Many factory elements are not well integrated for 200 mm
 - ☞ Significant cost reduction opportunities exist

Standards integrate important factory elements



Standards integrate important factory elements

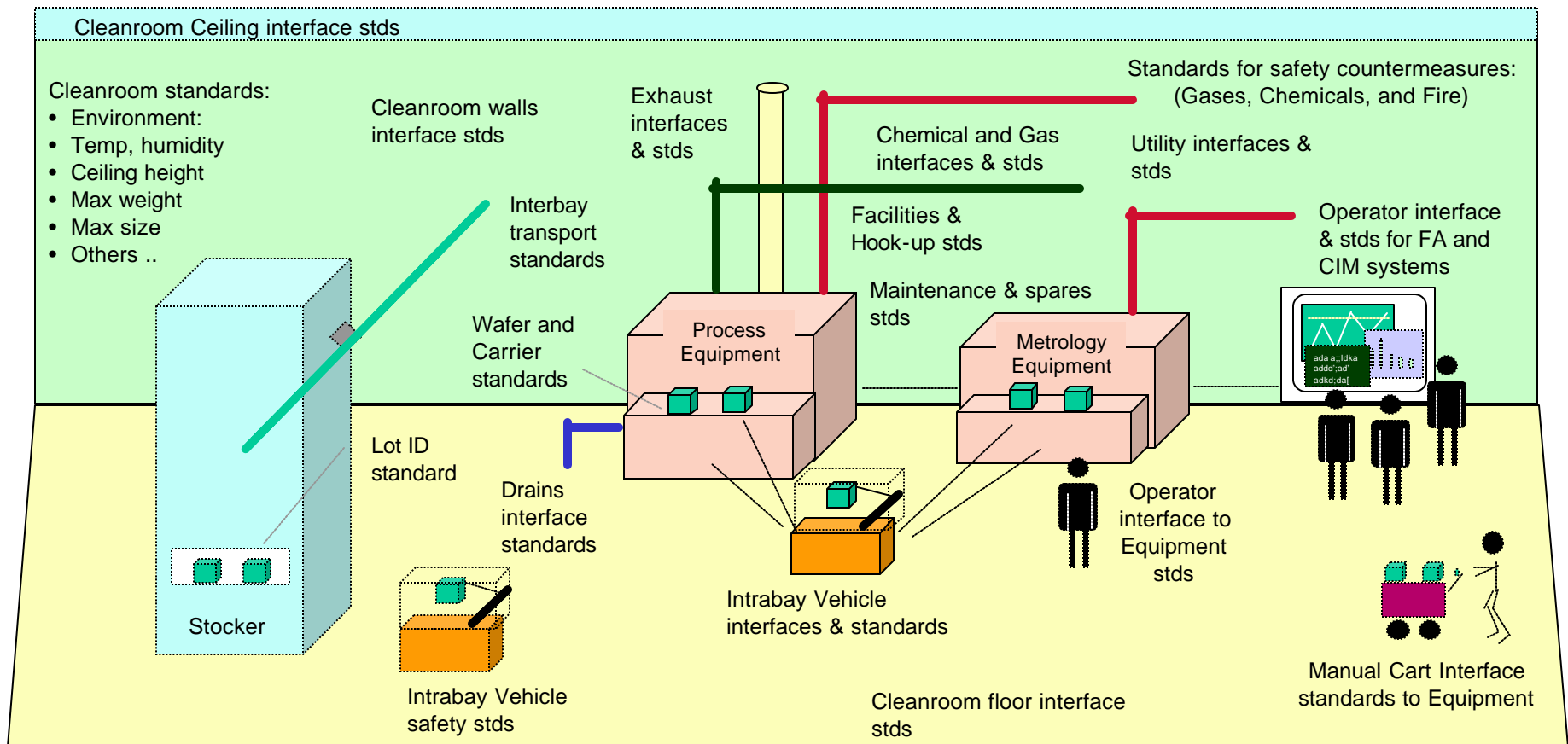


How standards benefit manufacturing

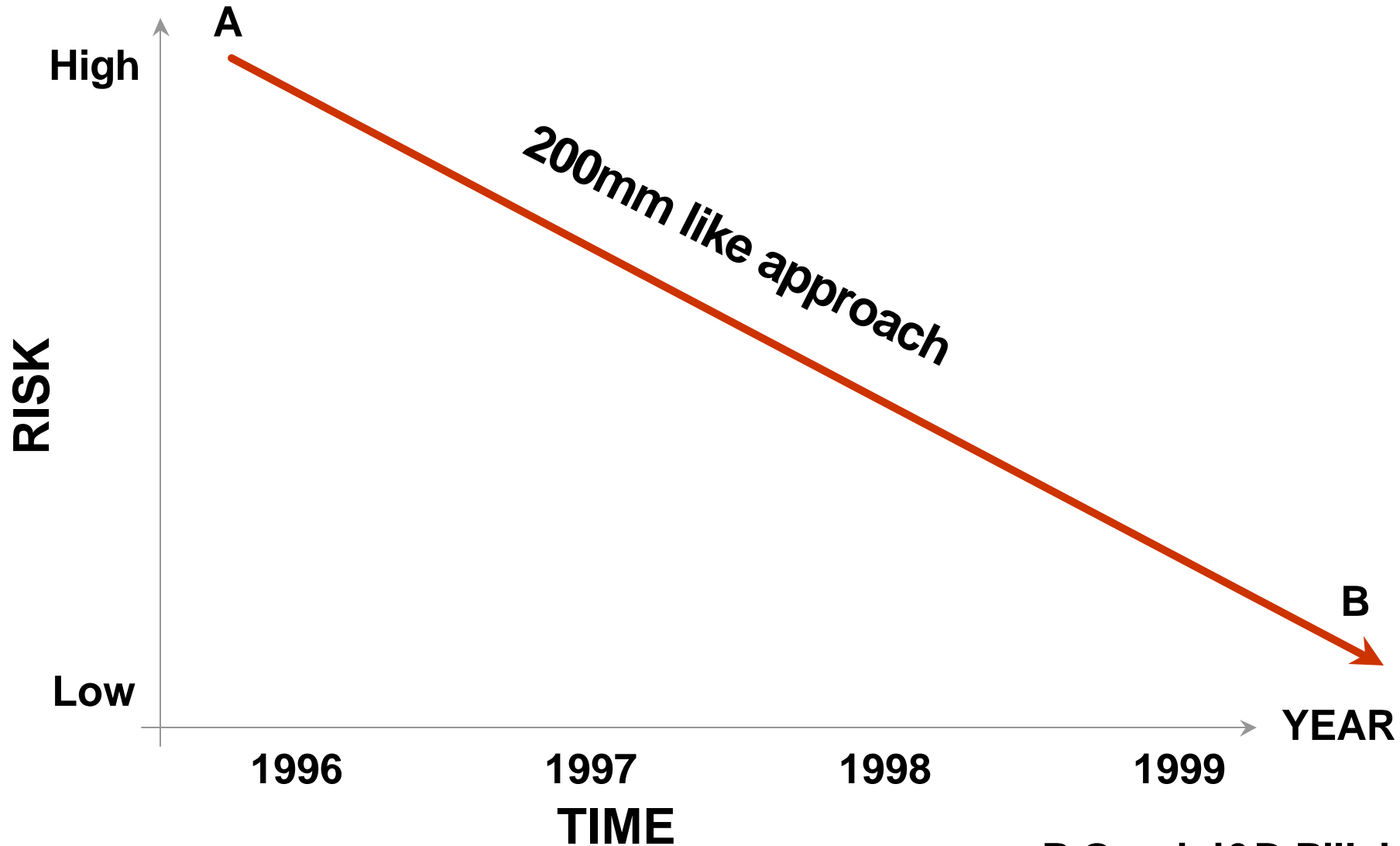
- Factory elements are better integrated and more consistent
- Systems work together cohesively and efficiently



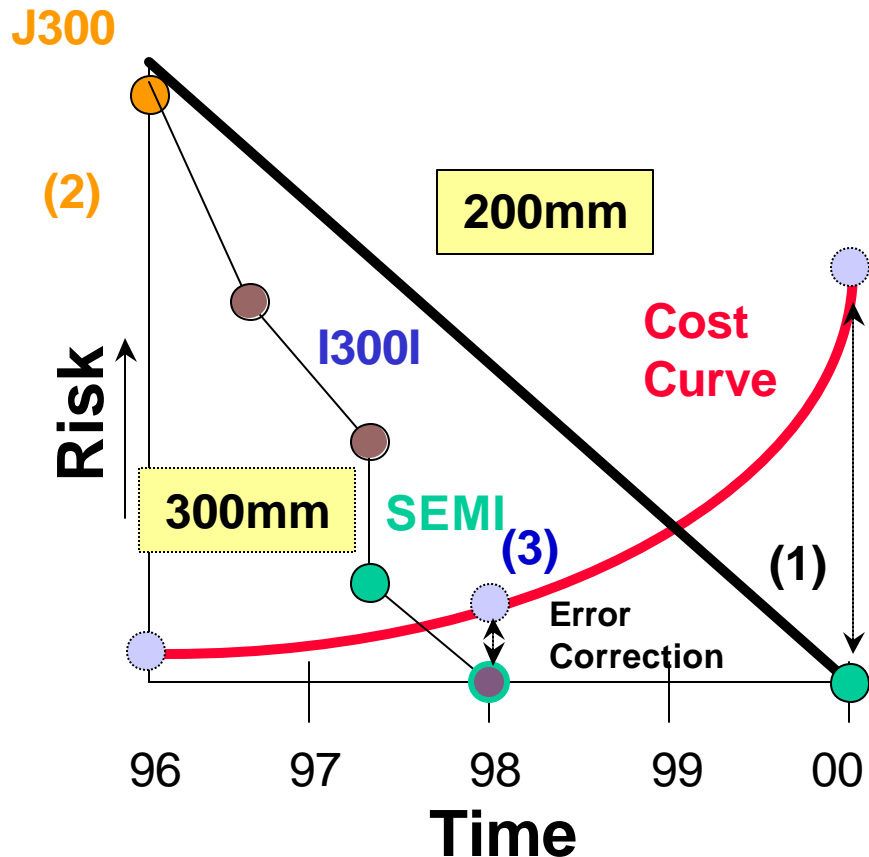
Material supply container stds



Traditional Standardization Process

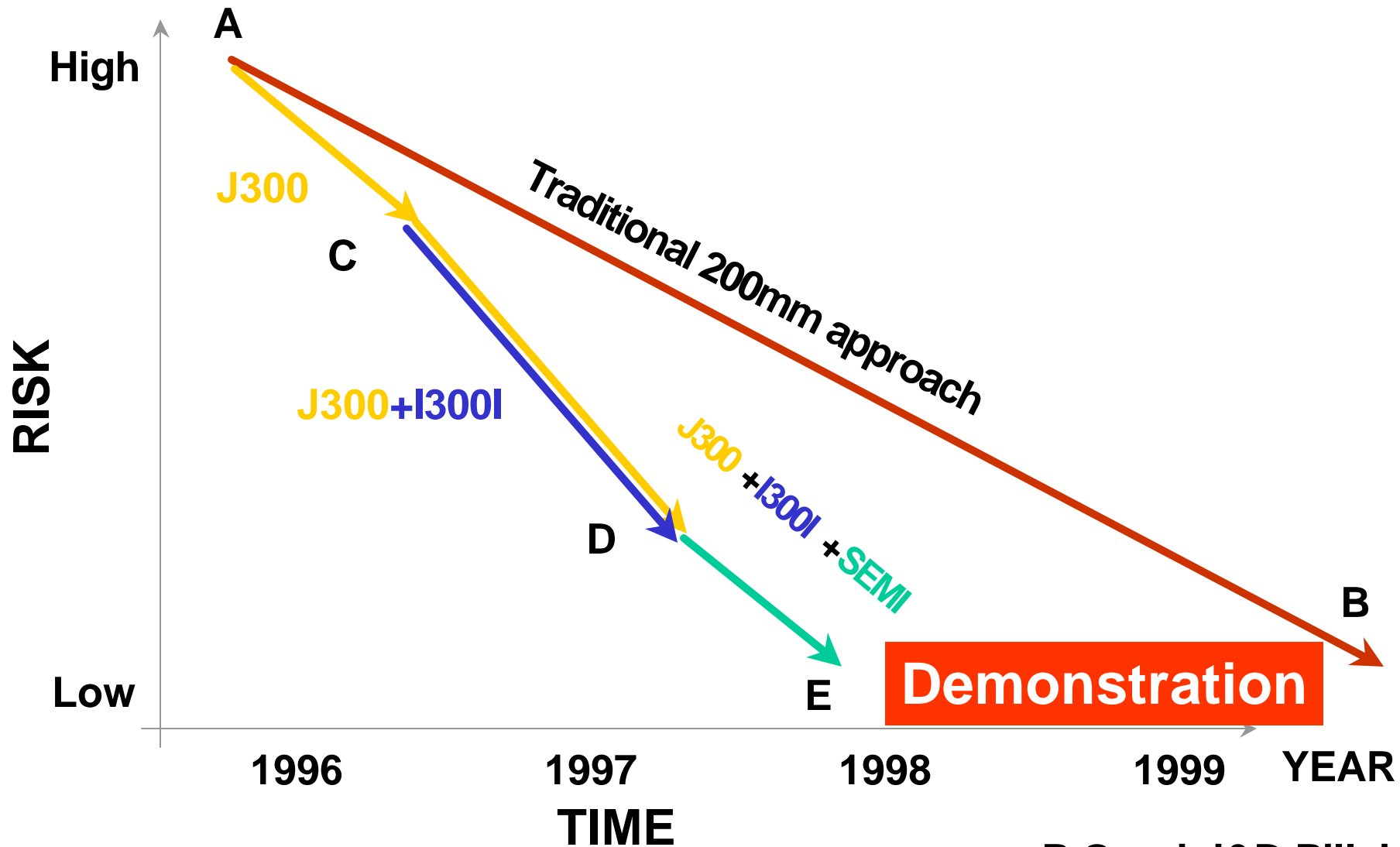


Standards Strategy for Semiconductor Industry

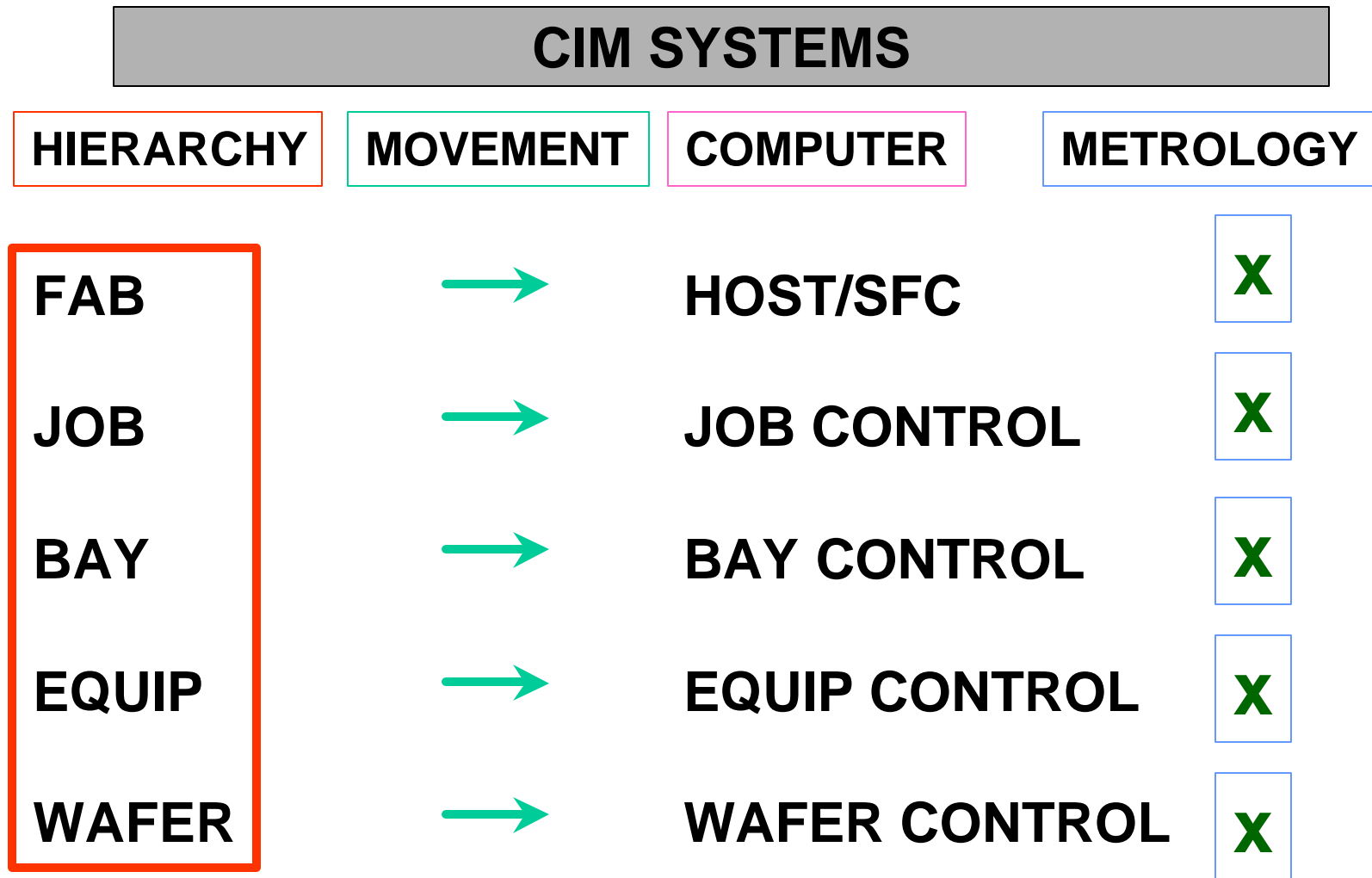


- (1) Implementing Standards after technology development is low risk but high cost
 - (2) Implementing Standards alone before technology development is high risk but low cost
 - (3) By leveraging J300, I300I, and SEMI, we can achieve low risk and lower cost by combining our efforts and intelligence
- Doing standards together before volume manufacturing allows time for correcting issues that are found

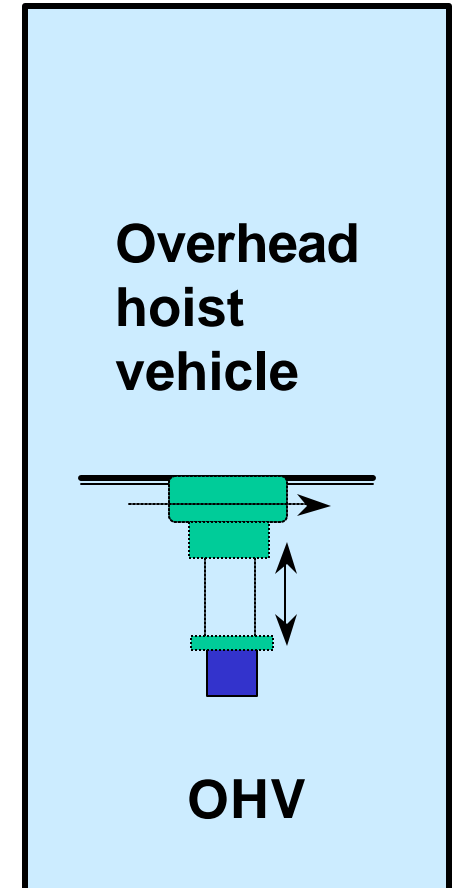
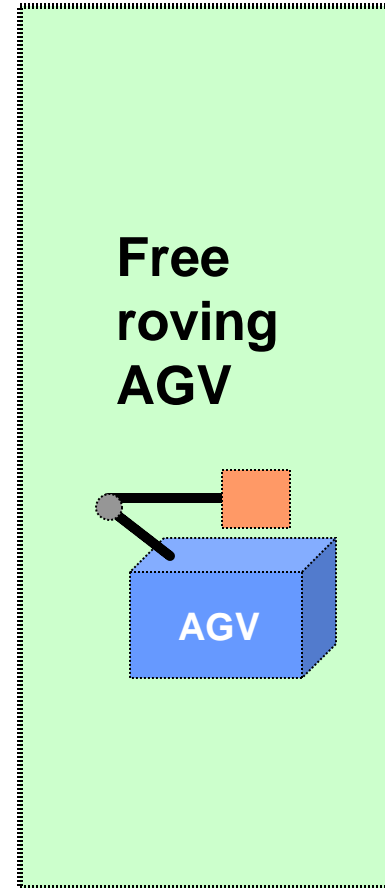
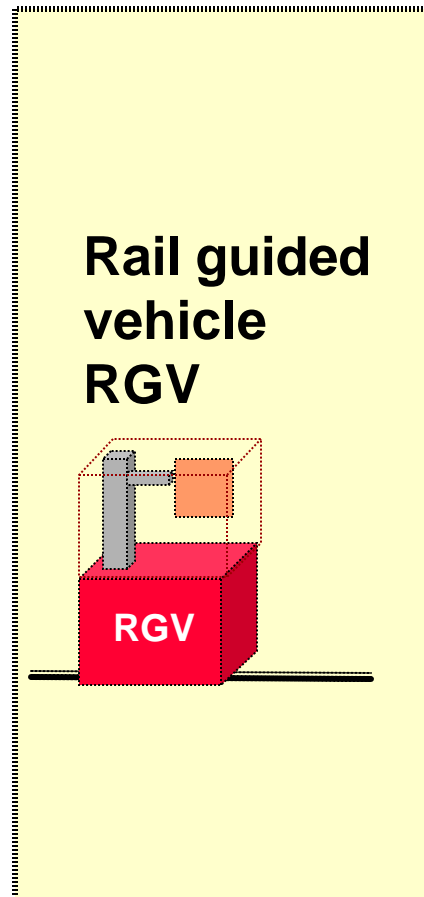
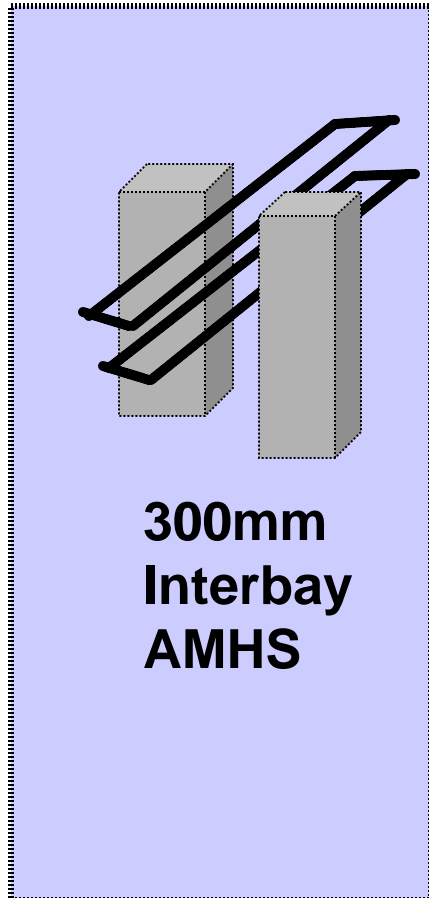
Risk Reduction using Global Participation and Standardization



Factory Architecture

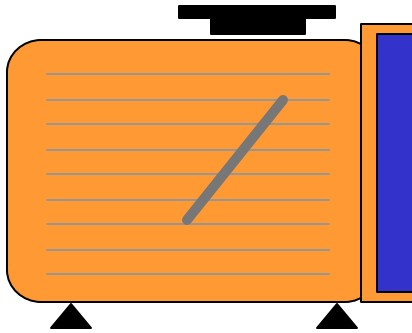


300mm AMHS Building Blocks

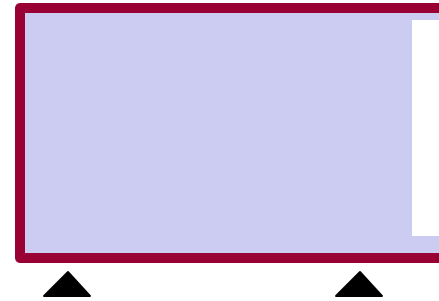


FOUP and Open Cassette

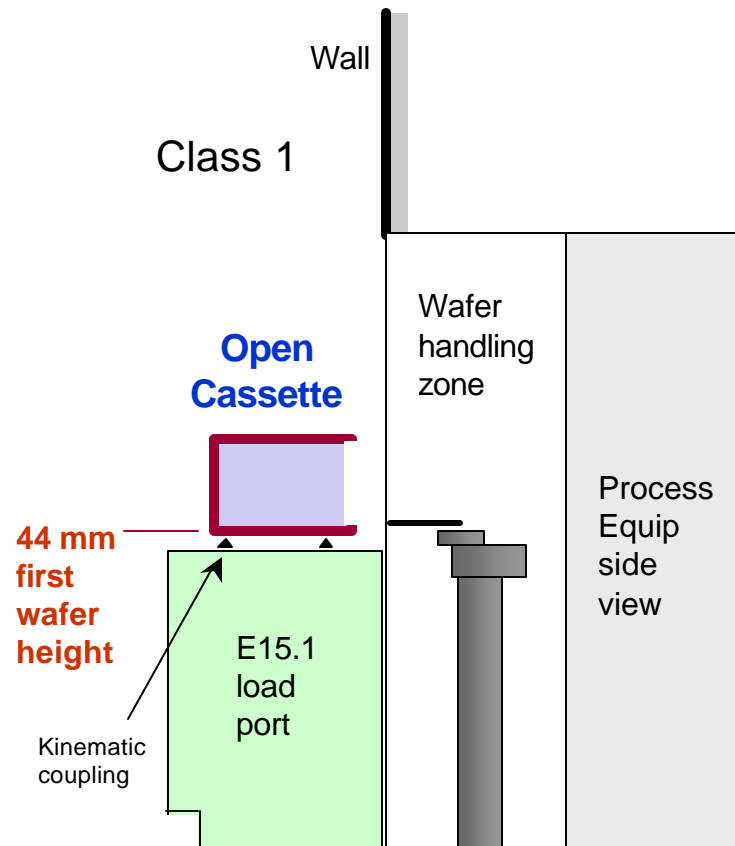
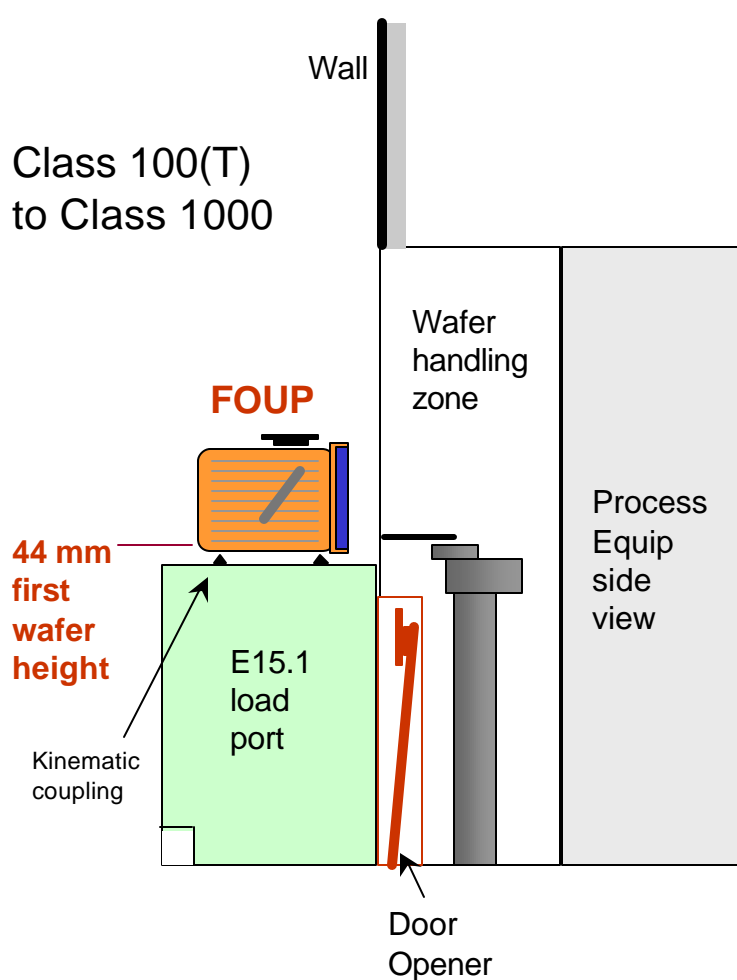
FOUP



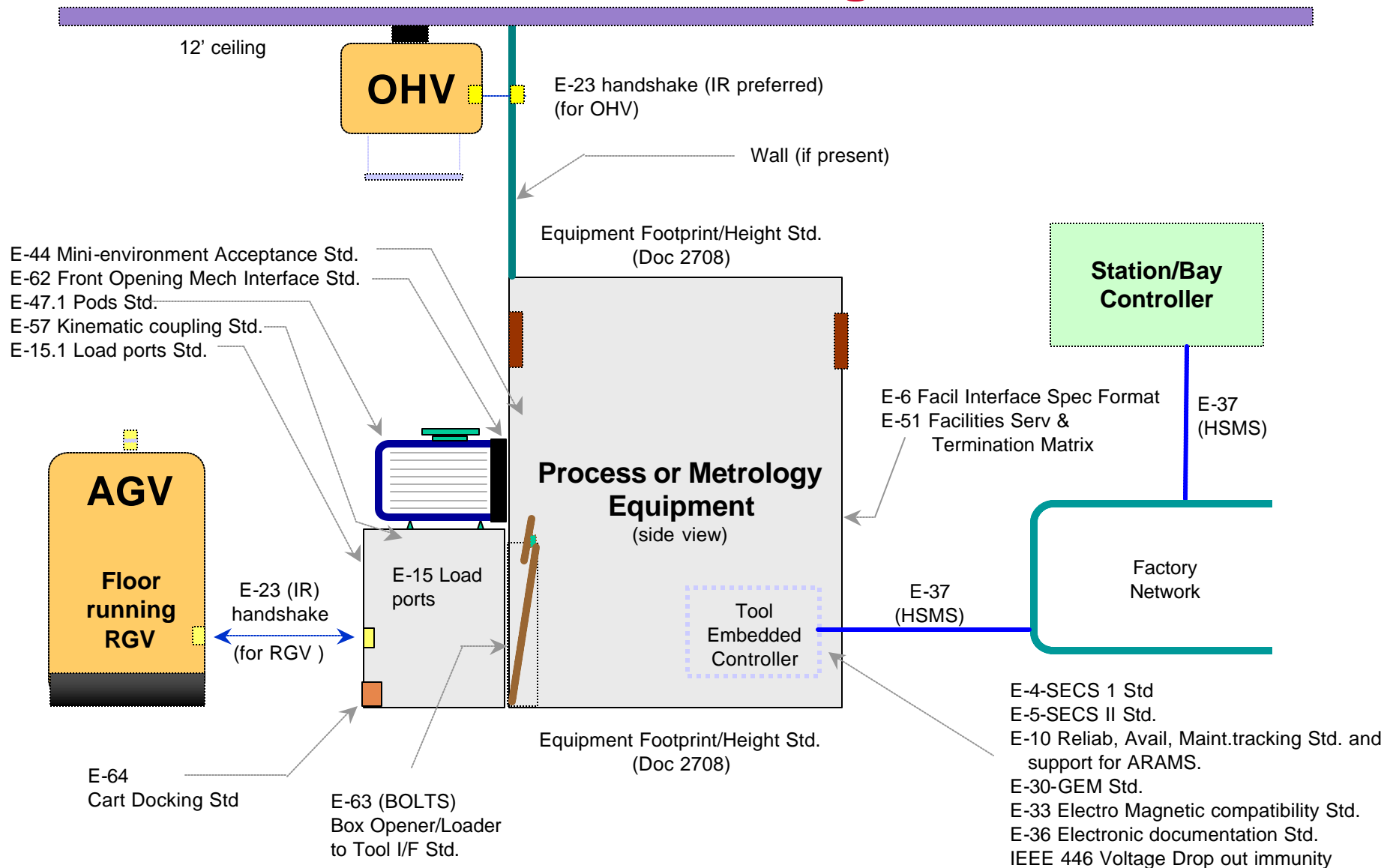
Open
Cassette



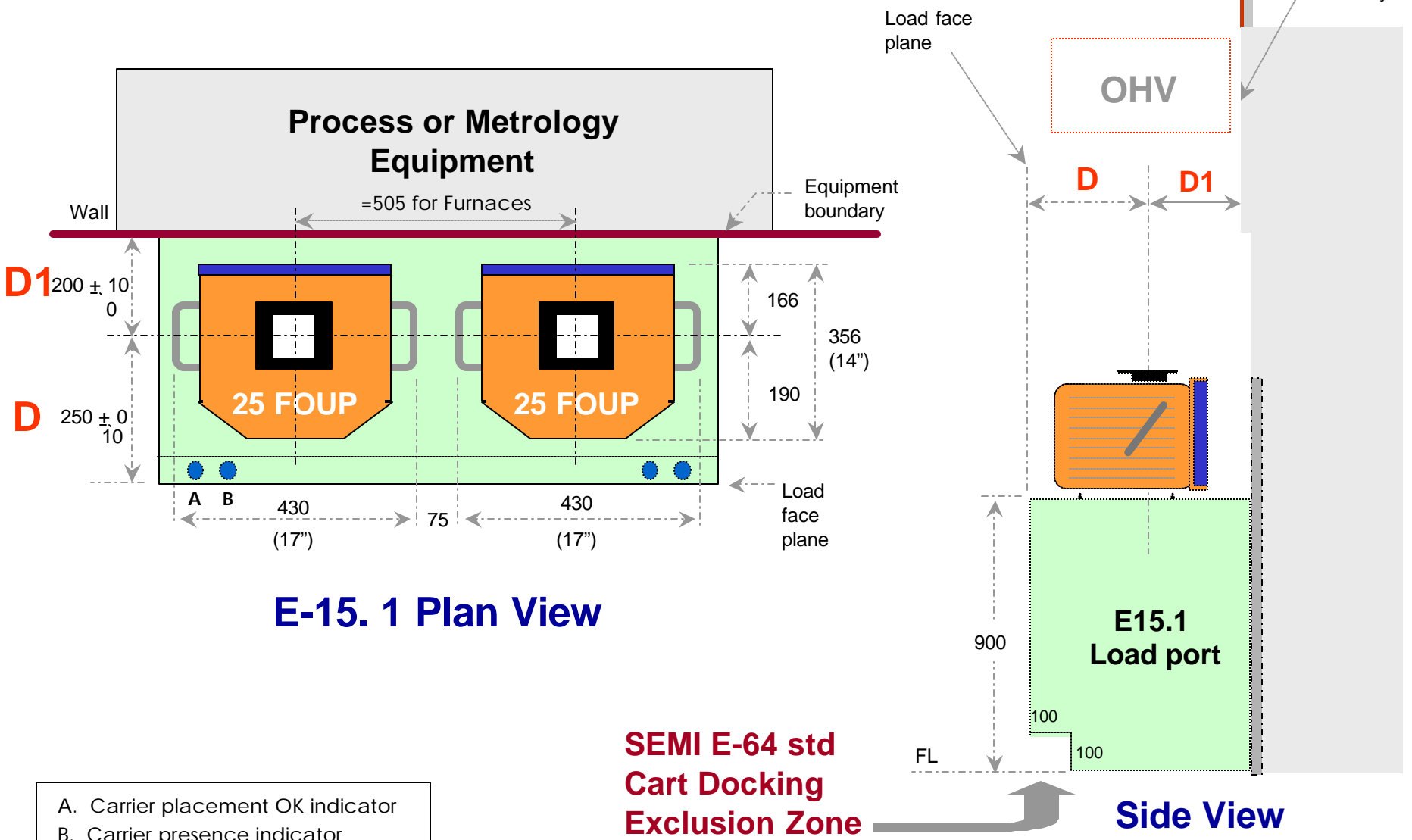
FOUP and Open Cassette Compatibility



“MUST HAVE” SEMI Standards covering Interfaces



SEMI Standards (overview)



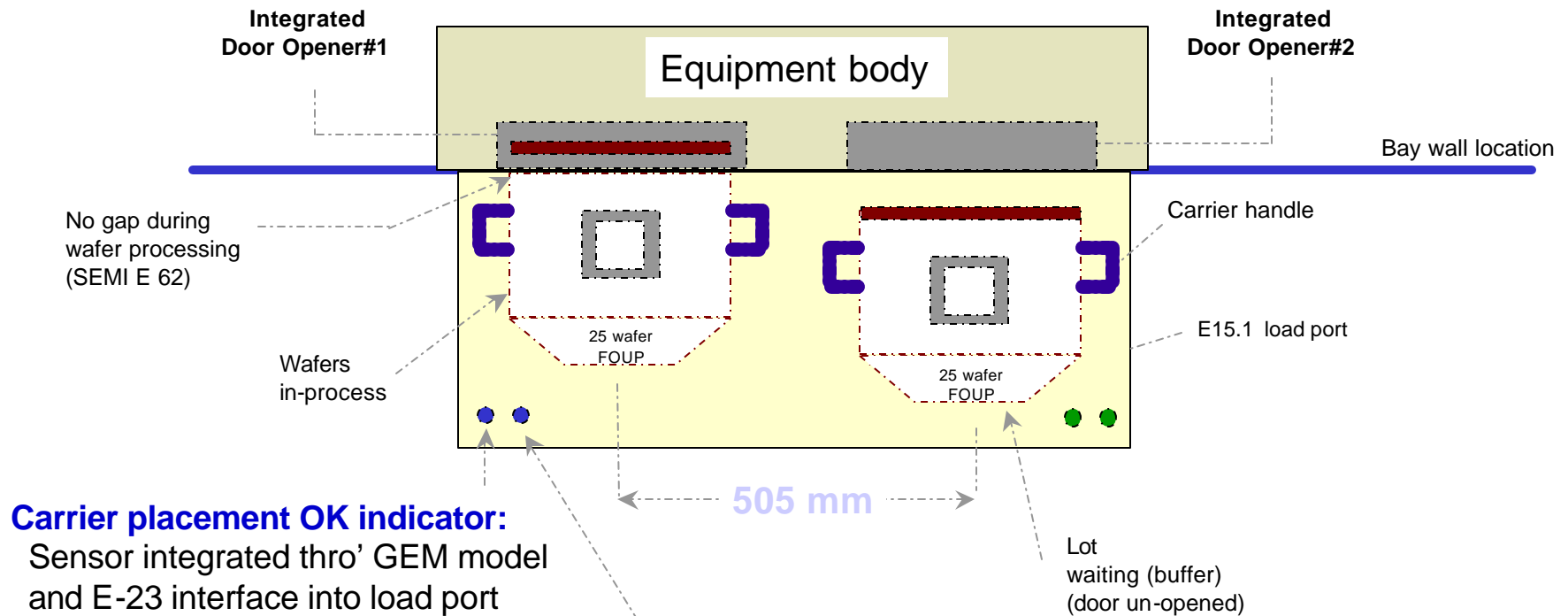
E-15.1 Plan View

Side View

A. Carrier placement OK indicator
B. Carrier presence indicator

**SEMI E-64 std
Cart Docking
Exclusion Zone**

E15.1 load ports



Carrier placement OK indicator:

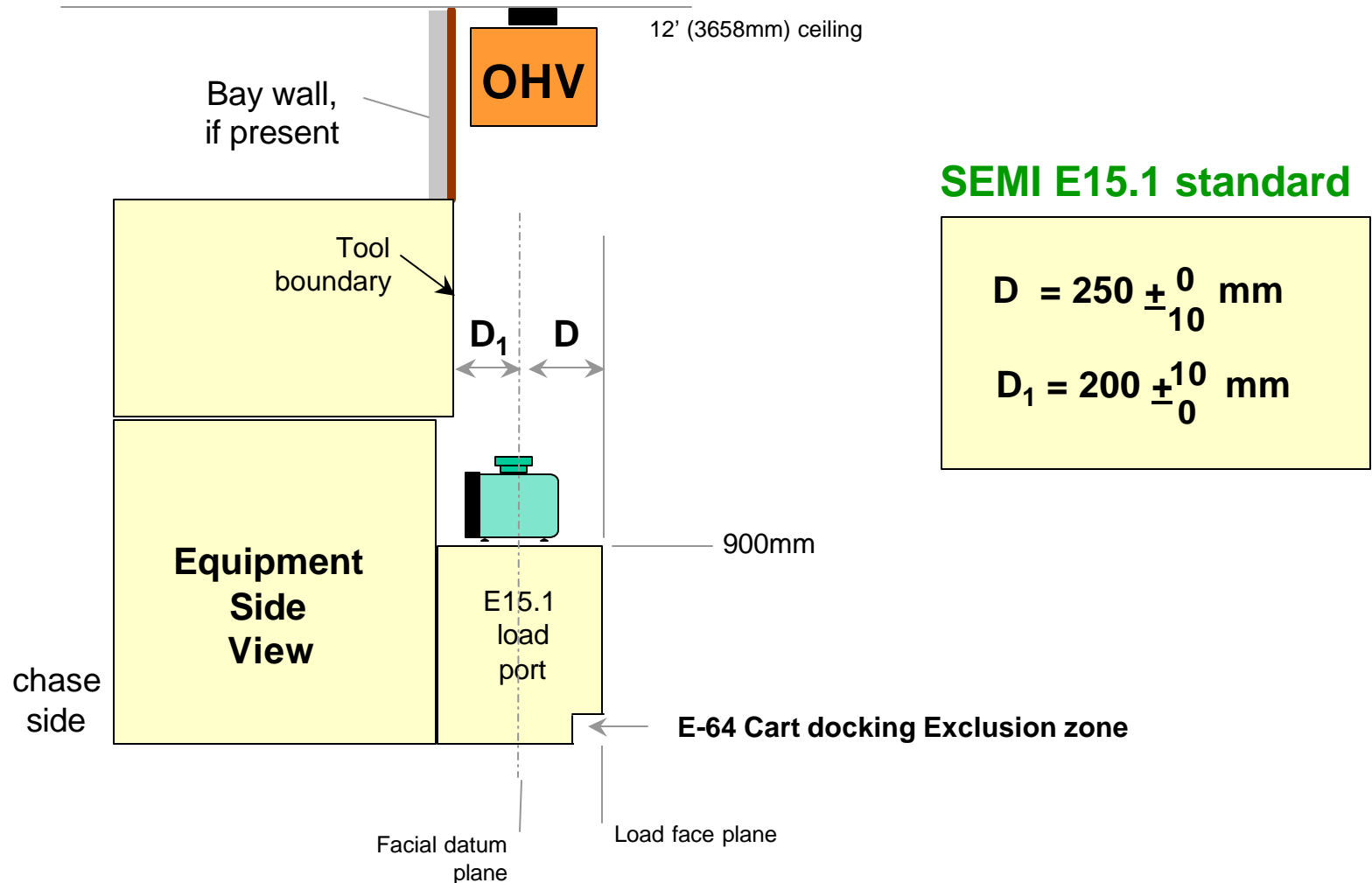
Sensor integrated thro' GEM model and E-23 interface into load port

Carrier presence OK indicator:

Sensor integrated thro' GEM model and E-23 interface into load port.

Tools such as the Furnace, Implanter, Wet Station, and Litho Track need more than 1+1.

Dimensions D and D₁ in SEMI E15.1 are critical for OHV and RGV implementation



Recently Passed Provisional Standards

- E15.1 - Loadport Standard
- E47 - Front Opening Unified Pod
- E57 - Kinematic Coupling
- E62 - Front Opening Interface Mech Standards
- E64 - Cart Docking Standard

These are I300I and J300 Endorsed SEMI Standards

Please use them in your equipment design!!!!!!

Intel's Synergy with 14 I300I Guidelines

(<http://www.I300I.org>)

Carrier Architecture:

- 25 wafer capacity carrier.
- SEMI stds compliant.
- Front Opening Unified pod.
- Pod handles for Operators & Automation.

Loadports:

- E15.1 with door openers.
- Slot/Cassette integrity.
- Ports only on one side.
- Straight line installation in bay, along bay wall.

Material Handling handshake:

- Low-level handshake.
- SEMI E23 interface (use IR option)

Cart Docking Interface:

- SEMI E-64.

Overhead Carrier Delivery:

- Easement for delivery system
- E15.1 Option 1 compatible

"Integrated" Mini-Environment:

- reqd. on all Beta equipment.
- requires Door Openers integrated to all tools.
- Buffers to be ME compatible.

Non-stop wafer processing:

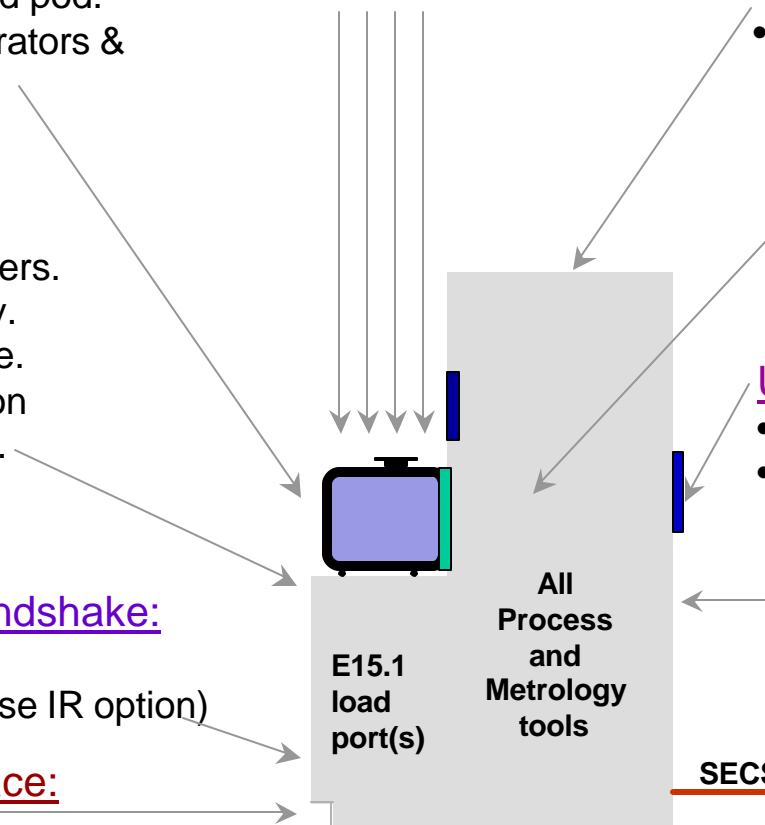
- Basic requirement is 1+1.
- Addtl. buffering is reqd for Furnace, Wet Stn, Implanter and Litho track.

U/I Controls/Display console

- Alternate connection to be provided.
- This is functionally equivalent to the one in the front.

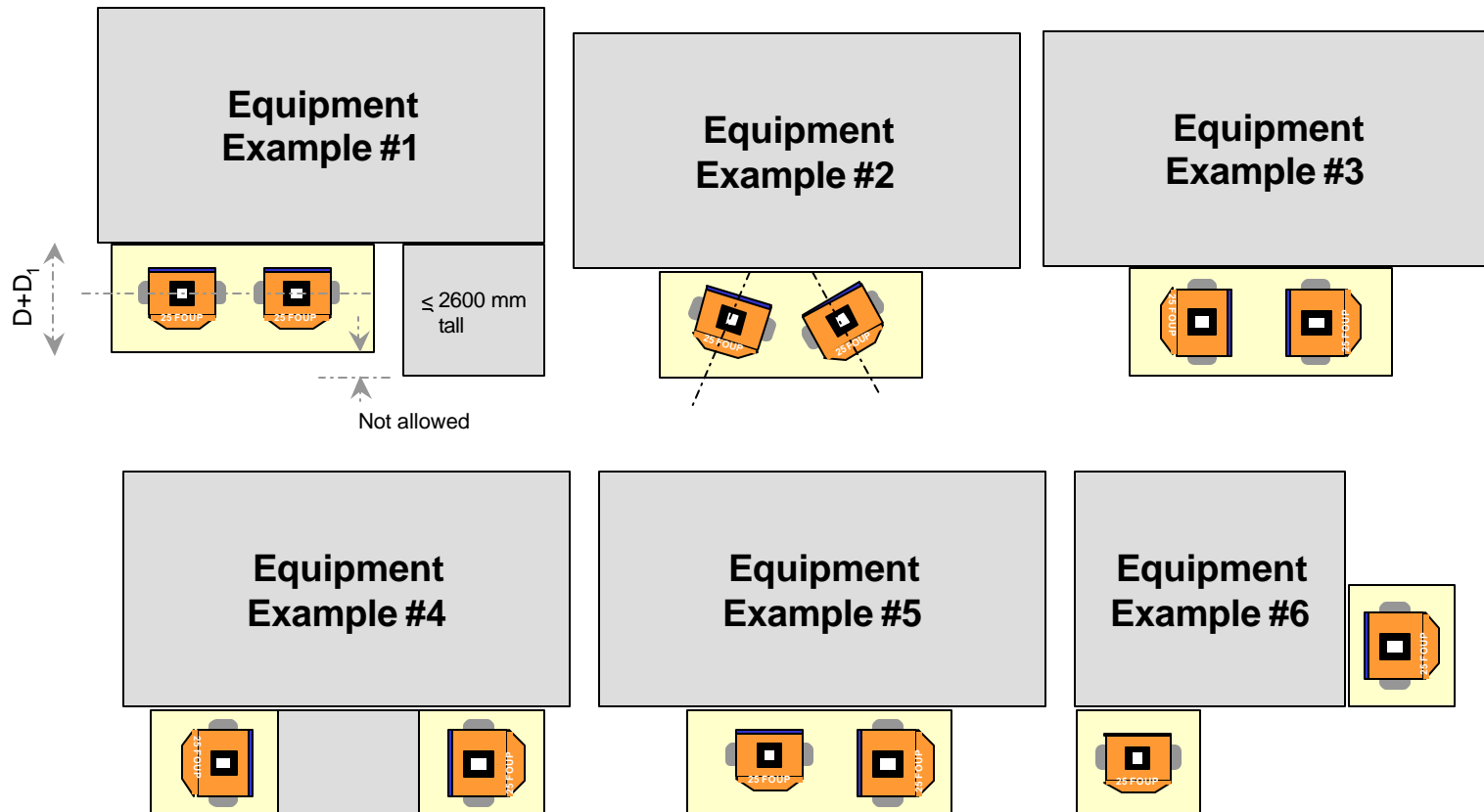
Dense packing in bay:

- should be supported without maintainability impacts.

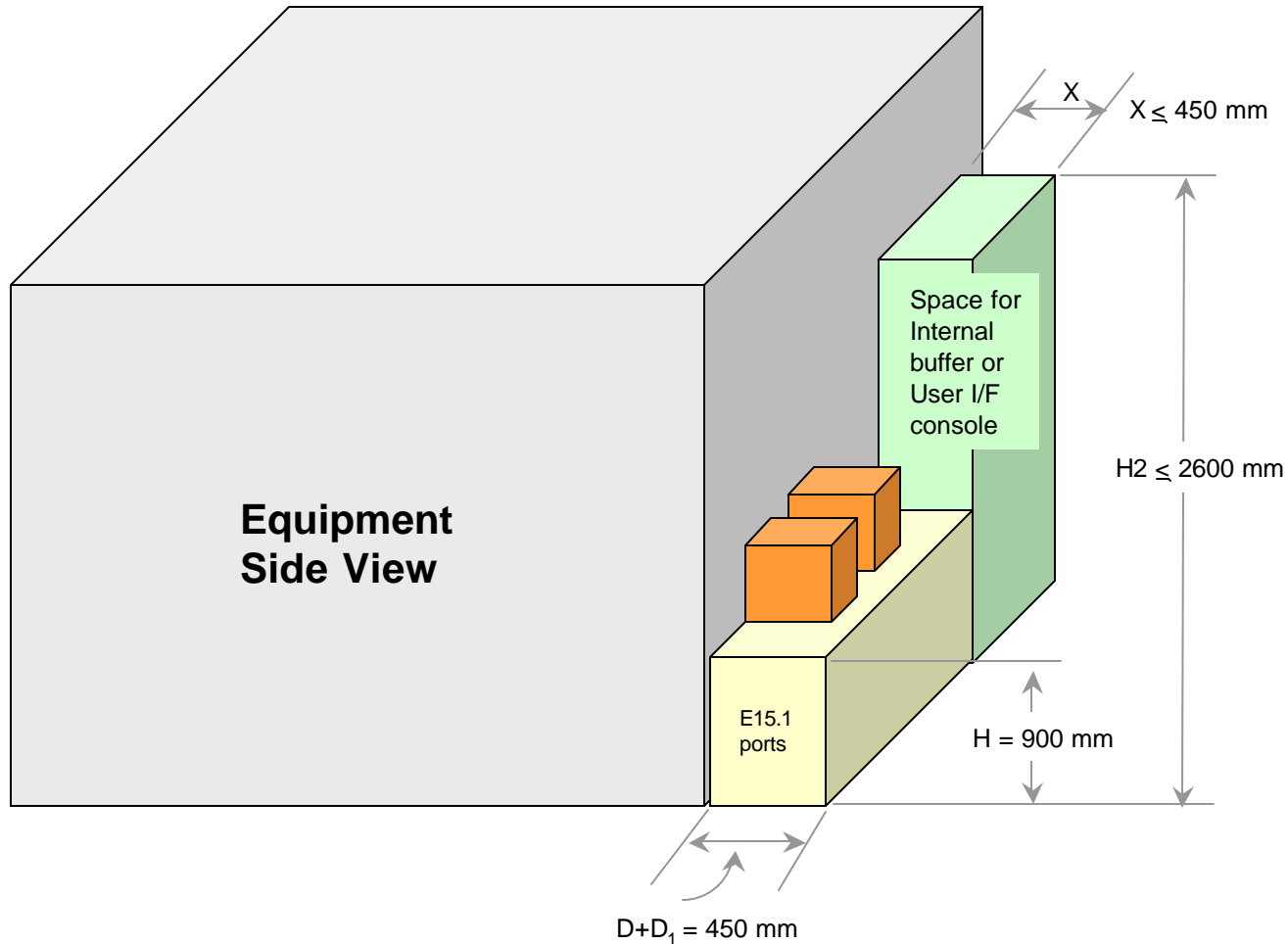


SECS II/GEM/HSMS Factory Network

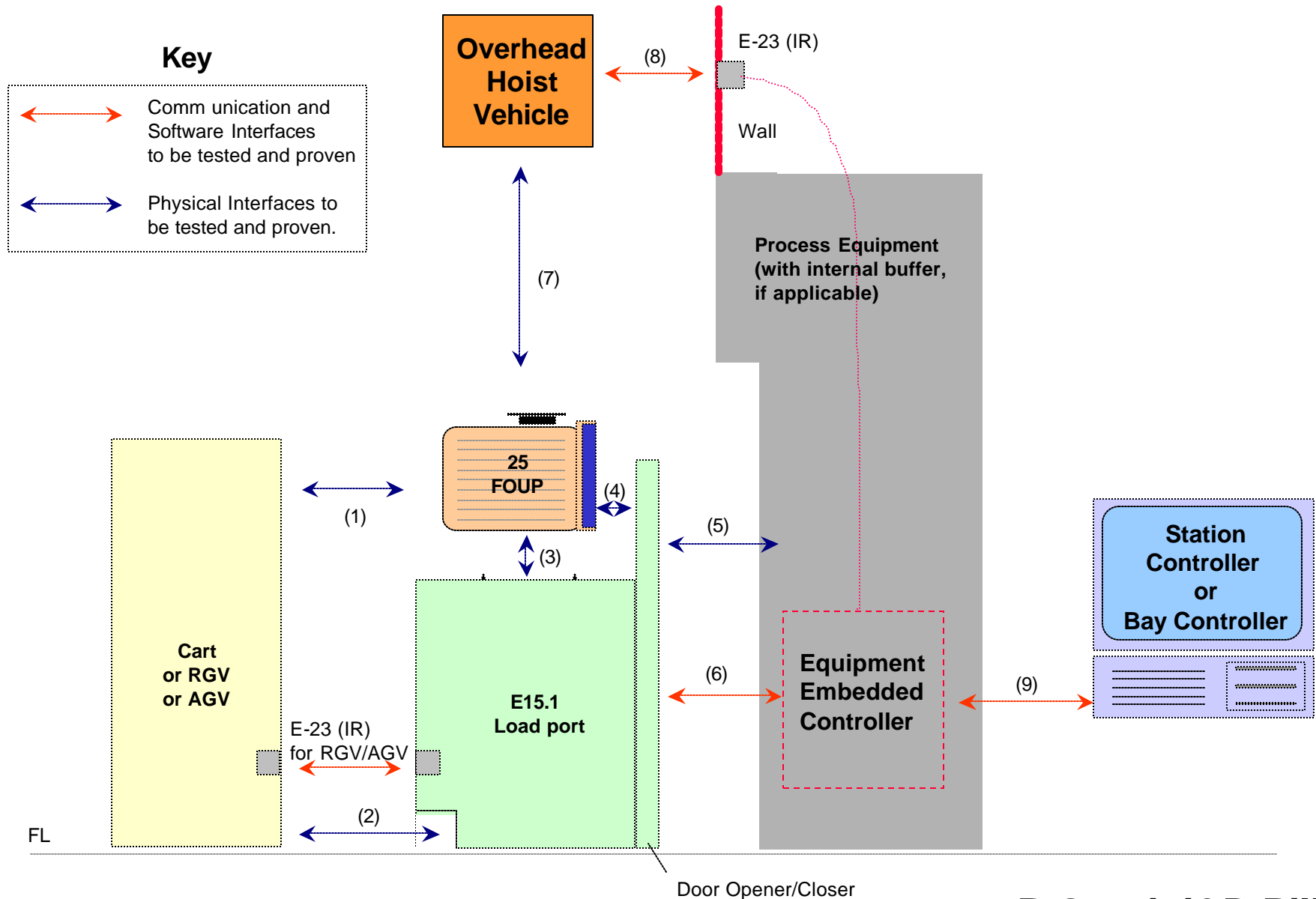
Some designs that don't meet Standards



A Valid Equipment Configuration

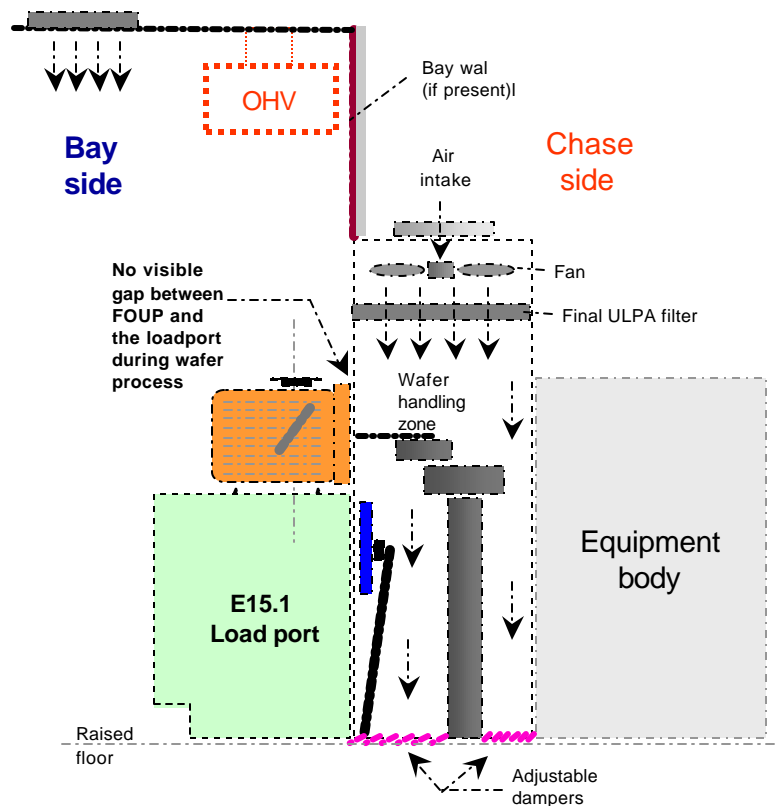


Inter-operability Interface testing



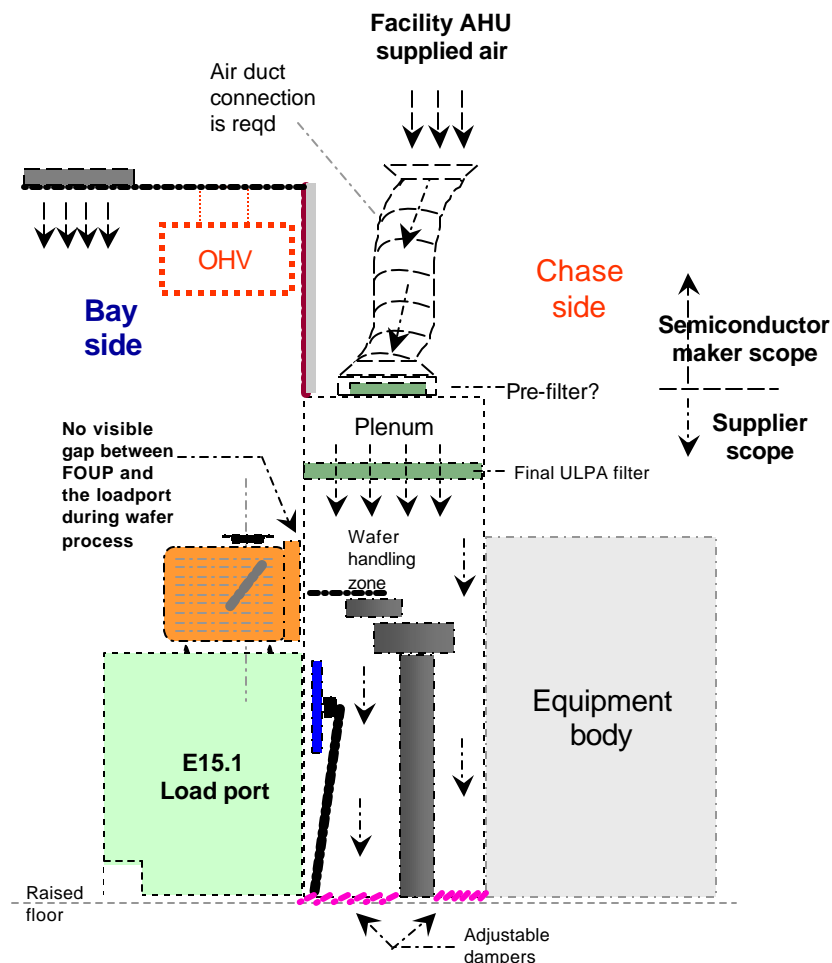
Integrated Mini-Environment is required

Integrated Mini-Environment



We want this!

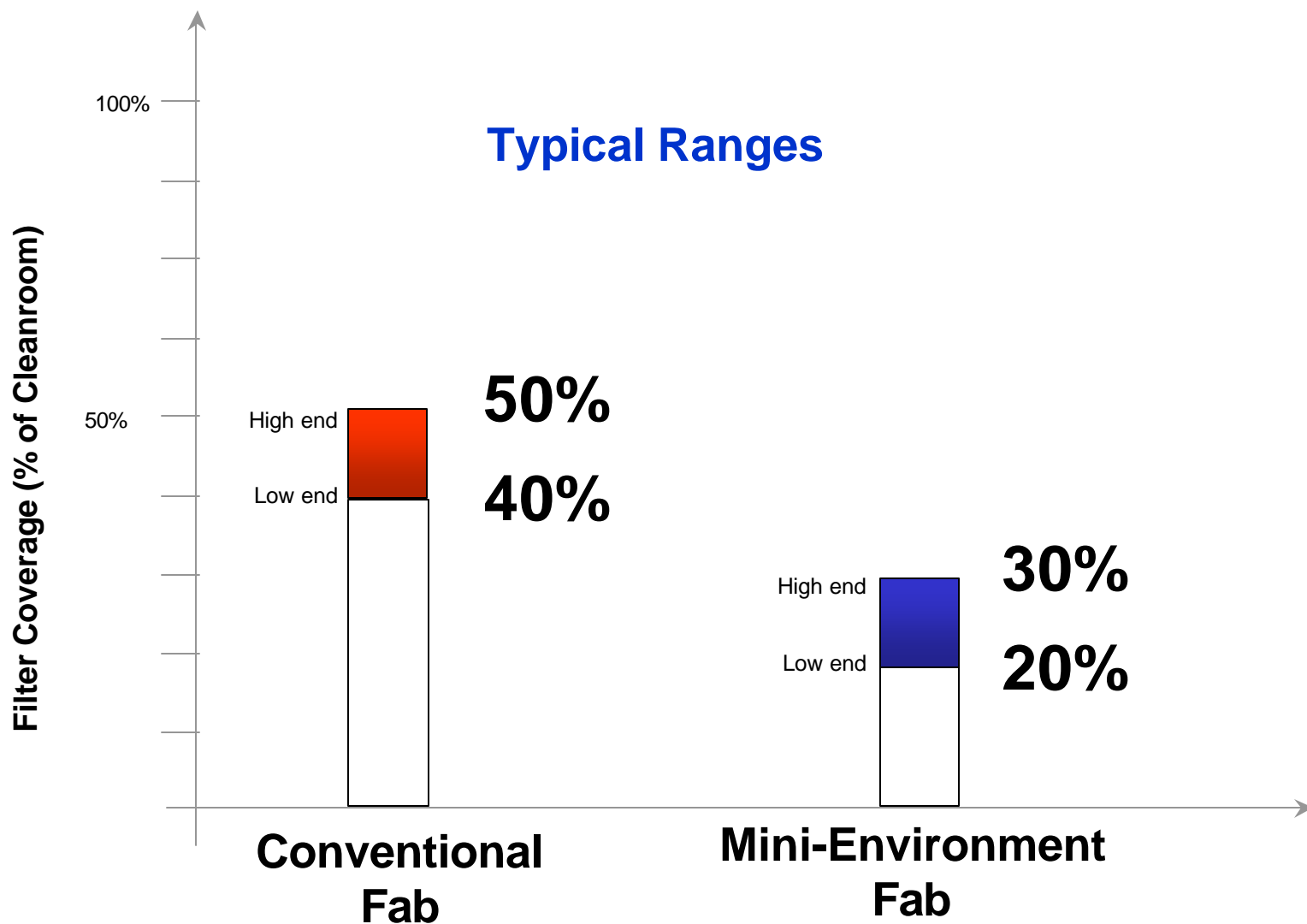
Facility Supplied Mini-Environment



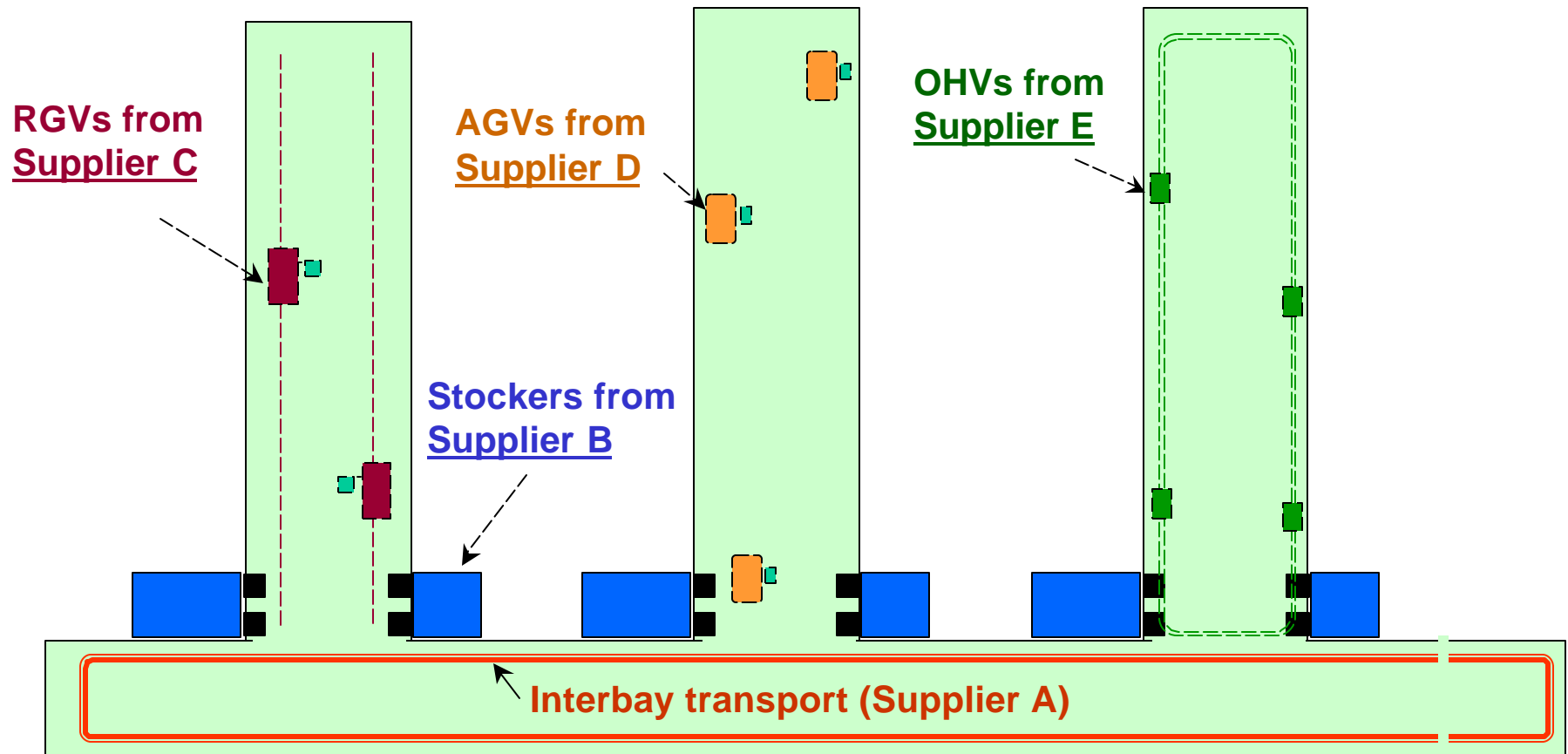
We don't want this!

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Reduced Filter Coverage with ME



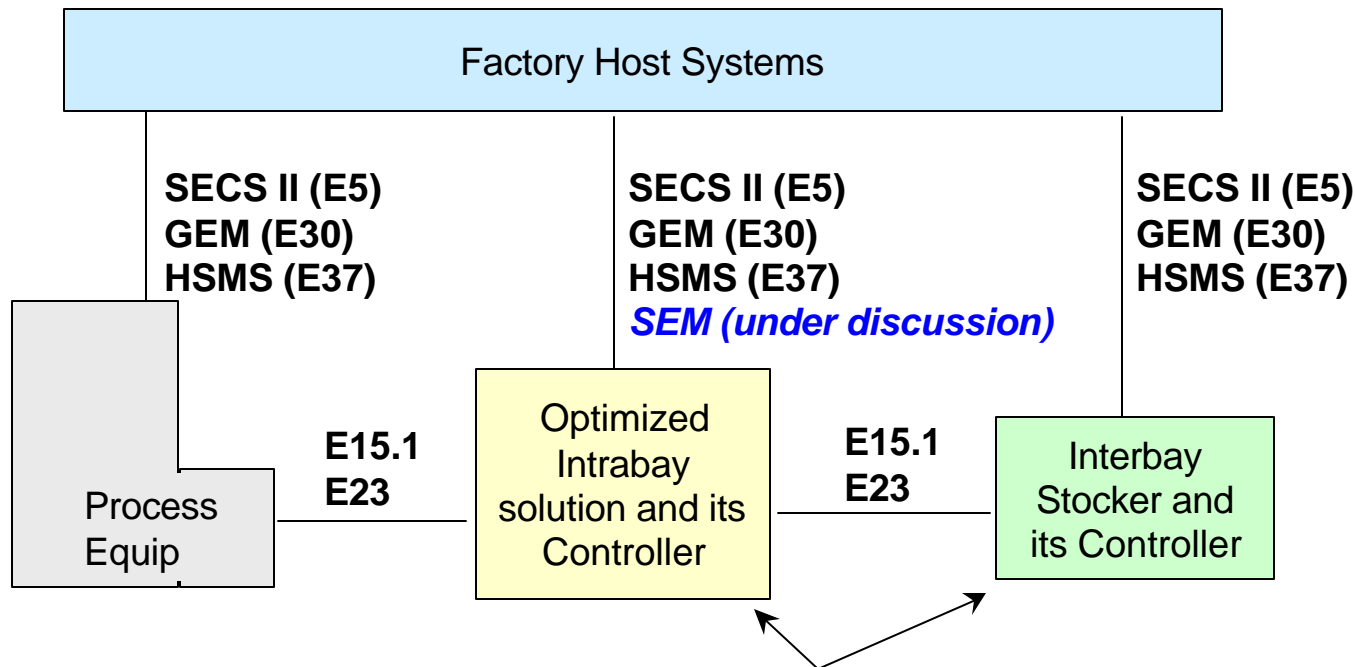
Standards enable AMHS to Inter-Operate



Use best systems that meets throughput, flexibility and cost goals

Important CIM Standards

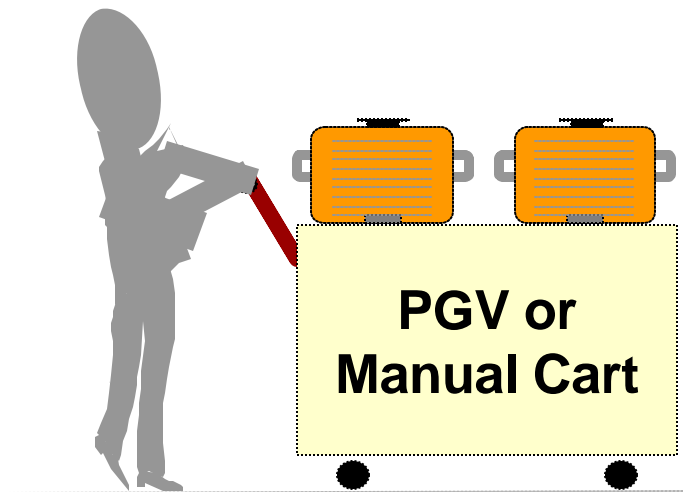
- In order to realize the desired bay throughput, layout flexibility and cost effectiveness of CIM systems, the device manufacturers want to ensure the best Intrabay solution for each bay. This implies multiple Intrabay solutions (best in class for Stocker, OHT, AGV or RGV) and their respective suppliers for a factory. Appropriate Controls architectures including standardization of physical and communication interfaces are a must to realize this vision.



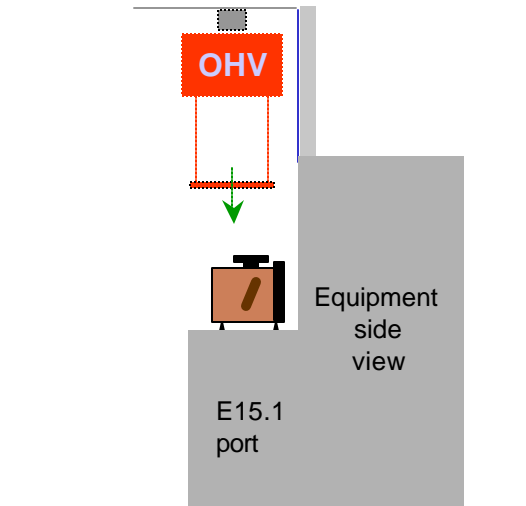
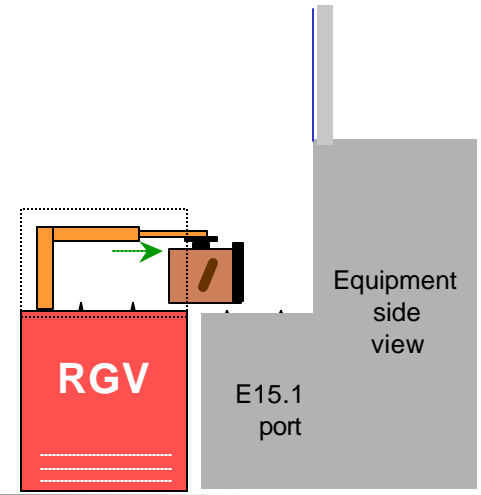
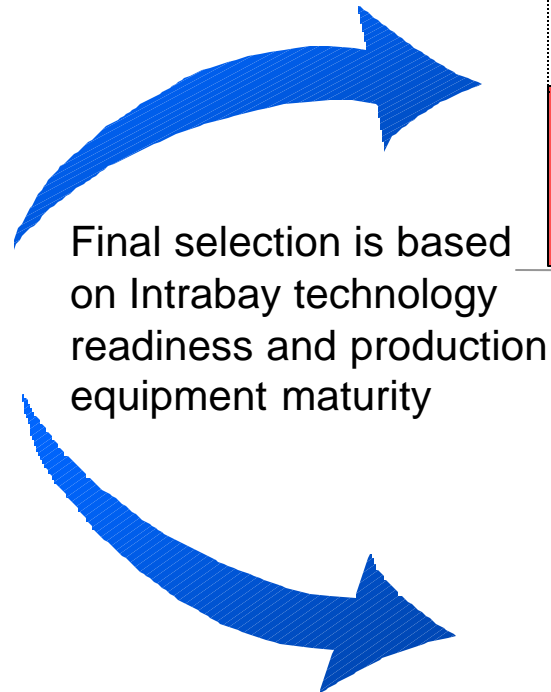
These may be supplied
by different manufacturers

Need to manage the Transition

Start-Up



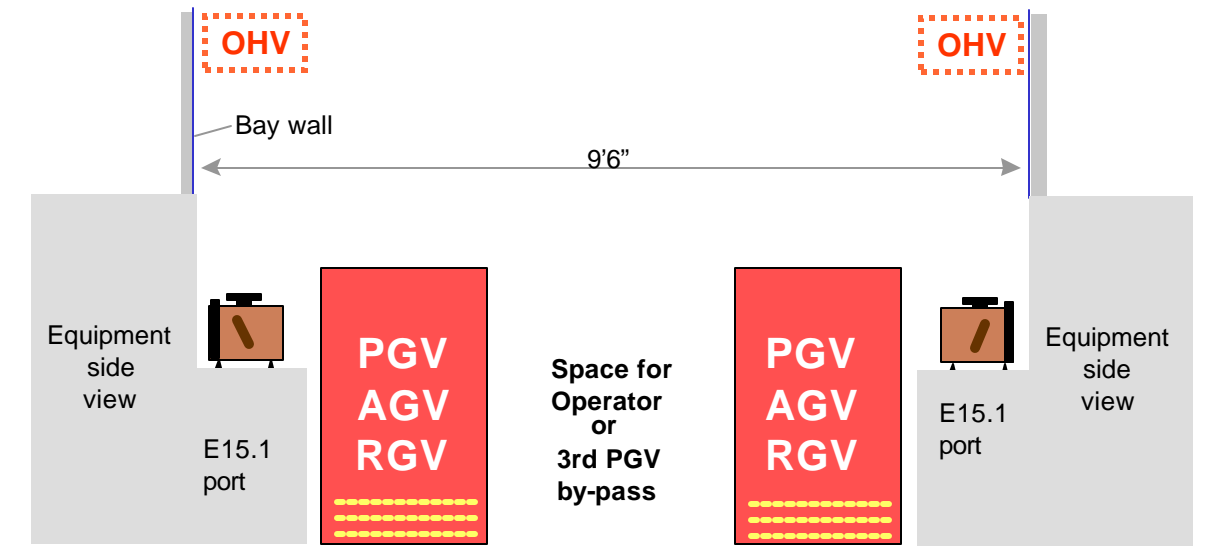
High Volume



Bay width transition for OHV

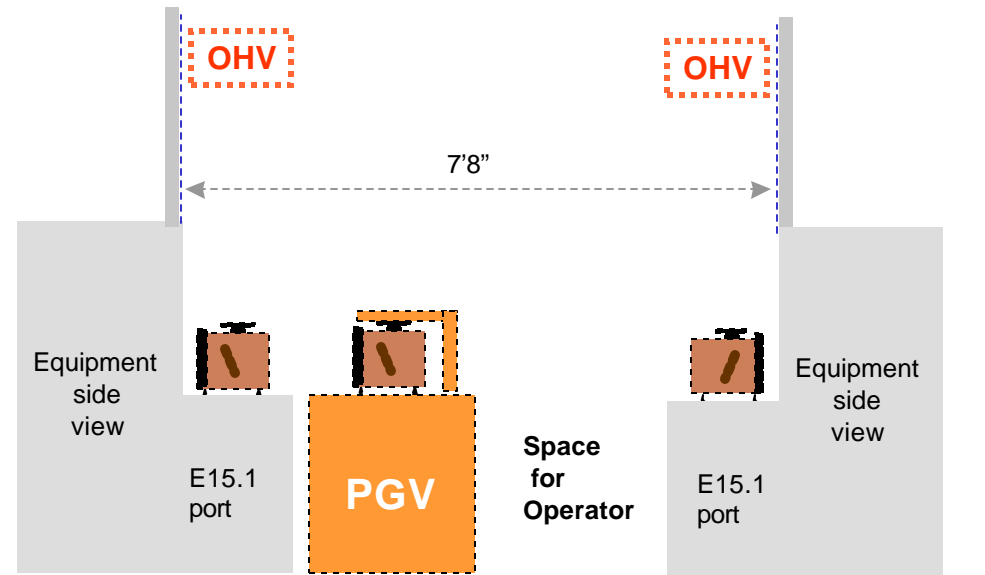
Low Risk

This bay-width permits RGV, PGV and OHV to be functional

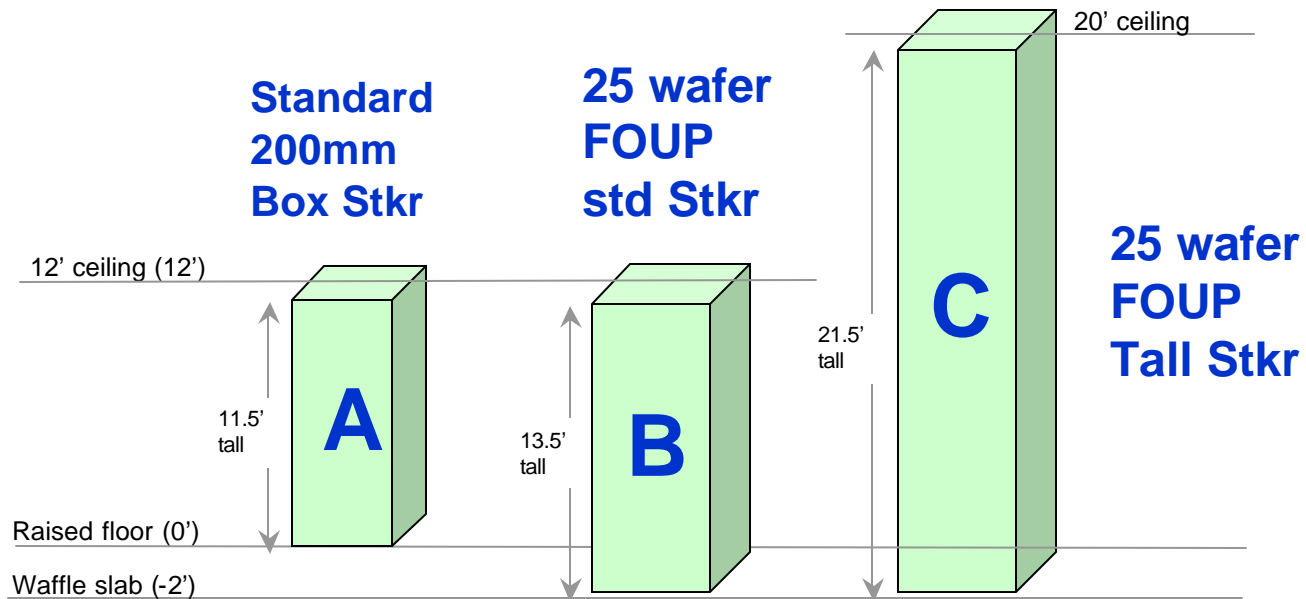
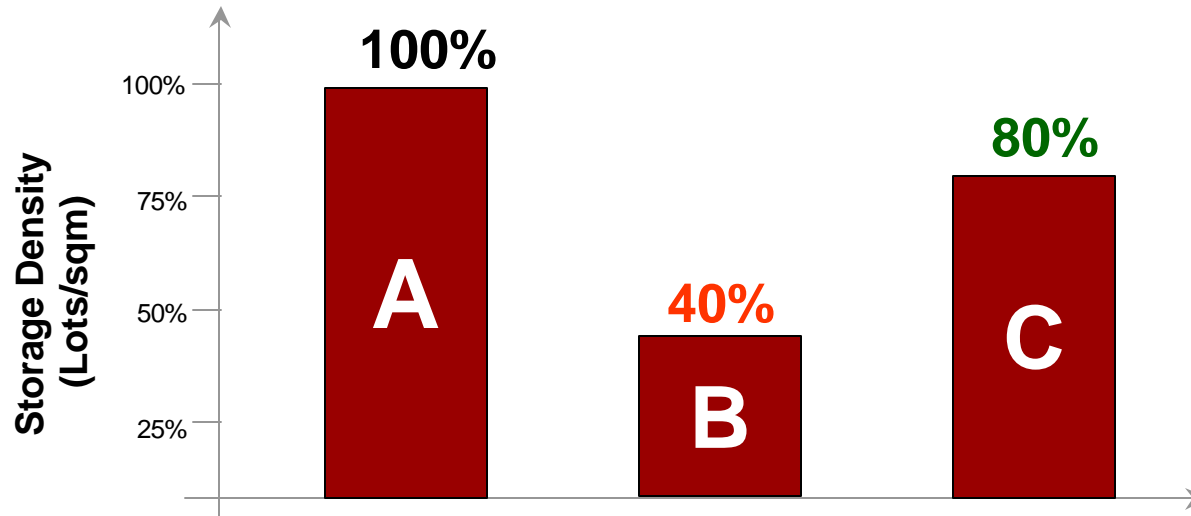


High Risk

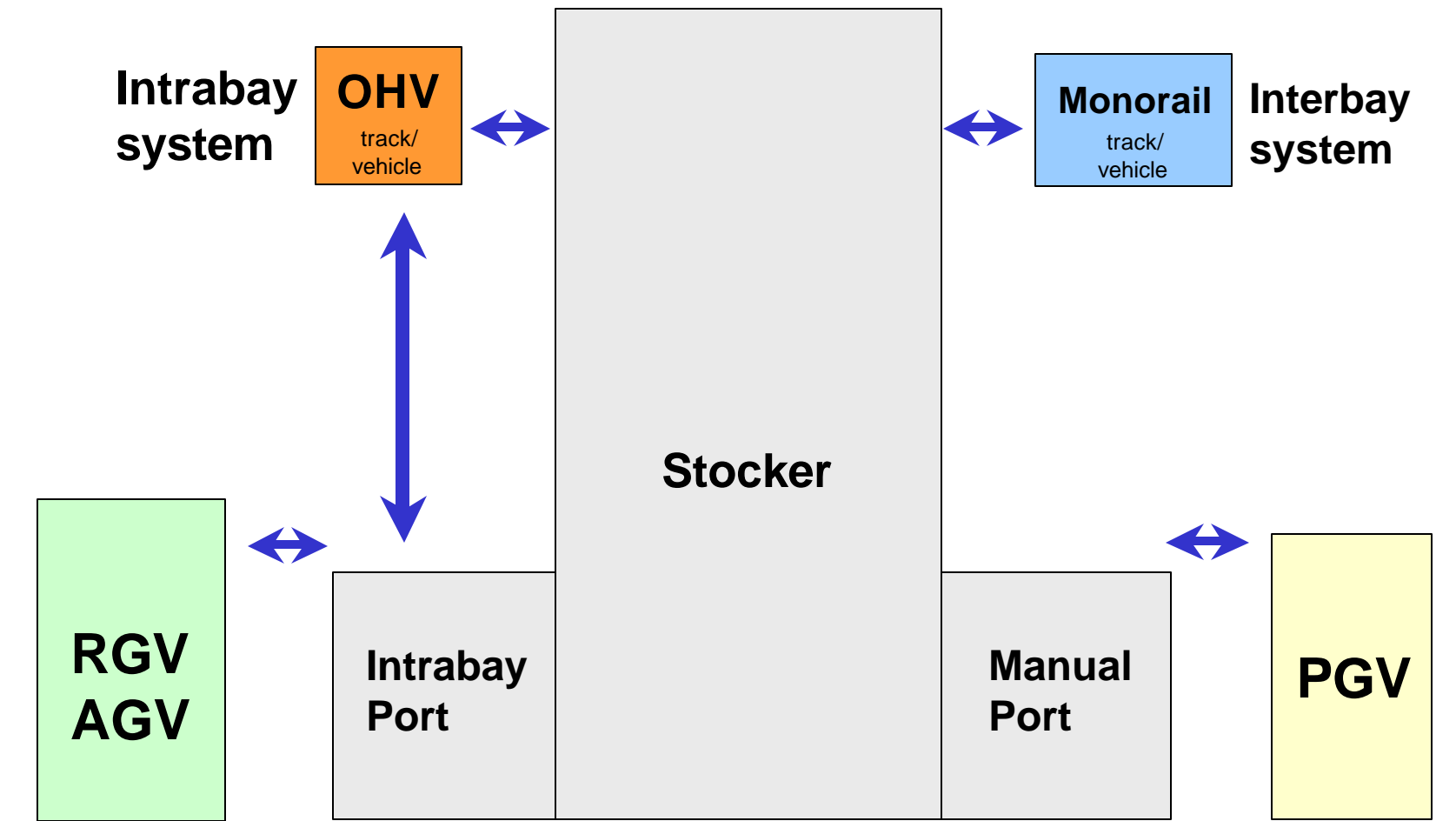
Here a single PGV is used as manual backup for OHV system. RGV not feasible with this approach. Also PGV does not have extended throughput capability.



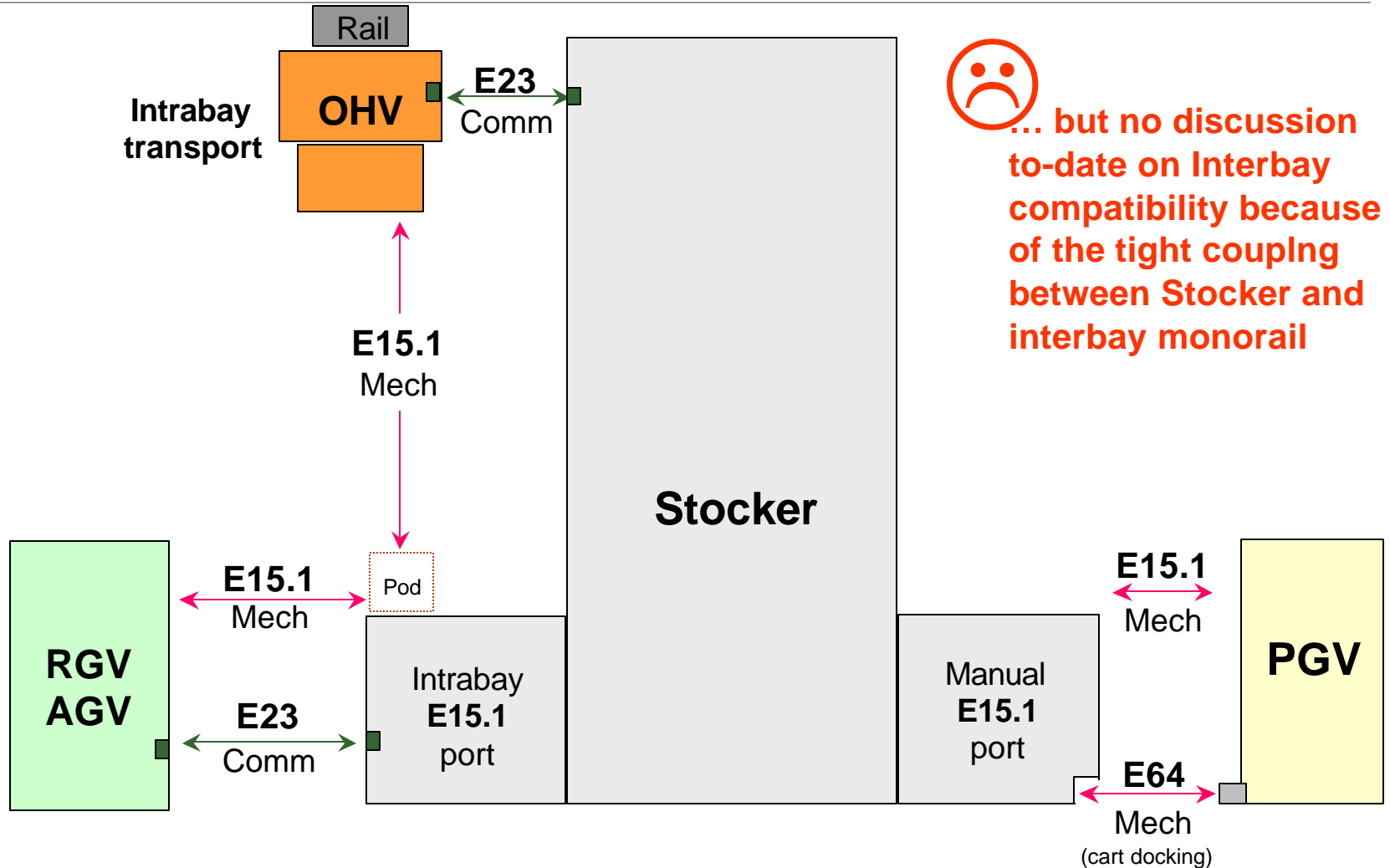
Why Tall Stockers ?



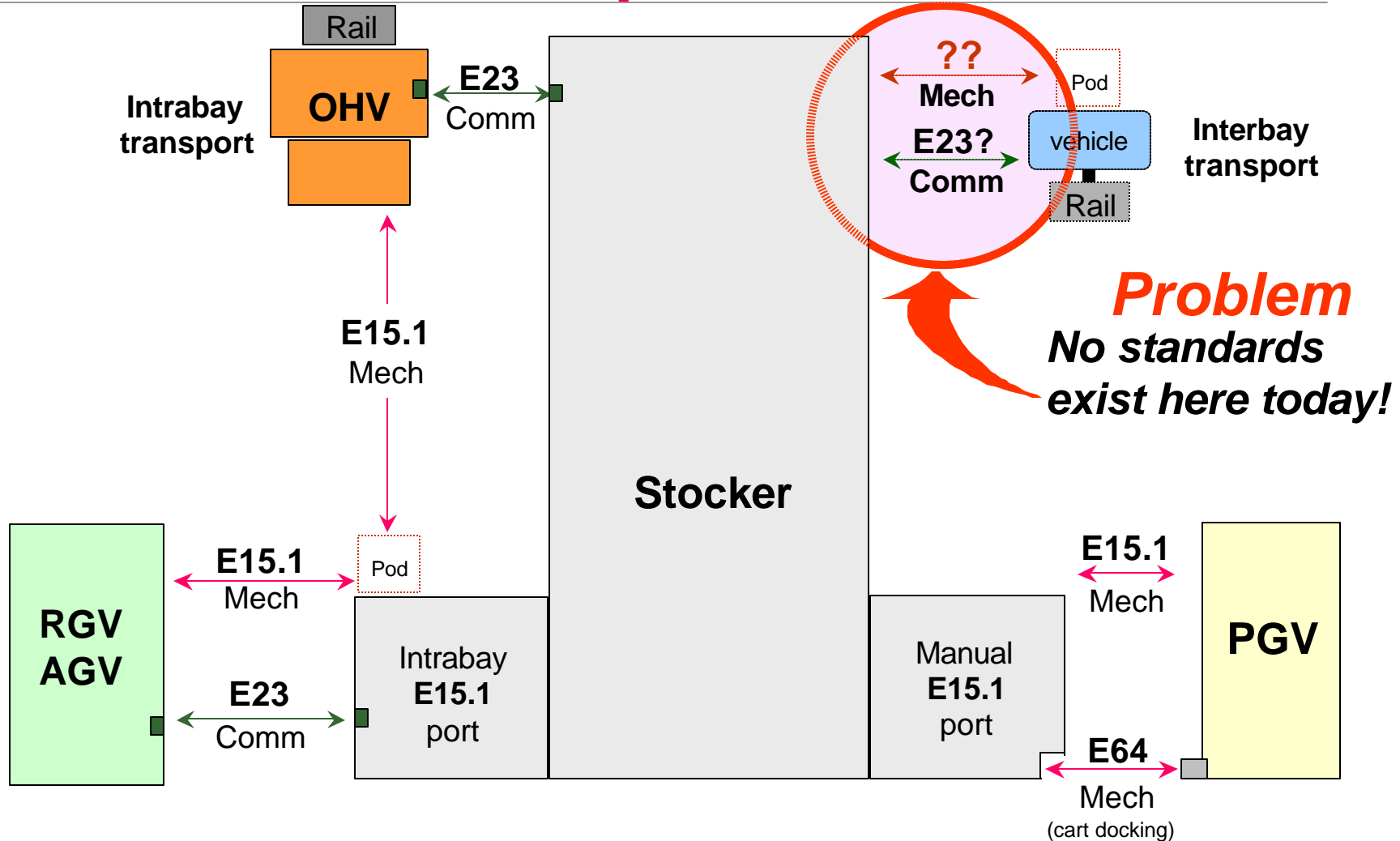
Interfaces required for AMHS Mix and Match



Intrabay componenets are fully inter-operable

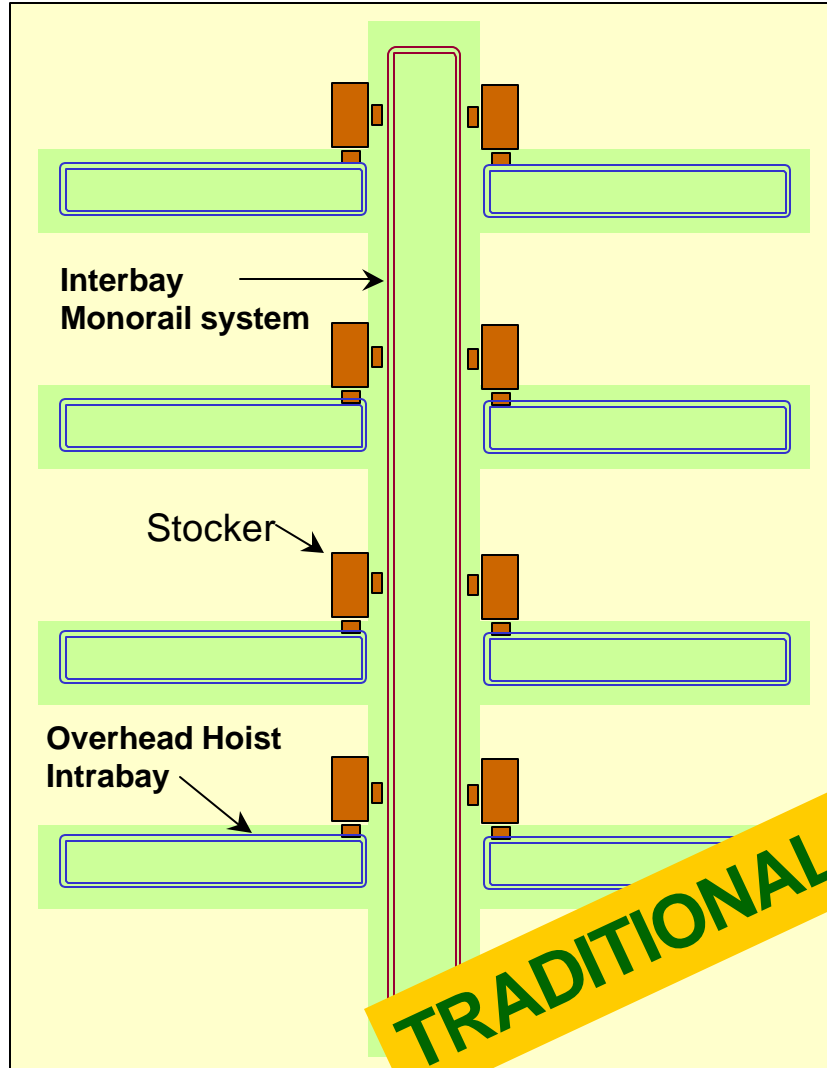


Complete AMHS Mix and Match: Inter-operability of Interbay components is required

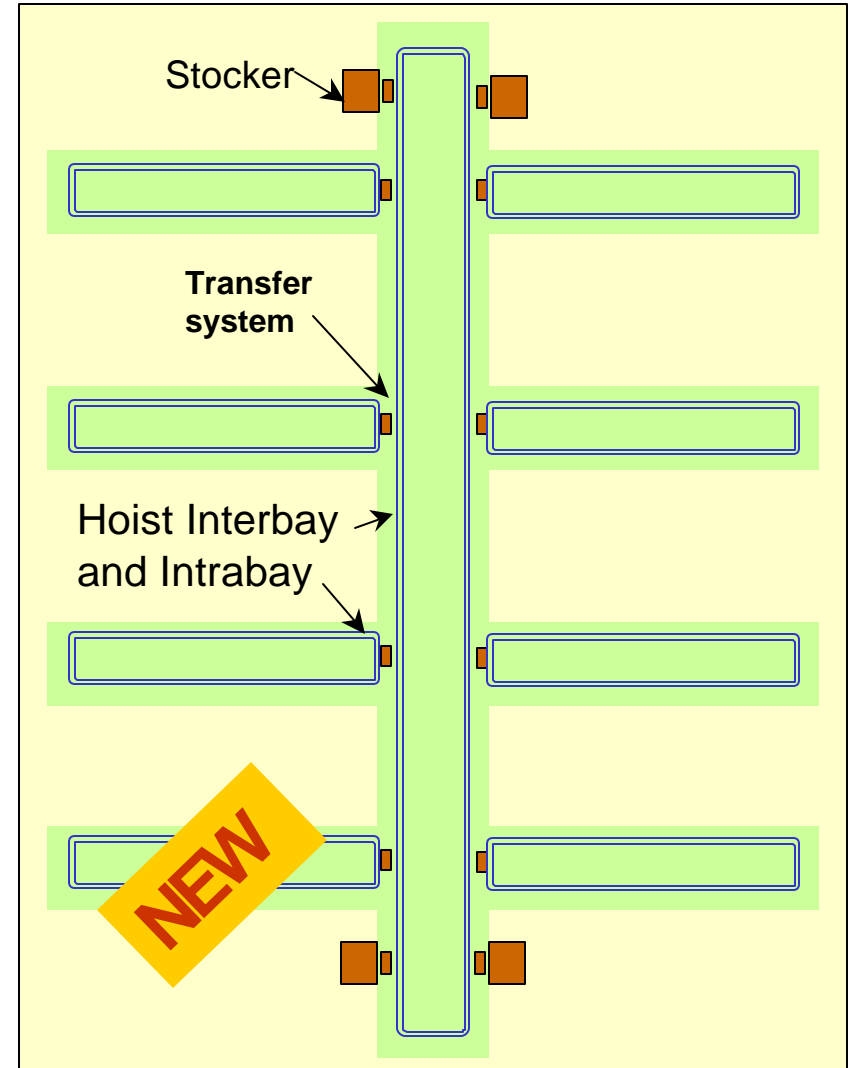


Combine Interbay and Intrabay Transport systems?

Separate Interbay and Intrabay

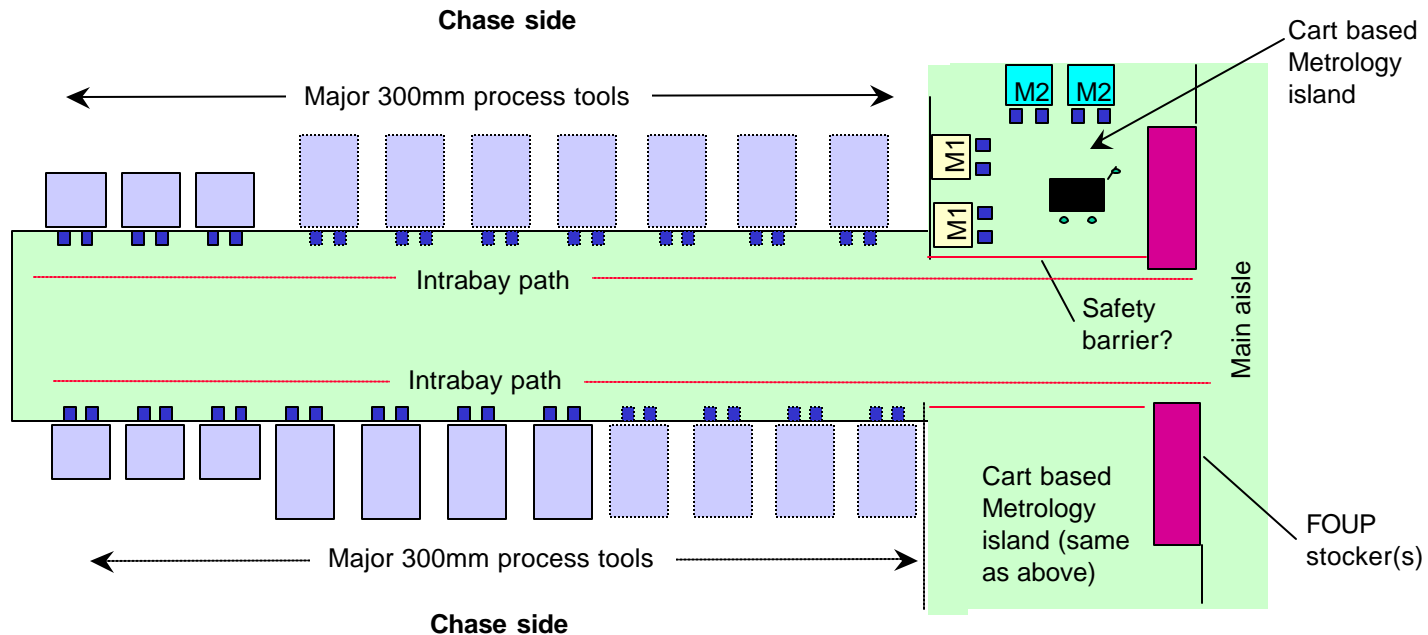


Combined Interbay and Intrabay



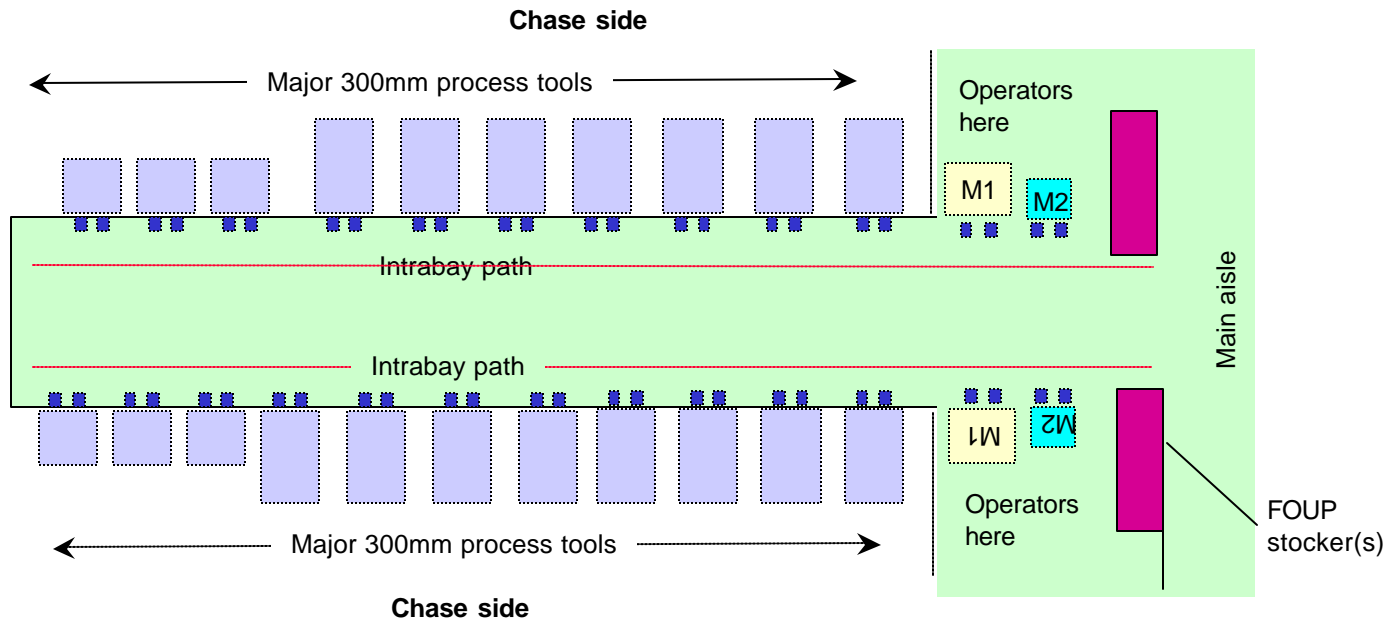
Metrology Layout Options

Case 1: Adjacent Metrology area for each bay



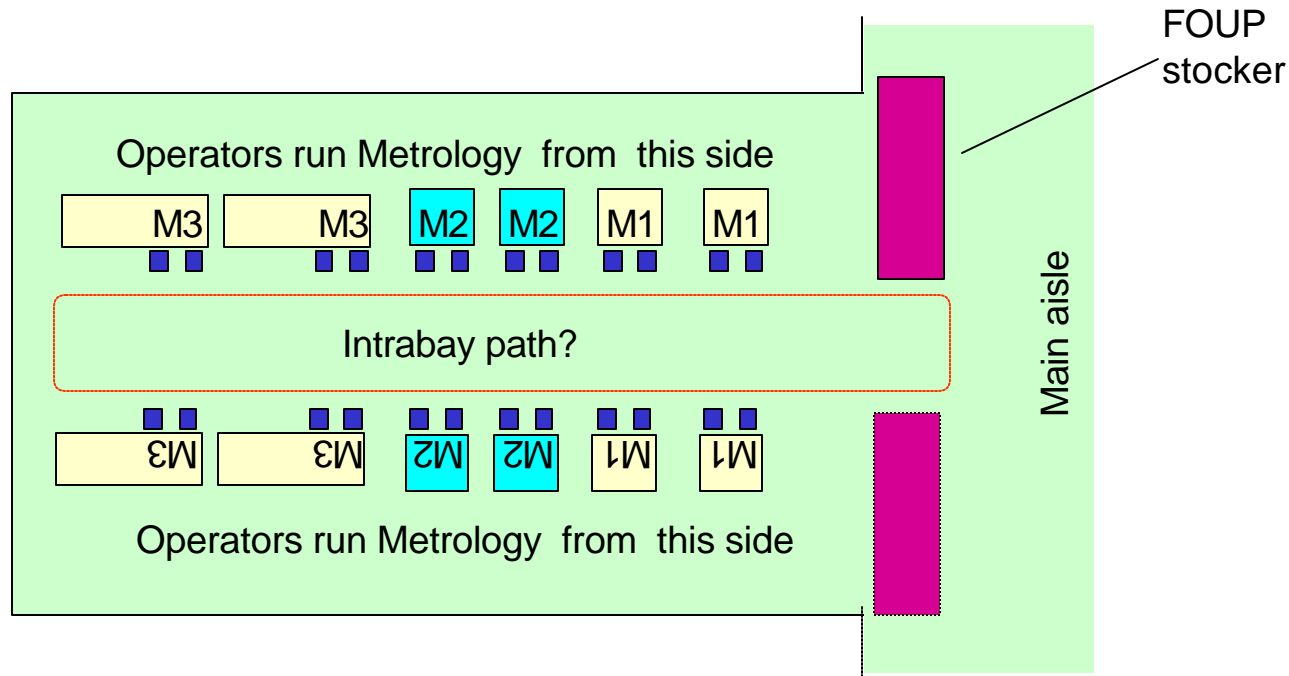
Metrology Layout Options ... contd

Case 2: In-line Metrology area mixed with major process equipment in each bay

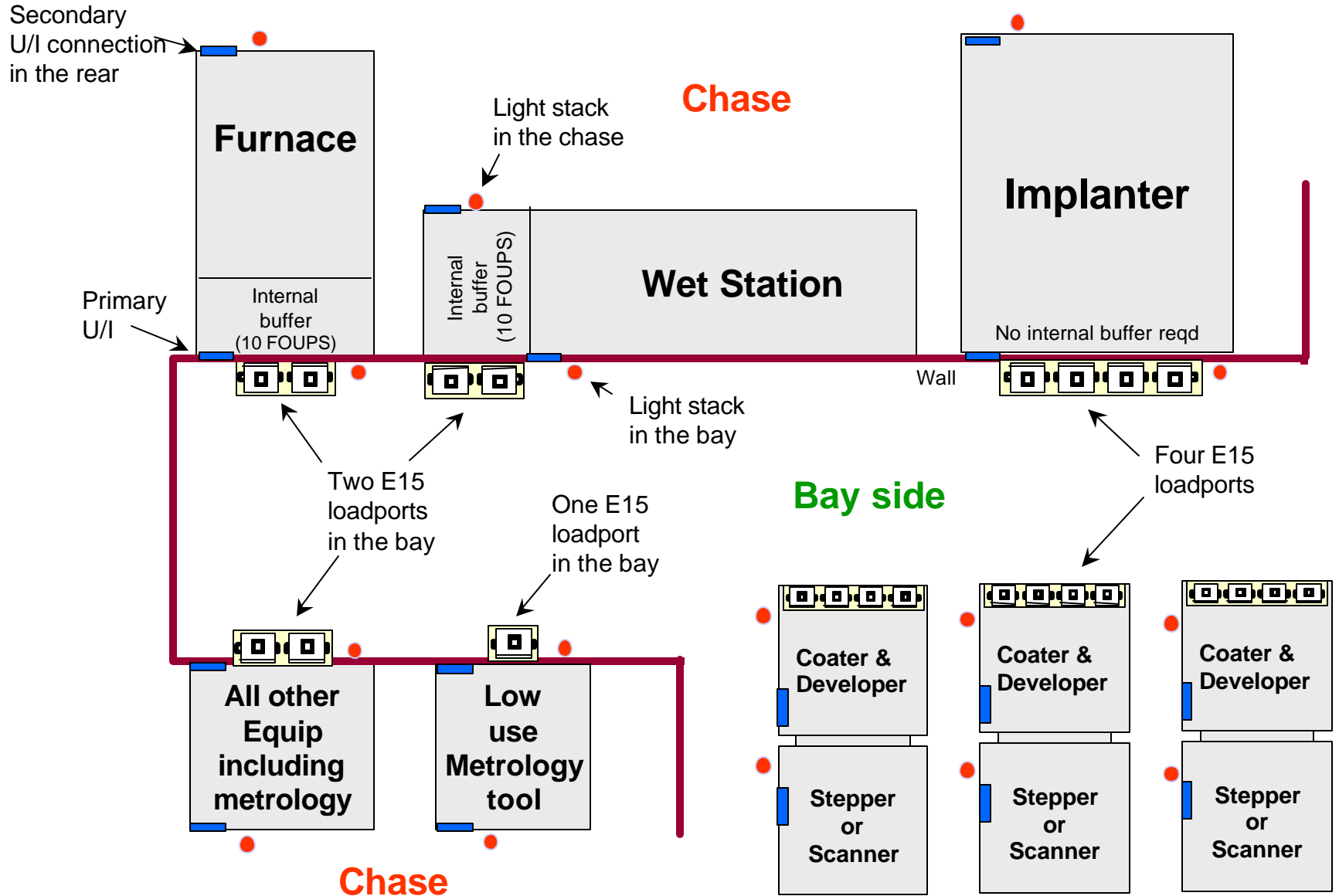


Metrology Layout Options ... contd

Case 3: Dedicated Metrology area laid out for Intrabay flexibility



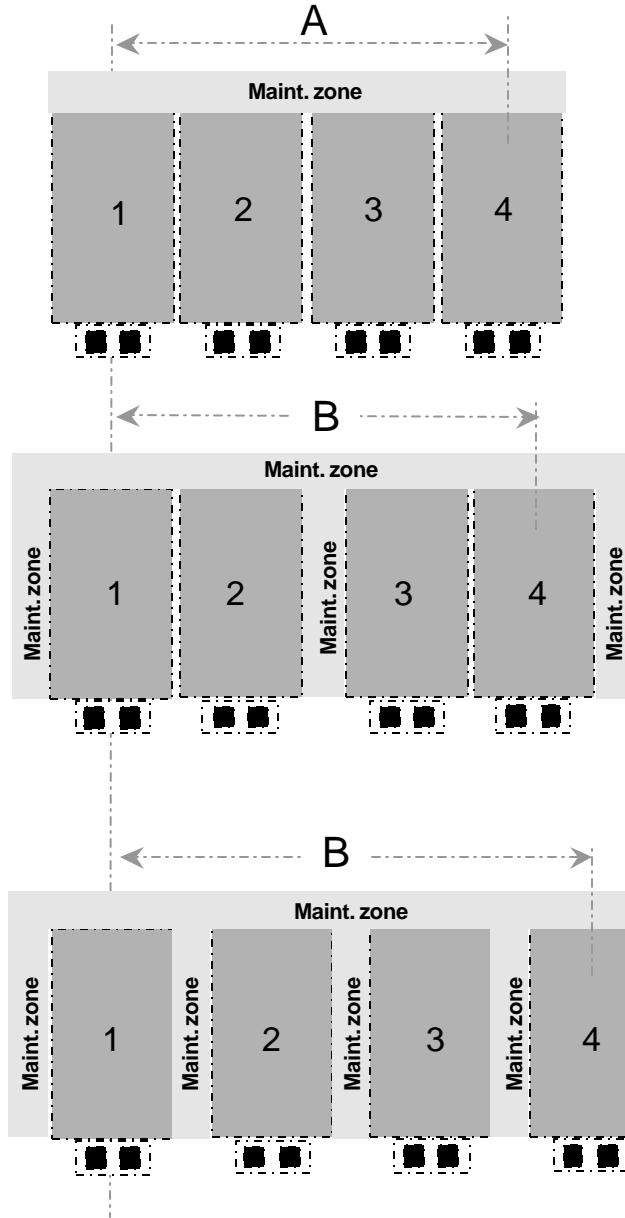
Equipment configuration for Buffering



Buffering Summary

Equipment type	# of E15.1 loadports	Addtl internally buffered lots
Vertical Diffusion Furnace	2	16
High Speed Wet Station	2	10
Implanter	4	No need
Coater/Developer Track	4	No need
All other tools, including in-line Metro tools	2	No need
Low use (off-line) Metrology tools	1	No need

Dense Packing of Equipment in a Bay



Case A
**Most
dense
packing**

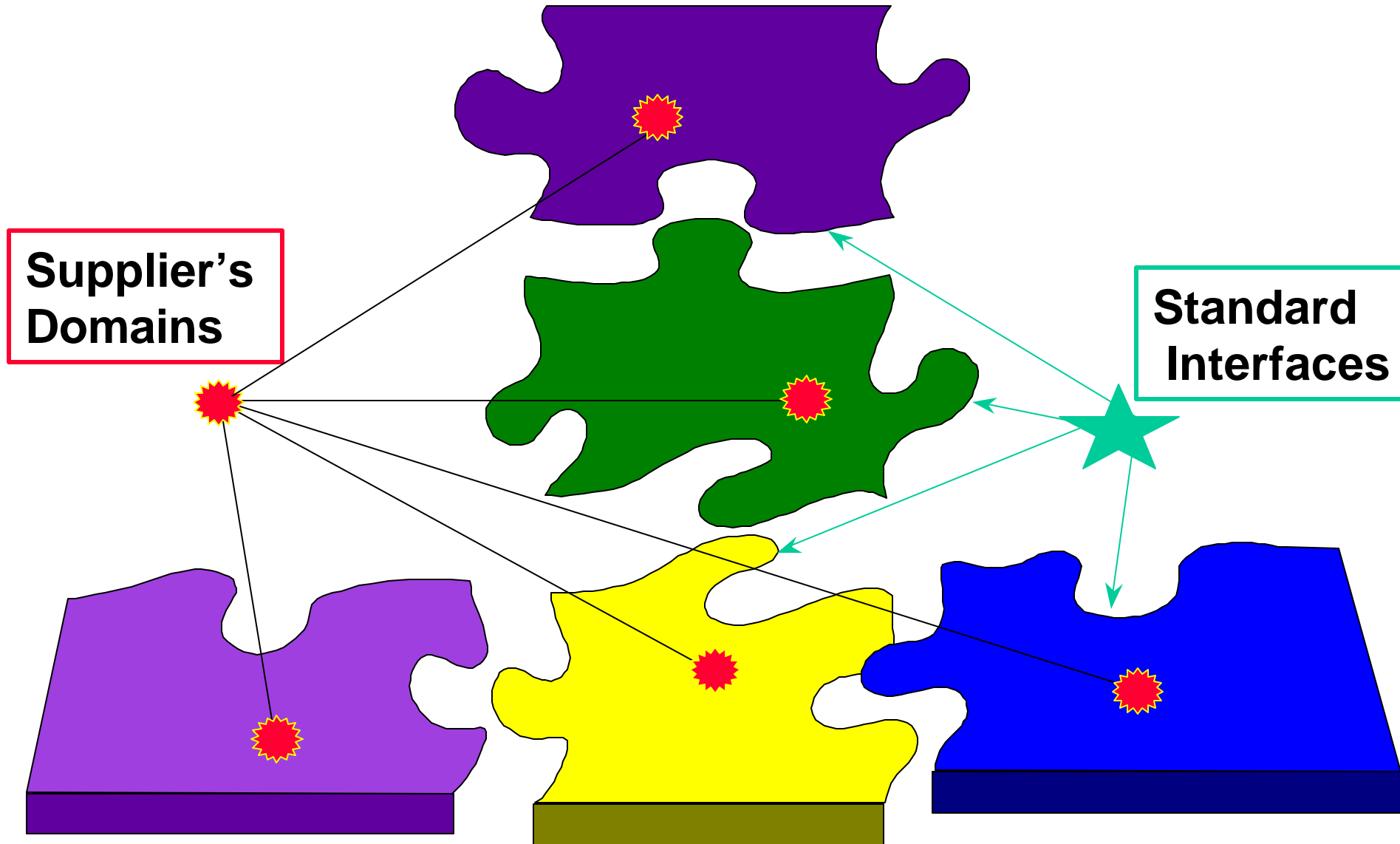
Case C
**Least
dense
packing**

New Paradigms to Factory Layouts

Layouts driven by business models and flexibility:

- **Traditional central spine layout or the PCS Forum's Windmill Fab?**
- **Do we combine Interbay and Intrabay system into one large system?**
- **Do we need bay walls or not?**

Factory Layout = business driven interconnection of standard interfaces

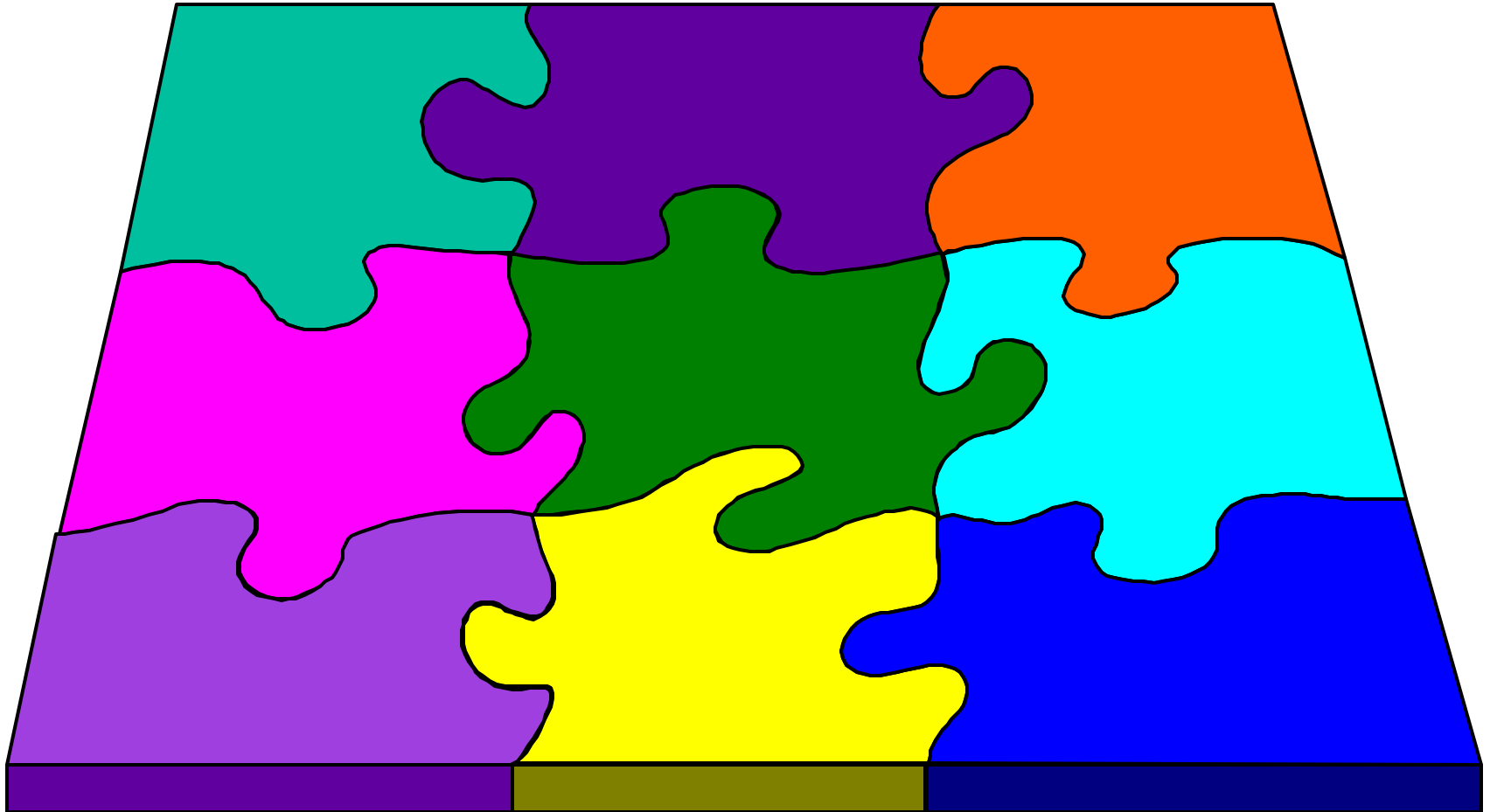


Factory Layout = business driven interconnection of standard interfaces

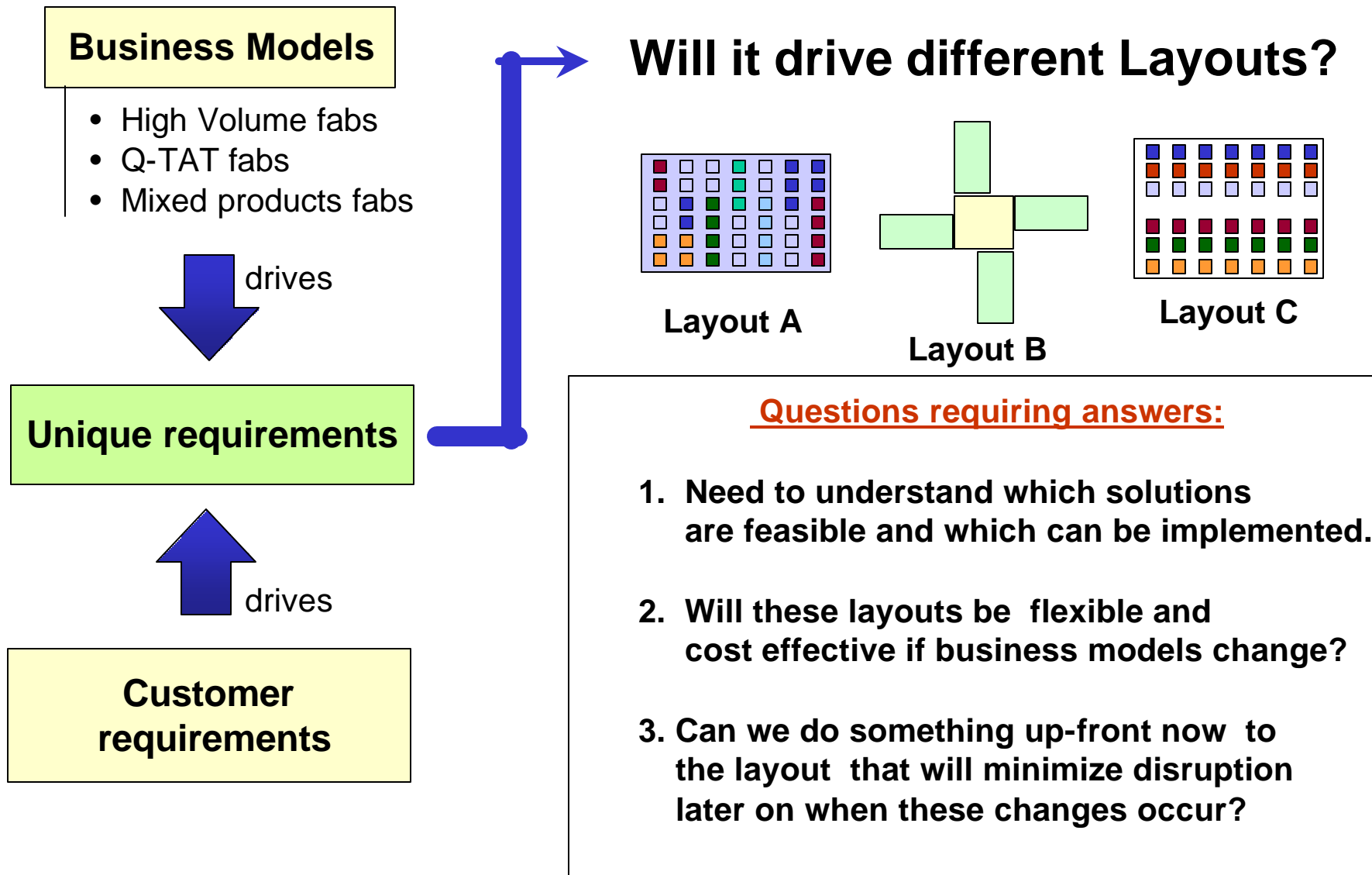


**Integration driven by semiconductor
manufacturer's business requirements**

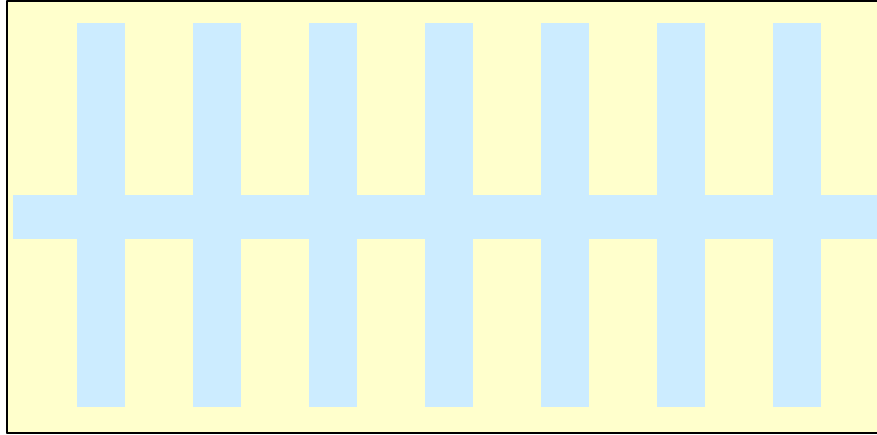
Factory Layout = business driven interconnection of standard interfaces



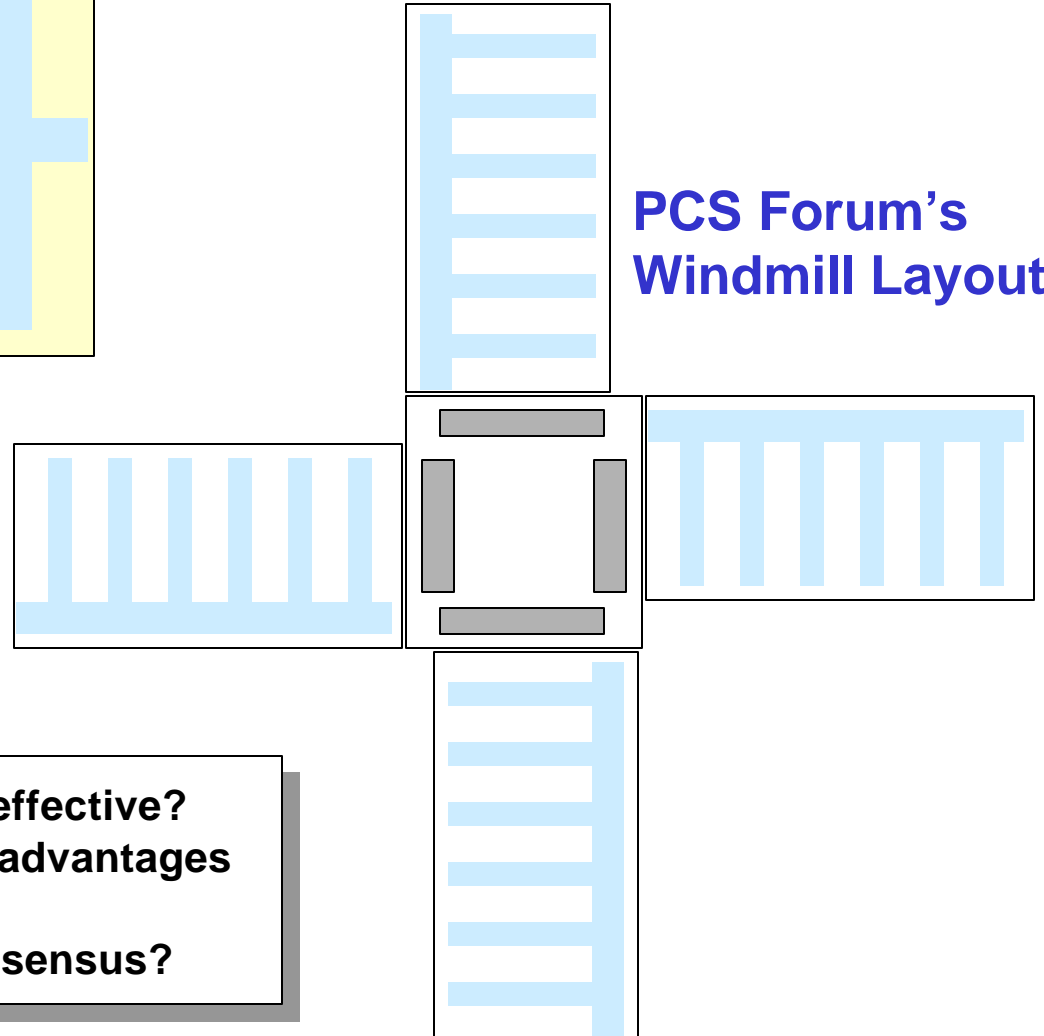
Layout Evaluation Planning



Traditional Spine Layout or Windmill Fab?



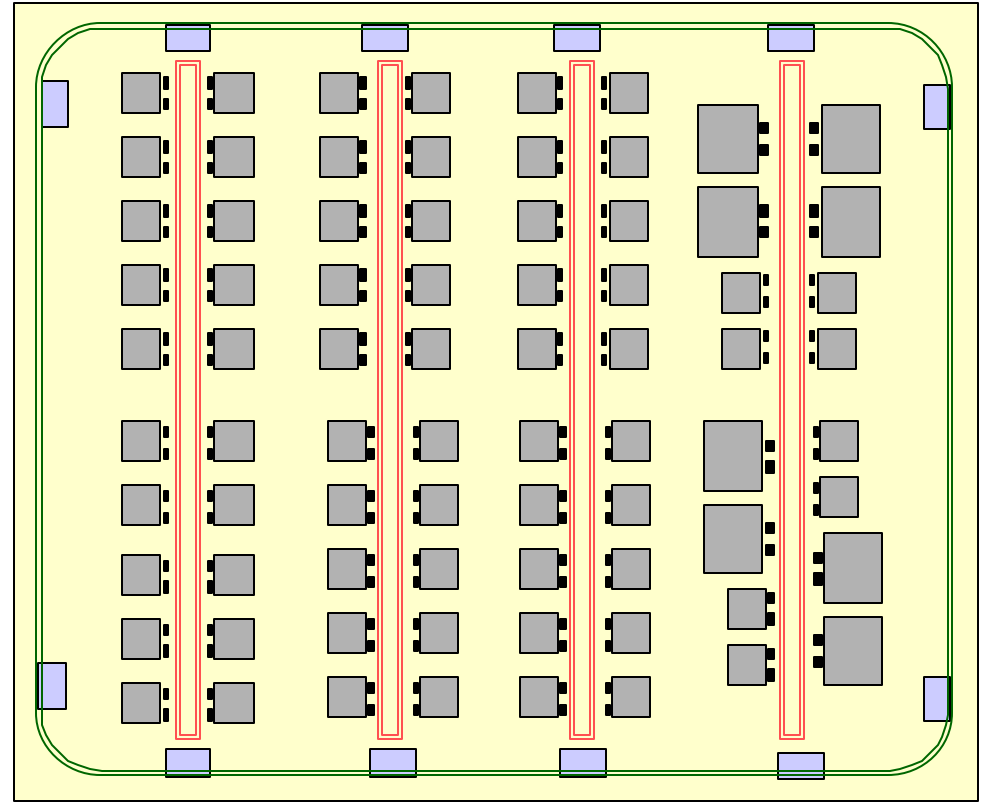
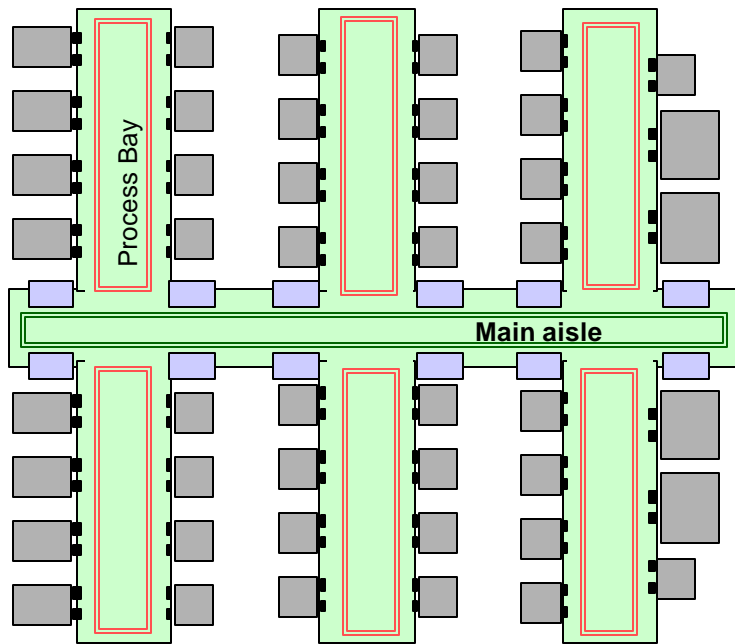
Spine Layout



PCS Forum's
Windmill Layout

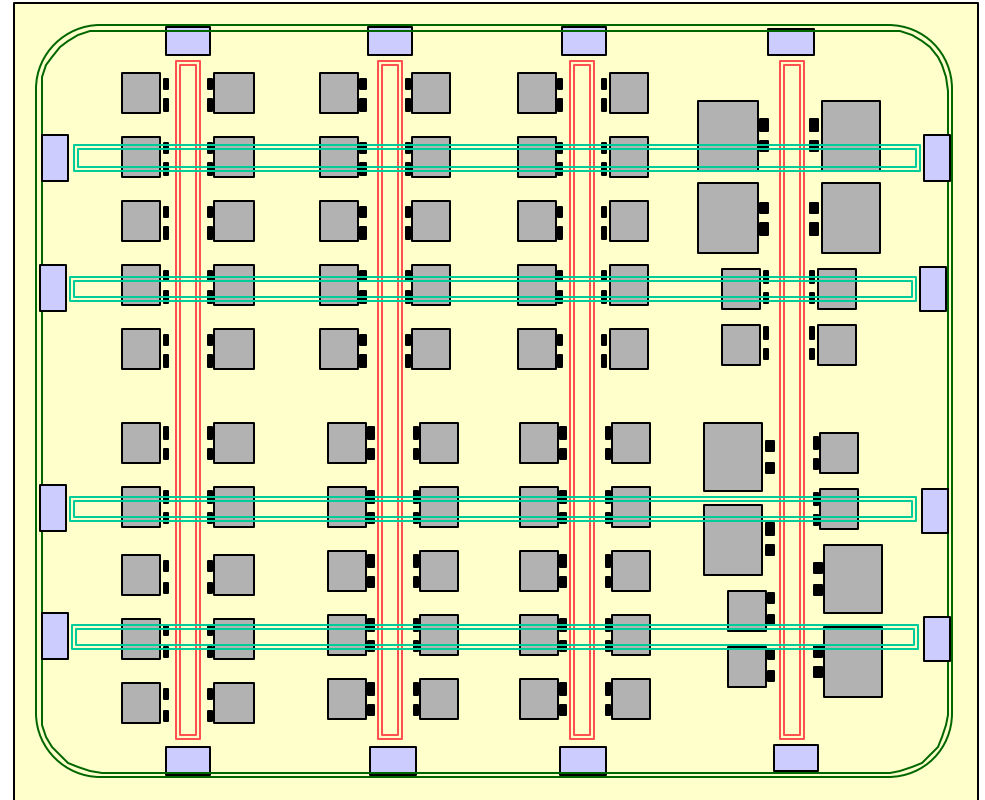
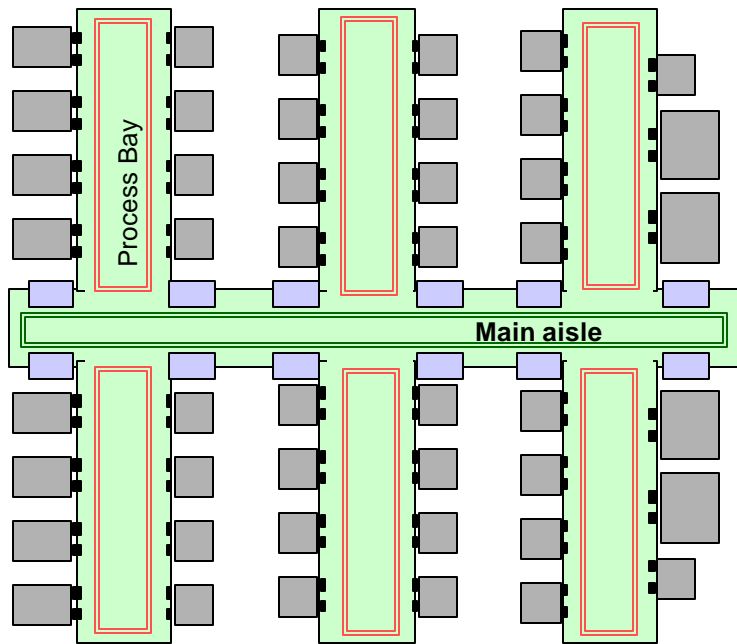
- Which is more efficient and cost effective?
- Does one layout have significant advantages or disadvantages?
- Will the Windmill Layout gain consensus?

Layout Options without Bay Walls



- Which is a more efficient Transportation system ?
- Will this lead to better equipment packing density?
- Will this result in improved human factors ?

Layout Options without Bay Walls

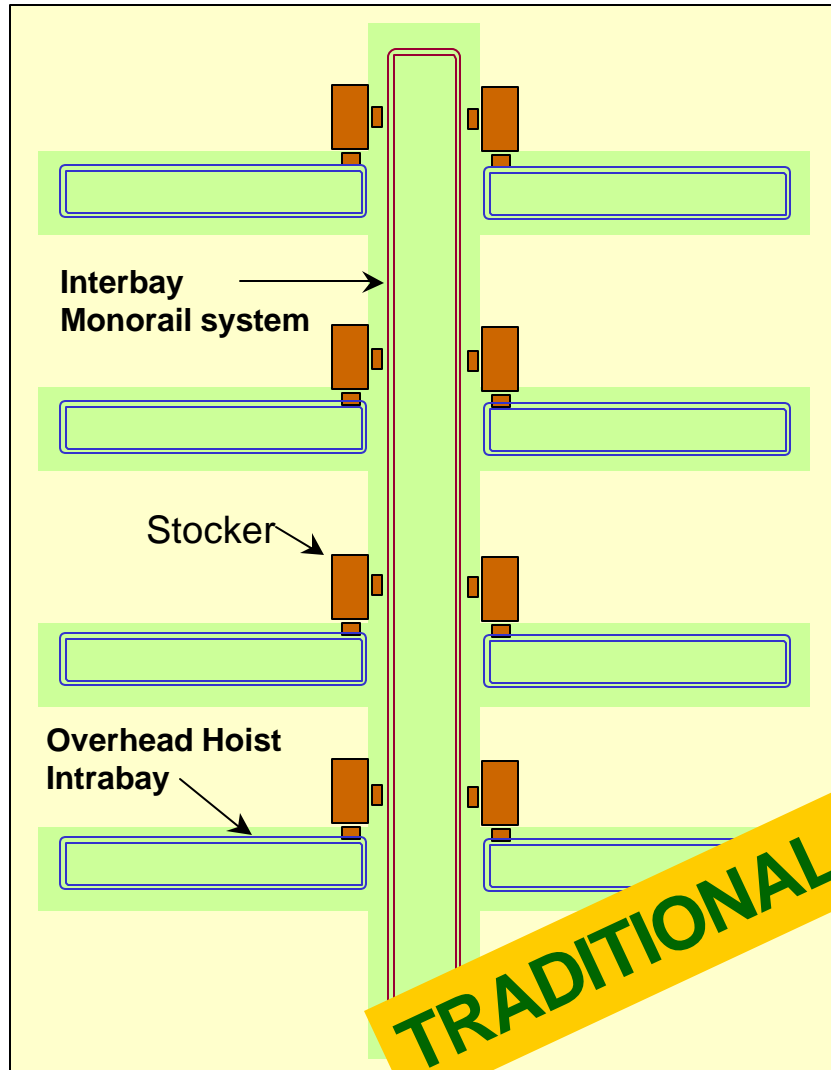


- Which is a more efficient Transportation system ?
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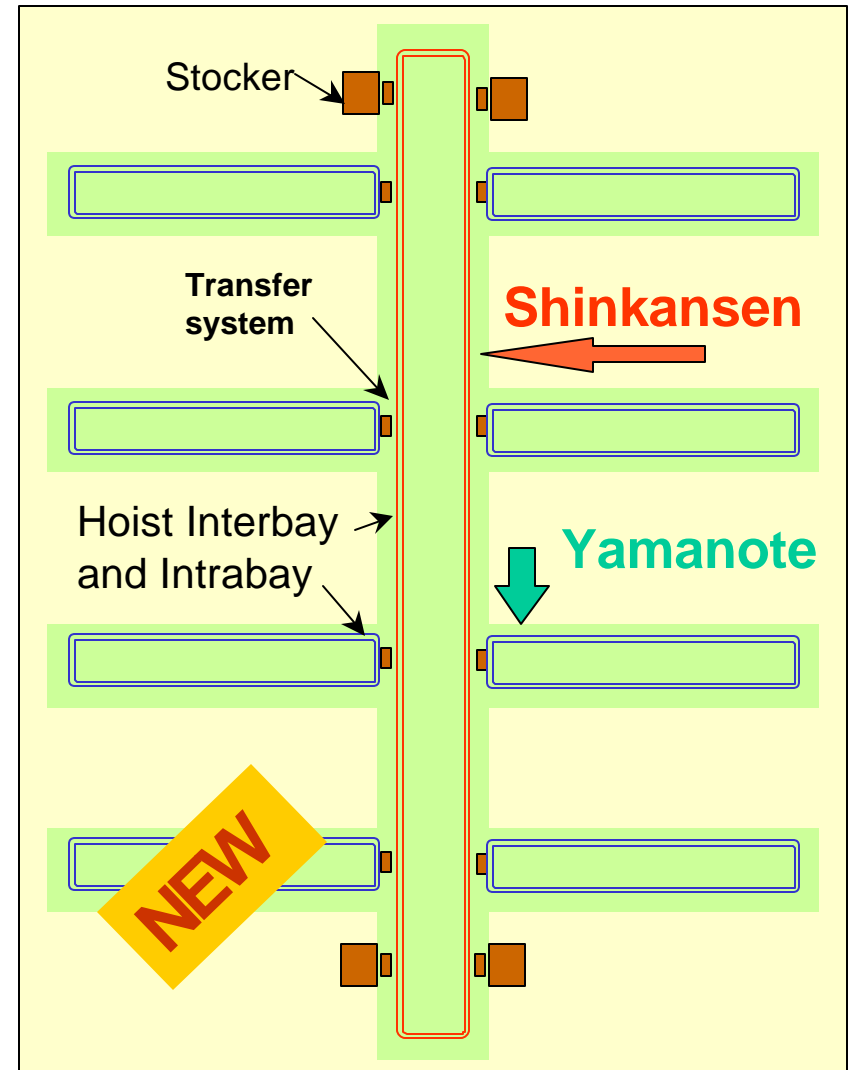
intel. Combining Interbay and Intrabay Transport Systems Makes a Very Efficient AMHS

STS97

Separate Interbay and Intrabay

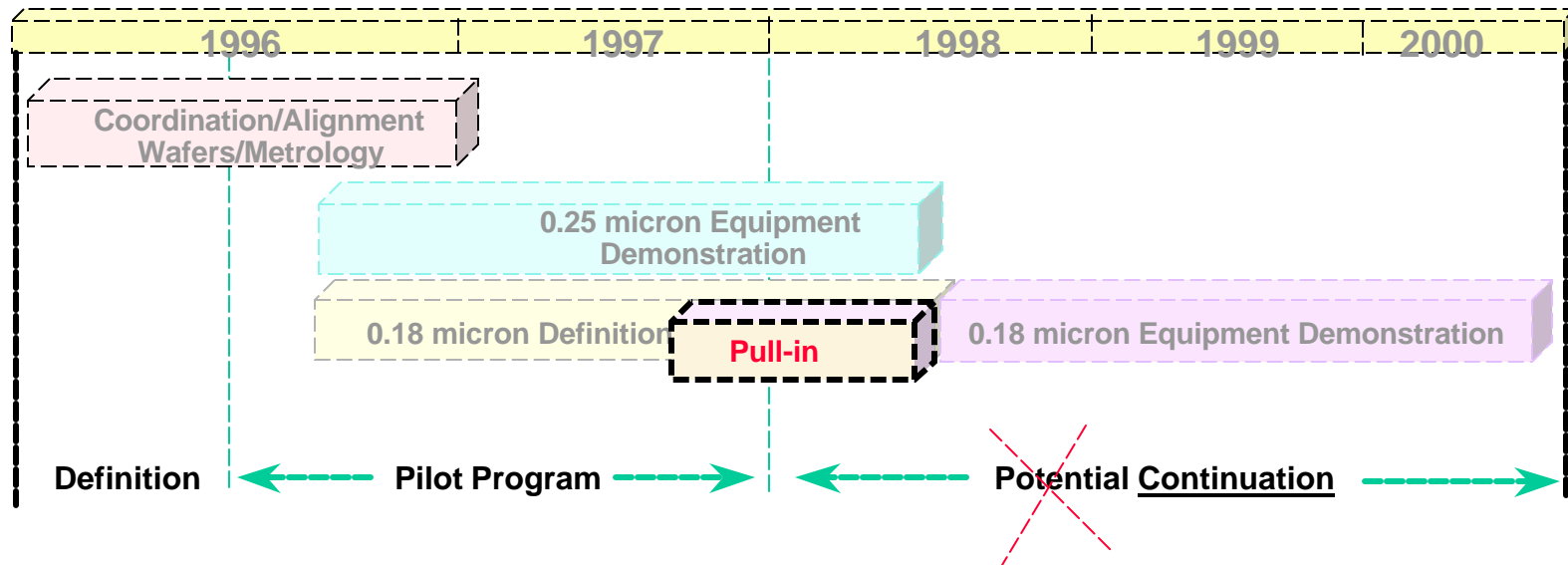


Combined Interbay and Intrabay



I300I Original Time Table

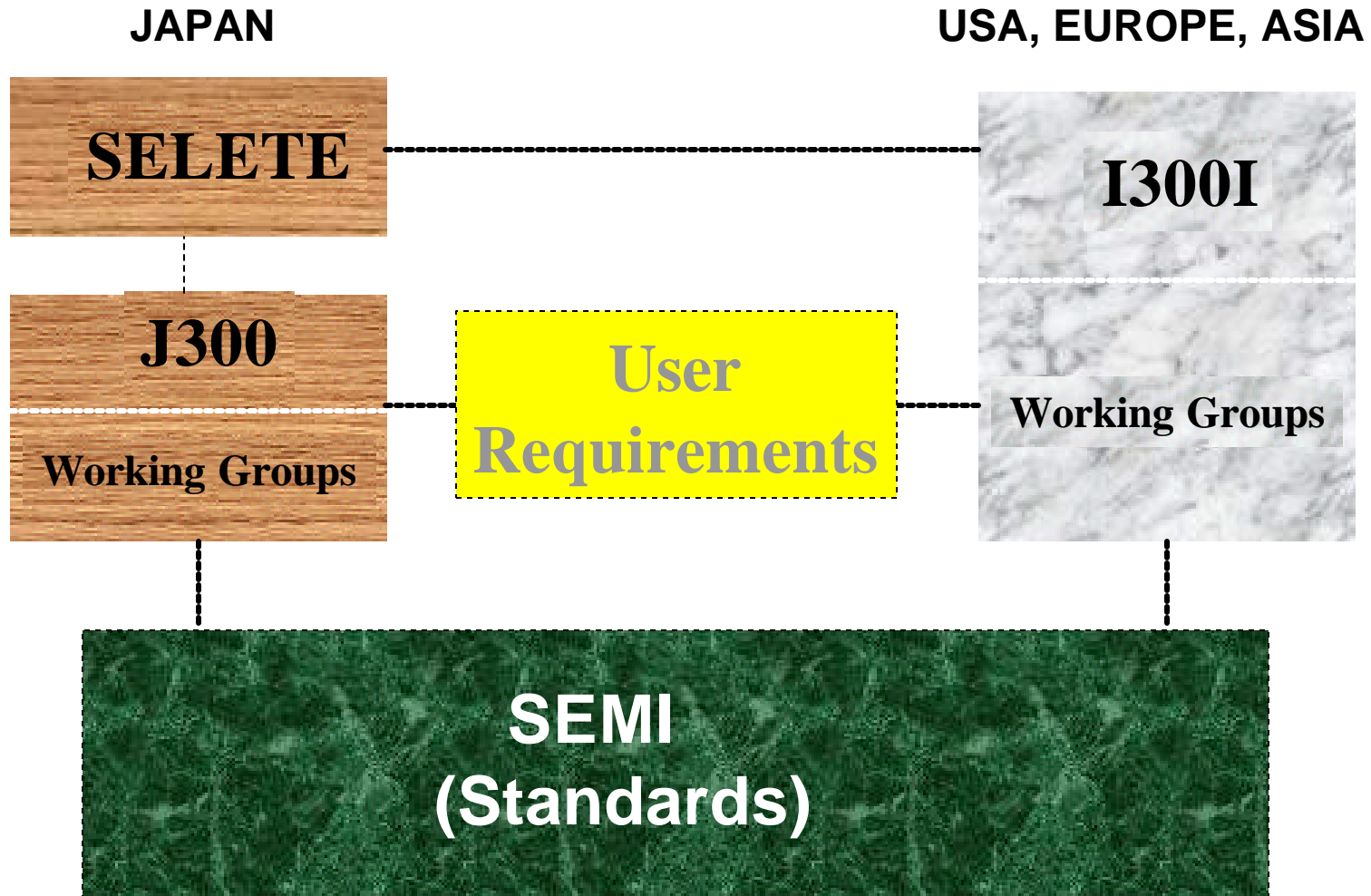
- International consortium, 18 month pilot program, with budget of \$26M



Proposed Focus Areas

- Standards: Defined “In Time”
- Device company consensus on schedule, demo methods and metrics
- Work with suppliers on standard test and processing capability
- Metrology capability and wafers for early development
- Validate standard demonstrations of targeted 0.25 micron equipment
- End 0.25 micron / begin 0.18 micron tool demonstrations in mid-1998

Cooperation of Consortia



Conclusions

- The **outlook** of the Semiconductor Industry towards the 21st century is still very **promising**
- The **300mm** wafer conversion offers an opportunity to correct some of the errors of the past
- **Standards** provide a mechanism for integrating the different elements of the factory **cost-effectively**
- Relative equipment **cost** and relative **footprint** goals have been set with respect to 200mm wafer equipment of equal wafer throughput
- Load port standardization, consistent with **PGV**, **AGV**, **RGV**, and **OHV** intrabay solutions for both **FOUP** and **Open Cassette**, has been established

Conclusions

- **OHT** offers the opportunity of **combining** intrabay and interbay material movement systems
- Adoption of minienvironment (**FOUP**) provides new options for factory layout
- **Taller stockers** are required to maximize storage efficiency of 300mm wafer carriers
- Equipment **buffering** requirements have been established. In most cases **two E15.1** load ports are sufficient
- **Metrology** equipment needs to be carefully included as part of the factory layout

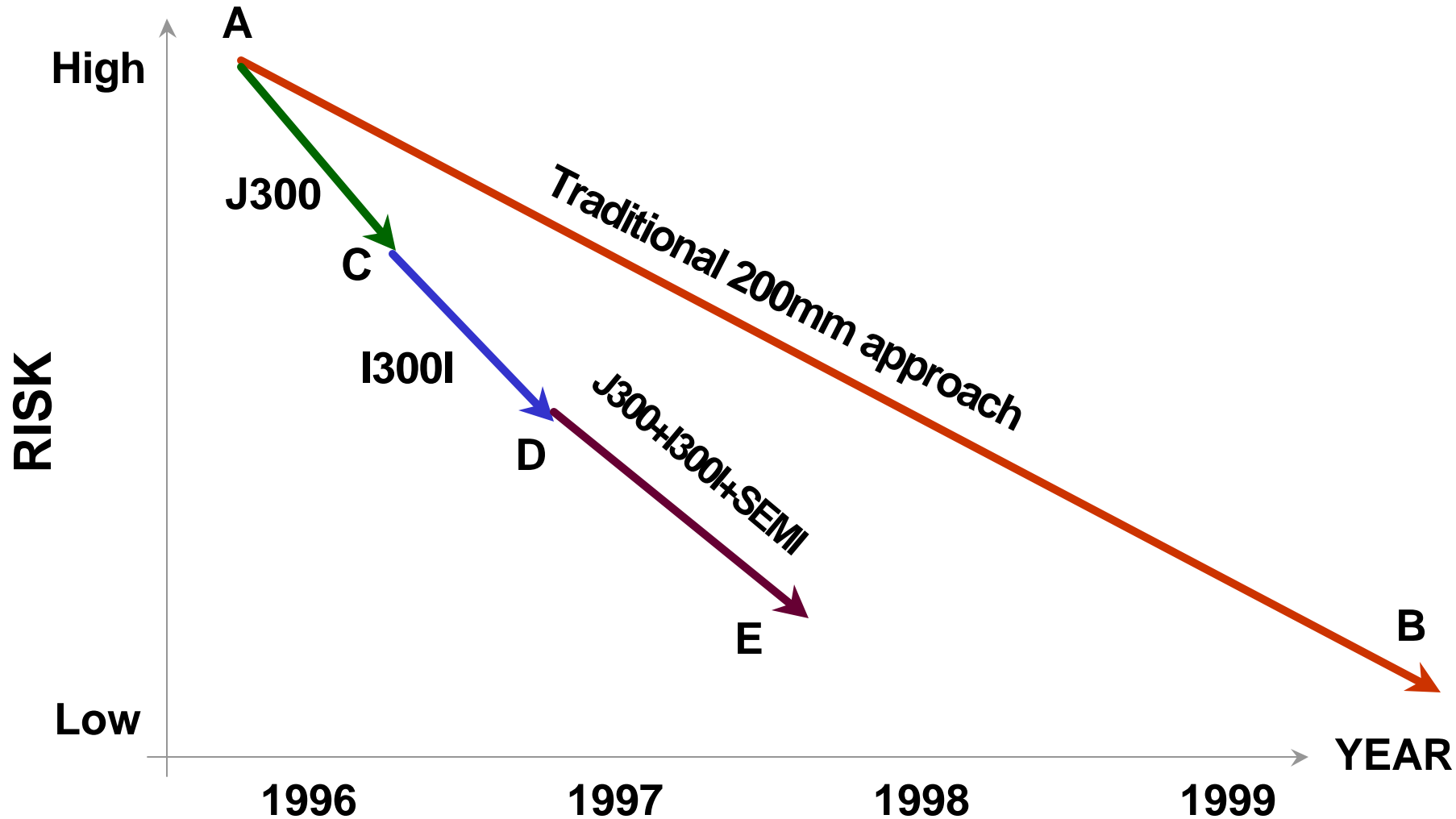
Conclusions

- Complete inter-operability of material handling and storage components dictates the adoption of a CIM architecture and new standards consistent with this requirement.
- **Global cooperation** is effective in accelerating program definition and execution
- Joint activities of **J300**, **I300I**, and **SEMI** are successfully continuing
- Joint activities of **Selete** and **I300I** are successfully continuing

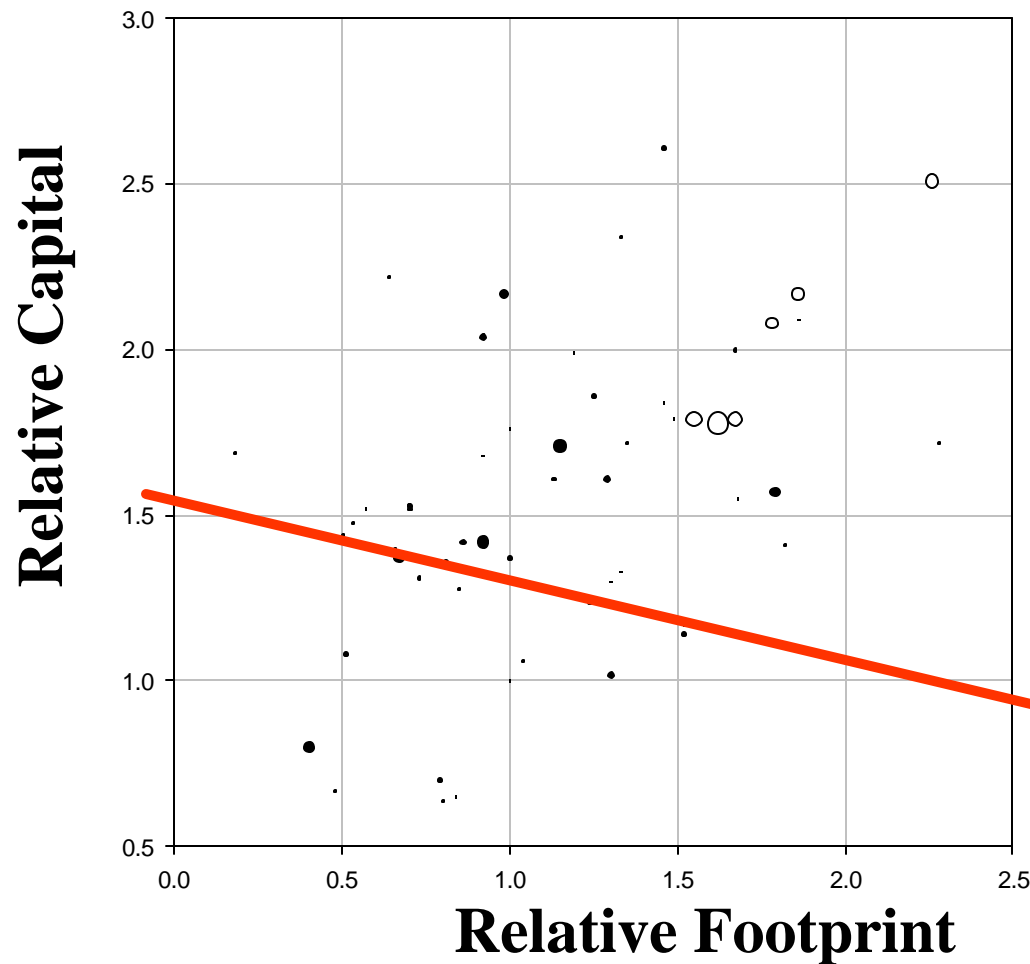
Conclusions

- Complete inter-operability of material handling and storage components dictates the adoption of a CIM architecture and new standards consistent with this requirement (I300I/J300 CIM Working Group).
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Risk Reduction using Global Participation and Standardization



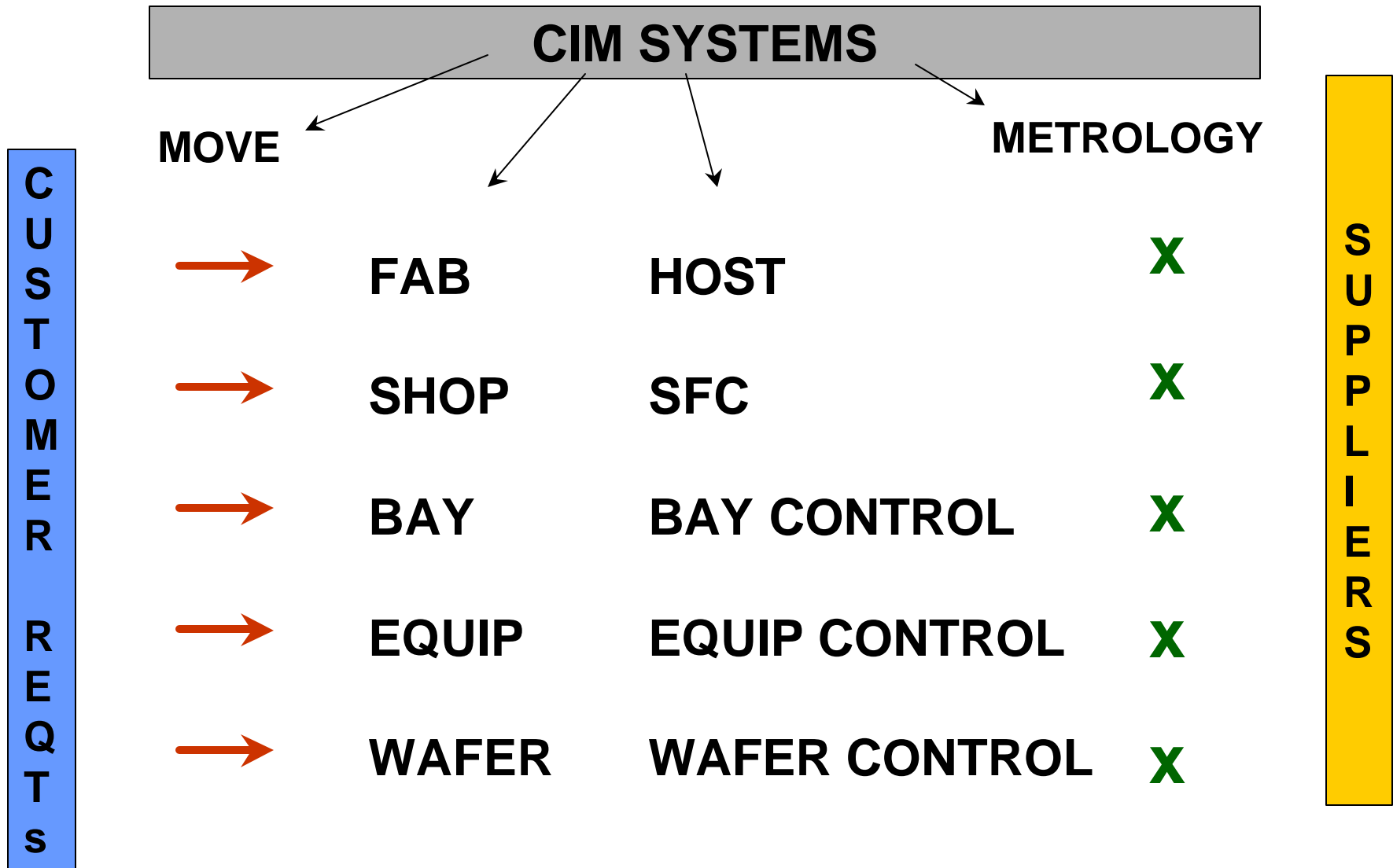
Early Productivity Scaling Factors for Tools in a 0.18 μ m Logic Process Flow



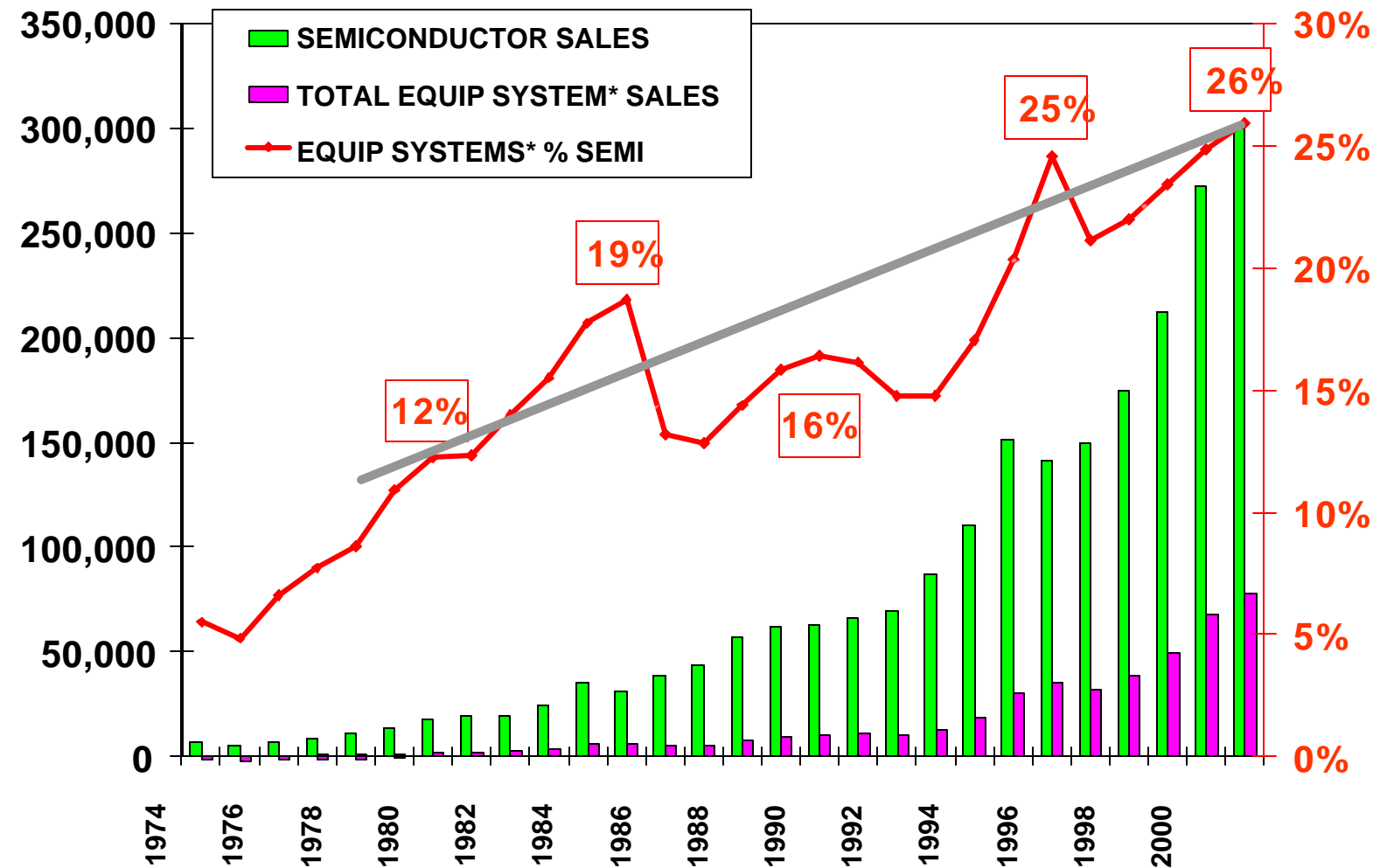
D.Seligson

P.Gargini&D.Pillai

Prioritized Testing FOR 1998



Total Equipment Cost



Source: VLSI Research Inc. July 1997

* Includes Spares and Service

Standards Strategy for Semiconductor Industry

